

BDV-E385/E390/T39

SERVICE MANUAL

Ver. 1.0 2012.03

US Model

BDV-E385/E390/T39

Canadian Model

BDV-E390

- BDV-E385/E390/T39 are composed of following models.
As service manuals are issued for each component model, please refer to them.

COMPONENT MODEL NAME

	BDV-E385	BDV-E390	BDV-T39
BLU-RAY DISC/ DVD receiver	HBD-E385	HBD-E390	HBD-T39
Front speaker	SS-TSB118	SS-TSB118	SS-TSB118
Center speaker	SS-CTB113	SS-CTB113	SS-CTB113
Surround speaker	SS-TSB118	SS-TSB118	SS-TSB118
Subwoofer	SS-WSB114	SS-WSB114	SS-WSB114

SPECIFICATIONS

Power requirements	120 V AC, 60 Hz
Power consumption	
BDV-E385/BDV-E390/ BDV-T39	On: 130 W Standby: 0.3 W (at the Power Saving mode)
Dimensions (approx.)	430 mm × 50 mm × 275 mm (17 in × 2 in × 10 7/8 in) (w/h/d) incl. projecting parts
Mass (approx.)	2.8 kg (6 lb 2 3/4 oz)

Design and specifications are subject to change without notice.

- Standby power consumption 0.3W.
- Over 85% power efficiency of amplifier block is achieved with the full digital amplifier, S-Master.

- iPhone, iPod, iPod classic, iPod nano, and iPod touch are trademarks of Apple Inc., registered in the U.S. and other countries.

Unpacking

- Front speakers (2)
- Surround speakers (2)
- Center speakers (1)
- Subwoofer (1)
- FM wire antenna (aerial) (1)
- Remote commander (remote) (1)
- R6 (size AA) batteries (2)
- Dock for iPod/iPhone (TDM-iP30) (1) (BDV-E390/BDV-T39)
- USB cable (1) (BDV-E390/BDV-T39)
- Video cord (1)
- High Speed HDMI cable (1) (BDV-T39)
- Operating Instructions
- Quick Setup Guide
- Speaker Installation Guide

– Refer to next page for accessories list –

BLU-RAY DISC/DVD HOME THEATRE SYSTEM

9-890-583-01

2012C80-1

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Sony Corporation

Published by Sony EMCS (Malaysia) PG Tec

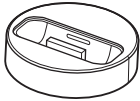
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ACCESSORIES

Ref. No.	Part No.	Description	Remark
1	4-418-859-12	MANUAL, INSTRUCTION (ENGLISH (US))	
	4-418-859-22	MANUAL, INSTRUCTION (FRENCH) (CND)	
	4-418-859-32	MANUAL, INSTRUCTION (SPANISH) (E385)	
	A-1797-336-A	OVERALL ASSY (Dock for iPod/iPhone: TDM-iP30) (E390/T39)	
2	1-489-949-11	REMOTE COMMANDER (including BATTERY LID) (RM-ADP072)	
3	1-793-184-41	CONNECTOR (F TYPE ADAPTOR) (FM wire antenna (aerial))	
4	1-756-988-21	BATTERY, MANGANESE (R6)	
5	1-835-855-31	CORD WITH CONNECTOR (1.5m) (HDMI CAB (High Speed HDMI cable) (T39)	
6	1-838-669-11	CORD, CONNECTION (1.2m) (Video cord)	
7	1-839-590-11	CORD, CONNECTION (USB5P) (0.5m) (USB cable) (E390/T39)	

- Abbreviation
CND : Canadian model

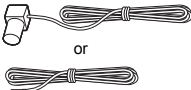
1 Dock for iPod/iPhone (TDM-iP30) (1) (E390/T39)



2 Remote commander (remote) (1)



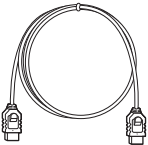
3 FM wire antenna (aerial) (1)



4 R6 (size AA) batteries (2)



5 High Speed HDMI cable (1) (T39)



6 Video cord (1)



7 USB cable (1) (E390/T39)



MEMO

HBD-E385/E390/T39

SERVICE MANUAL

Ver. 1.0 2012.03

US Model
HBD-E385/E390/T39
Canadian Model
HBD-E390



Photo: HBD-E390

- HBD-E385 is the amplifier, video, HDMI, BD/DVD/CD system, USB, LAN, wireless LAN and FM tuner section in BDV-E385.
- HBD-E390 is the amplifier, video, HDMI, BD/DVD/CD system, USB, LAN, wireless LAN and FM tuner section in BDV-E390.
- HBD-T39 is the amplifier, video, HDMI, BD/DVD/CD system, USB, LAN, wireless LAN and FM tuner section in BDV-T39.

Model Name Using Similar Mechanism	BDP-S390
Mechanism Type	BPX-7
Optical Pick-up Block Name	KEM480AAA

SPECIFICATIONS

Amplifier Section

BDV-E385/BDV-E390/BDV-T39

US models:

AUDIO POWER SPECIFICATIONS

POWER OUTPUT AND TOTAL HARMONIC

DISTORTION:

(FTC)

Front L + Front R: With 3 ohms loads, both channels driven, from 180 Hz - 20 kHz; rated 50 watts per channel minimum RMS power, with no more than 1% total harmonic distortion from 250 milli watts to rated output.

Other models:

POWER OUTPUT (rated)

Front L/Front R: 75 W + 75 W (at 3 ohms, 1 kHz, 1% THD)

POWER OUTPUT (reference)

Front L/Front R/Surround L/Surround R: 125 W (per channel at 3 ohms, 1 kHz)

Center: 250 W (at 6 ohms, 1 kHz)

Subwoofer: 250 W (at 6 ohms, 80 Hz)

Inputs (Analog)

AUDIO (AUDIO IN) Sensitivity: 1 V/400 mV

Inputs (Digital)

TV (Audio Return Channel/OPTICAL)

Supported formats: LPCM
2CH (up to 48 kHz), Dolby
Digital, DTS

Video Section

Outputs VIDEO: 1 Vp-p 75 ohms

HDMI Section

Connector Type A (19pin)

BD/DVD/Super Audio CD/CD System

Signal format system NTSC

USB Section

➤ (USB) port: Type A (For connecting USB memory, memory card reader, digital still camera, and digital video camera)

LAN Section

LAN (100) terminal 100BASE-TX Terminal

Wireless LAN Section

Blu-ray Disc/DVD receiver (HBD-E390/HBD-E385/HBD-T39)

Standards Compliance IEEE 802.11 b/g/n
Frequency and Channel 2.4 GHz - 2.4835 GHz
[CH1 -11]

FM Tuner Section

System PLL quartz-locked digital synthesizer
Tuning range 87.5 MHz - 108.0 MHz
(100 kHz step)

Antenna (aerial) FM wire antenna (aerial)

Antenna (aerial) terminals 75 ohms, unbalanced

General

Power requirements 120 V AC, 60 Hz

Power consumption
BDV-E385/BDV-E390/
BDV-T39

On: 130 W
Standby: 0.3 W (at the
Power Saving mode)
Dimensions (approx.) 430 mm × 50 mm × 275
mm (17 in × 2 in ×
10 7/8 in) (w/h/d) incl.
projecting parts
Mass (approx.) 2.8 kg (6 lb 2 3/4 oz)

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- Standby power consumption 0.3W.
- Over 85% power efficiency of amplifier block is achieved with the full digital amplifier, S-Master.

BLU-RAY DISC/DVD RECEIVER

9-890-584-01

2012C80-1

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CAUTION

Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.

ATTENTION AU COMPOSANT AYANT RAPPORT À LA SÉCURITÉ!

LES COMPOSANTS IDENTIFIÉS PAR UNE MARQUE  SUR LES DIAGRAMMES SCHÉMATIQUES ET LA LISTE DES PIÈCES SONT CRITIQUES POUR LA SÉCURITÉ DE FONCTIONNEMENT. NE REMPLACER CES COMPOSANTS QUE PAR DES PIÈCES SONY DONT LES NUMÉROS SONT DONNÉS DANS CE MANUEL OU DANS LES SUPPLÉMENTS PUBLIÉS PAR SONY.

SAFETY CHECK-OUT

After correcting the original service problem, perform the following safety check before releasing the set to the customer:

Check the antenna terminals, metal trim, “metallized” knobs, screws, and all other exposed metal parts for AC leakage.

Check leakage as described below.

LEAKAGE TEST

The AC leakage from any exposed metal part to earth ground and from all exposed metal parts to any exposed metal part having a return to chassis, must not exceed 0.5 mA (500 microamperes.). Leakage current can be measured by any one of three methods.

1. A commercial leakage tester, such as the Simpson 229 or RCA WT-540A. Follow the manufacturers' instructions to use these instruments.
2. A battery-operated AC milliammeter. The Data Precision 245 digital multimeter is suitable for this job.
3. Measuring the voltage drop across a resistor by means of a VOM or battery-operated AC voltmeter. The “limit” indication is 0.75 V, so analog meters must have an accurate low-voltage scale. The Simpson 250 and Sanwa SH-63Trd are examples of a passive VOM that is suitable. Nearly all battery operated digital multimeters that have a 2 V AC range are suitable. (See Fig. A)

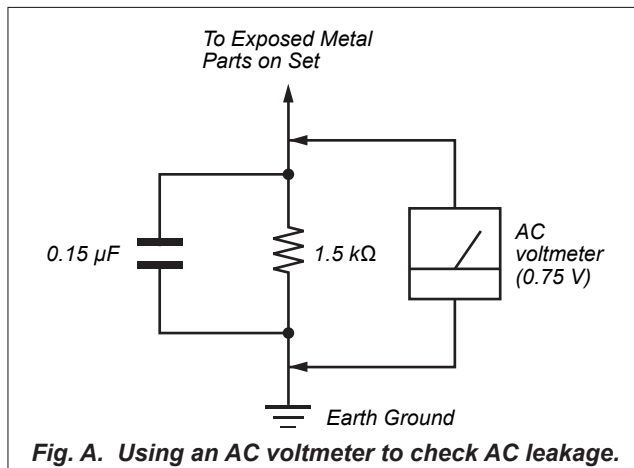


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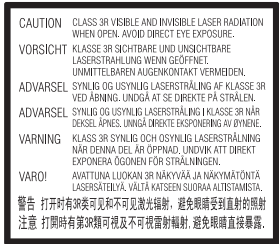
6-2. Front Panel Section

6-3. Chassis Bottom Section

6-4. Chassis Section

6-5. BD Drive Section (BPX-7)

7. ELECTRICAL PARTS LIST



This appliance is classified as a CLASS 1 LASER product. This marking is located on the rear exterior.

This appliance is classified as a CLASS 3R LASER product. Visible and invisible laser radiation is emitted when the laser protective housing is opened, so be sure to avoid direct eye exposure. This marking is located on the laser protective housing inside the enclosure.

NOTES ON CHIP COMPONENT REPLACEMENT

- Never reuse a disconnected chip component.
- Notice that the minus side of a tantalum capacitor may be damaged by heat.

FLEXIBLE CIRCUIT BOARD REPAIRING

- Keep the temperature of soldering iron around 270 °C during repairing.
- Do not touch the soldering iron on the same conductor of the circuit board (within 3 times).
- Be careful not to apply force on the conductor when soldering or unsoldering.

SECTION 1

SERVICING NOTES

NOTES ON HANDLING THE OPTICAL PICK-UP BLOCK OR BASE UNIT

The laser diode in the optical pick-up block may suffer electrostatic break-down because of the potential difference generated by the charged electrostatic load, etc. on clothing and the human body. During repair, pay attention to electrostatic break-down and also use the procedure in the printed matter which is included in the repair parts.

The flexible board is easily damaged and should be handled with care.

NOTES ON LASER DIODE EMISSION CHECK

The laser beam on this model is concentrated so as to be focused on the disc reflective surface by the objective lens in the optical pickup block. Therefore, when checking the laser diode emission, observe from more than 30 cm away from the objective lens.

UNLEADED SOLDER

Boards requiring use of unleaded solder are printed with the lead-free mark (LF) indicating the solder contains no lead.

(Caution: Some printed circuit boards may not come printed with the lead free mark due to their particular size)



: LEAD FREE MARK

Unleaded solder has the following characteristics.

- Unleaded solder melts at a temperature about 40 °C higher than ordinary solder.
Ordinary soldering irons can be used but the iron tip has to be applied to the solder joint for a slightly longer time.
Soldering irons using a temperature regulator should be set to about 350 °C.
Caution: The printed pattern (copper foil) may peel away if the heated tip is applied for too long, so be careful!
- Strong viscosity
Unleaded solder is more viscous (sticky, less prone to flow) than ordinary solder so use caution not to let solder bridges occur such as on IC pins, etc.
- Usable with ordinary solder
It is best to use only unleaded solder but unleaded solder may also be added to ordinary solder.

RELEASING THE DISC TRAY LOCK

The disc tray lock function for the antitheft of an demonstration disc in the store is equipped.

Releasing Procedure:

1. Press the [I/⏻] button to turn on the system.
2. Touch the [FUNCTION] sensor to select "BD/DVD".
3. Touch the [■] and [▲] sensors simultaneously and hold down unit "DEMO OFF" displayed on the fluorescent indicator tube (around 5 seconds).

Note: When "DEMO LOCK" is displayed, the disc tray lock is not released by turning power on/off with the [I/⏻] button.

ABOUT THE LENS CLEANING

Do not do the lens cleaning with the cotton bud etc. It causes the trouble.

NOTE OF REPLACING THE IC101, IC501 AND IC8001 ON THE MB148 BOARD

IC101, IC501 and IC8001 on the MB148 board cannot exchange with single. When these parts are damaged, exchange the complete mounted board.

NOTE OF REPLACING THE IC106 AND IC206 ON THE MB148 BOARD

Replacement of IC106 and IC206 on the MB148 board used in this unit requires a special tool.

TEST DISC

Part No.	Description	Layer
J-6090-199-A	BLX-104	Single Layer
J-6090-200-A	BLX-204	Dual Layer
J-2501-307-A	CD (HLX-A1)	
J-2501-305-A	HLX-513	Single Layer (NTSC)
J-2501-306-A	HLX-514	Dual Layer (NTSC)
J-6090-077-A	HLX-506	Single Layer (PAL)
J-6090-078-A	HLX-507	Dual Layer (PAL)

Note: Refer to the service manual of BDP-BX1/S350 (Part No. 9-883-989-1□) (page 1-3 to 1-14E) for the use of BLX-104/204.

Operation and Display:

1. BLX-104

Procedure:

1. Select 23.976Hz/1080p.
2. Play "4.Motion picture".
3. Check whether player can play back or not.
4. Check each outputs.

Video:

Composite/S Video/component/HDMI.

Audio:

Speaker out.

- * When 1080/24p monitor is nothing, 1080i (59.94Hz or 50Hz) can use instead of 1080/24p.
However this is temporary correspondence.

2. BLX-204

Procedure:

1. Select 1080i (59.94Hz or 50Hz).
2. Play "4.Motion picture".
3. Check whether player can play back or not (Check the picture and sound output).

3. CD (HLX-A1)

Procedure:

Check whether player can play back or not (Check the sound output).

4. HLX-513/514 (NTSC), HLX-506/507 (PAL)

Procedure:

1. After displayed Main Menu, select "1.Video Signal".
2. Play "1.Color bar 100%" (Check the picture and sound output).
3. Return to Menu.
4. Play "Demonstration 4:3" or "Demonstration 16:9" (Check the picture and sound output).

NOTE OF REPLACING THE OPTICAL DEVICE (KEM480AAA) OR MB148 BOARD

The password will be supplied to only service HQ, and service center name, q'ty and all of software registered information should be maintained by service HQ, and Audio will ask to report the registration information.

Optical device (KEM480AAA) for BD requires precise read out functions and secure contents protection system for more than past DVD/CD.

Therefore, in the case repaired as follows, the writing work of the OP data is necessary.

- When the optical device (KEM480AAA) is replaced (The MB148 board doesn't replace).
- When both the optical device (KEM480AAA) and MB148 board are replaced.
- When the MB148 board is replaced (The optical device (KEM480AAA) doesn't replace) (In this case, do the work of "3. Optical device (KEM480AAA) replacement" other than the replacement of new optical device).

Note: The servo adjustment is done while writing the OP data. The manual adjustment is unnecessary.
LD ON TIME history doesn't carry over.
Do not touch any optical block parts, turn table and during replacing. BD laser diode is very sensitive.

1. Preparation

1-1. ESD Countermeasure

It is necessary to confirm the state of static electricity in the work space before the repair is started.

The static electricity resistance of the BD laser is weaker than that of the DVD/CD laser.

Do work space and worker's ESD countermeasures to prevent destruction by ESD.

1-2. Jig

- Digital camera (Recommend with macro mode)
- USB memory
- PC
- Barcode decoder (Refer to "1-3. Barcode decoder (BDPRdec)")

1-3. Barcode decoder (BDPRdec)

Jig name : BDPRdec.exe

Release : 2011.8.25

Version : 3.0.0.0

Software contents :

- BDPRdec.exe : Barcode decoder software
- SavePath.ini : Decoded file destination setting file (Initial destination is "C:\BD\BuData.txt")
- TasmanBars.dll : Decode dll
- Uninst.exe : Uninstall the "BDPRdec.exe" from PC

Install procedure:

1. Unzip the barcode decoder files to any PC folder.
2. Check the attached 2D code photo (OK_sample.JPG) drag & drop onto "BDPRdec.exe".

When the barcode decoder is used for the first time, the password is necessary. It is unnecessary since the second times.

Note 1: The password will be supplied to only service headquarters, and service center name/q'ty/all of software registered information should be maintained by service headquarters.

Note 2: Do not change the decoded file name "BuData.txt".

3. When ".NET frame work requirements" is displayed, download following applications from Microsoft download site.

- Microsoft .NET Framework Version 2.0 Redistributable Package (x86)
<http://www.microsoft.com/downloads/details.aspx?displaylang=en&FamilyID=0856eacb-4362-4b0d-8edd-aab15c5e04f5>
- Microsoft .NET Framework 2.0 Service Pack 1 (x86)
<http://www.microsoft.com/downloads/details.aspx?displaylang=en&FamilyID=79bc3b77-e02c-4ad3-aacf-a7633f706ba5>

How to use:

Case 1 Drag & drop 2D code photo onto "BDPRdec.exe".

Case 2 Drag & drop BU data file onto "BDPRdec.exe".

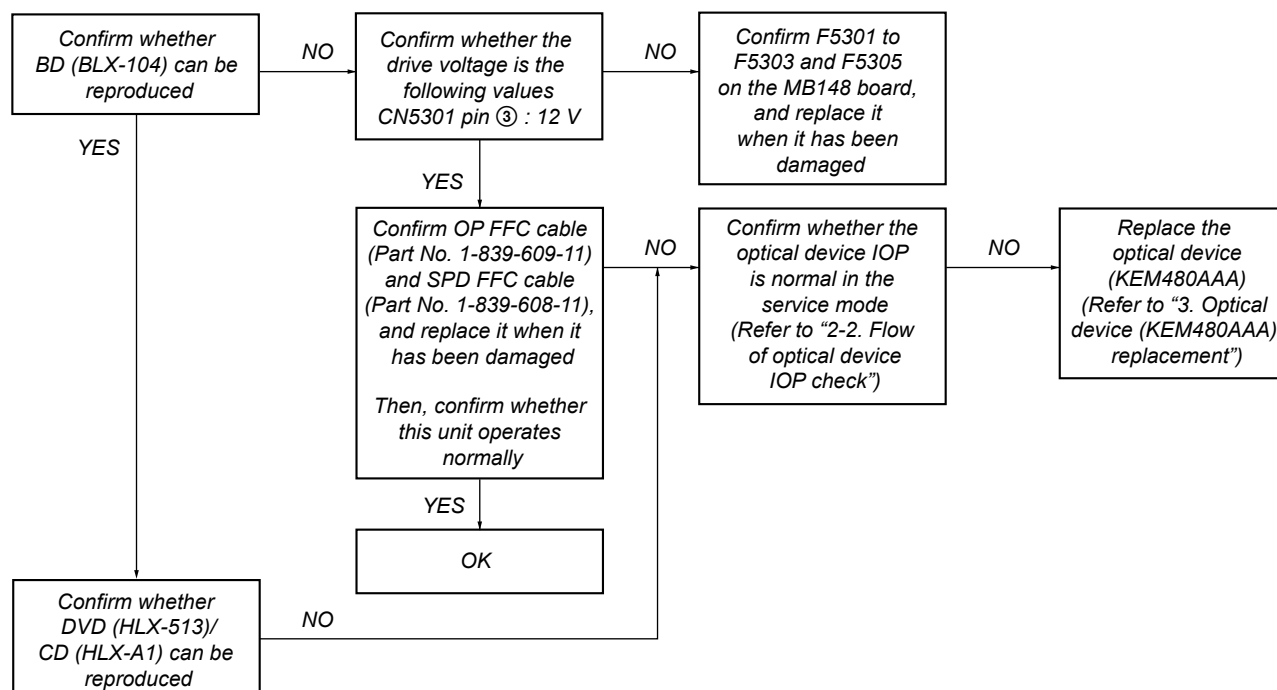
Data file name be changed to specify format and end of 7 character are defined.

You can also enter the file path at the prompt.

2. Pass-fail judgment of the optical device (KEM480AAA)

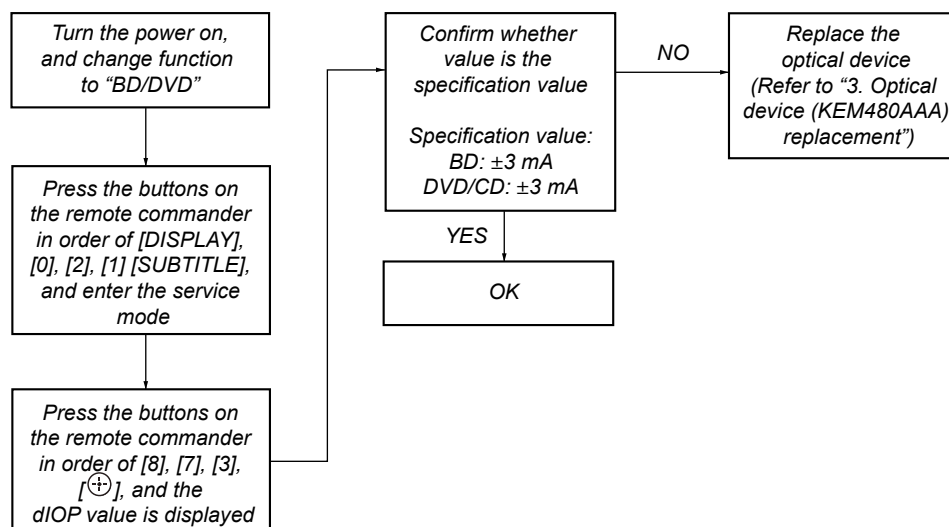
Perform pass-fail judgment to judge whether the repair of the optical device (KEM480AAA) is necessary.

2-1. Flow of drive section check



Note: Refer to "2-11. OPTICAL PICK-UP BLOCK (KEM480AAA), WIRE (FLAT TYPE)" (page 19) about how to remove the COVER, DRIVE.

2-2. Flow of optical device IOP check



3. Optical device (KEM480AAA) replacement

Flow of replacement:

Note: The photo in flow is an image.



Barcode label on new optical device (KEM480AAA) bottom side



Take photo (JPEG) by digital camera



Change photo into the text data with the barcode decoder



Save the text data to USB memory (memory capacity need not be 8GB)



Connect USB memory with front USB connector on this unit, and read the text data by the service mode

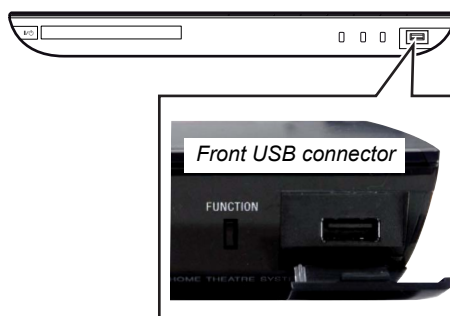
Procedure:

1. Remove the INSULATOR (4 pieces) and broken optical device (KEM480AAA) from LOADING ASSY.
2. Take photo of the barcode on new optical device (KEM480AAA) bottom side by digital camera.



3. Assemble the INSULATOR (4 pieces) to new optical device (KEM480AAA), fix (Torque value: 2 kgf) it to LOADING ASSY with screw, and assemble this unit.
4. Drag & drop the taken photo by step 2 to "BDPRdec.exe", and make the text data (File name: BuData.txt).
5. Save the text data to USB memory.
6. Connect USB memory with front USB connector on this unit, and turn the power on.

– Front view –



7. Press the [FUNCTION] button on the remote commander to select "BD/DVD".
8. Press the buttons on the remote commander in order of [DISPLAY], [0], [2], [1], [SUBTITLE], and enter the service mode.
9. Press the buttons on the remote commander in order of [8], [1], [ENTER], and execute "[1] Drive OP data Write".
10. Turn the power off after writing the OP data.
11. Turn the power on, and enter the service mode again.
12. Press the buttons on the remote commander in order of [8], [7], [3], [ENTER], and the dIOP value is displayed.
13. Confirm value is the following specification value, and turn the power off.

Specification value:

BD : ± 3 mA
DVD/CD : ± 3 mA

14. Turn the power on, confirm playback performance of the BD (BLX-104)/DVD (HLX-514)/CD (HLX-A1).
15. Completely assemble this unit, and complete the repair.

NOTE THE BD DRIVE (BPX-7) PARTS REPLACING

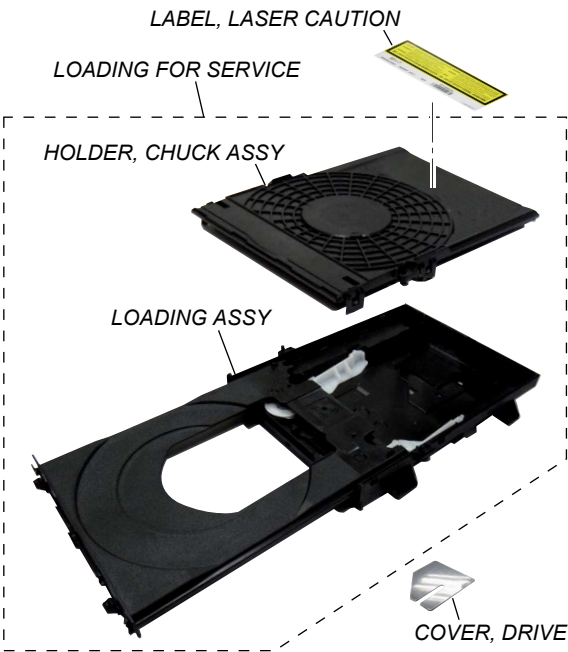
The mechanism blocks except optical device of BD drive (BPX-7) are chiefly composed of the following parts.

- HOLDER, CHUCK ASSY
- LOADING ASSY
- COVER, DRIVE

These parts are produced by two venders, it is not compatible. Therefore, HOLDER, CHUCK ASSY and LOADING ASSY are supplied by one pair as repair parts. Please exchange both HOLDER, CHUCK ASSY and LOADING ASSY at the same time.

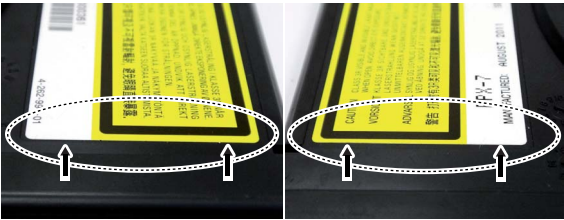
COVER, DRIVE need not be exchanged at the same time.

Note 1: The laser caution label is not pasted to LOADING FOR SERVICE. Please peel off an original laser caution label, and paste it to LOADING FOR SERVICE when you use LOADING FOR SERVICE.



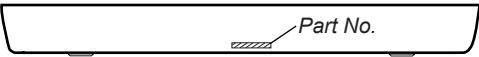
• Limit sample in state of laser caution label

Note 2: If laser caution label is a condition of the figure below, it is acceptable.



MODEL IDENTIFICATION

- Rear view -



Model	Part No.
E385/E390/T39	4-295-427-2□

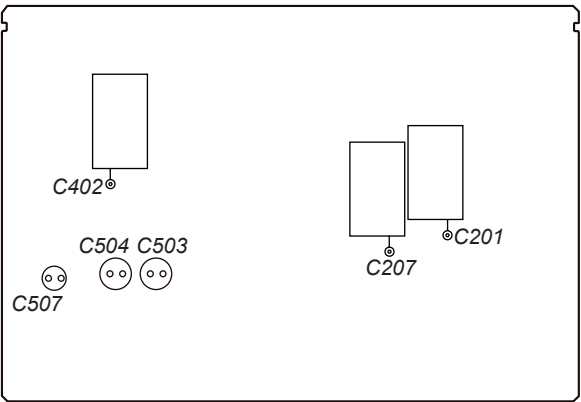
CAPACITOR ELECTRICAL DISCHARGE PROCESSING

When checking the board, the electrical discharge is necessary for the electric shock prevention.

Connect the resistor to both ends of respective capacitors.

- Switching regulator
C207, C201, C402, C503, C504, C507

- SWITCHING REGULATOR (Conductor Side) -



(To both ends of each capacitor)

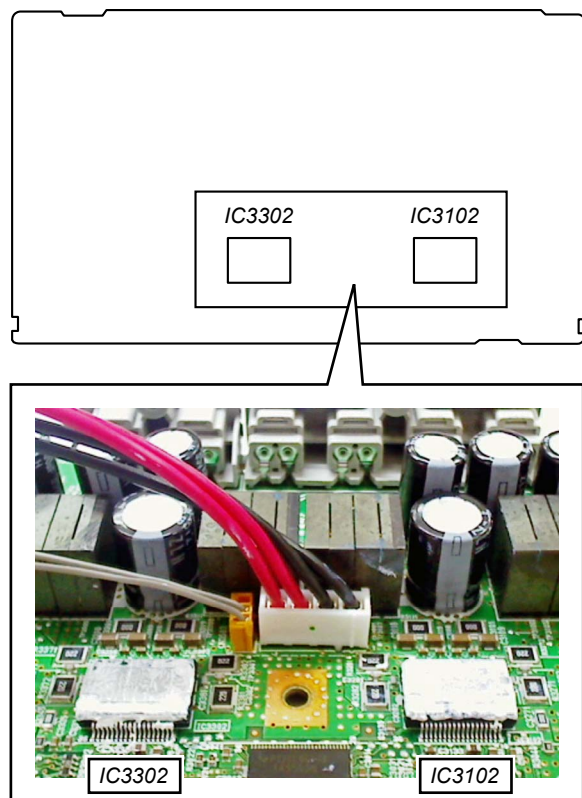


NOTE OF REPLACING THE IC3102 AND IC3302 ON THE AMP BOARD AND THE COMPLETE AMP BOARD

When IC3102 and IC3302 on the AMP board and the complete AMP board are replaced, it is necessary to spread the compound (GREASE (G-747) 1KG) (Part No. 7-640-004-33) between parts and heat sink.

Spread the compound referring to the figure below.

– AMP Board (Component Side) –



CHECKING METHOD OF NETWORK OPERATION

It is necessary to check the network operation, when replacing the MB148 board. Check the operation of wireless and wired LAN, according to the following method.

1. Checking Method of Wireless LAN Operation

Check that access point is recognized surely.

Necessary Equipment:

Wireless access point with router function (AP)

Procedure:

1. Press the [HOME] button on the remote commander to enter the home menu.
2. Press the [↓]/[↑]/[←]/[→] buttons on the remote commander to select “Network Settings”→“Internet Settings”, and press the [⊕] button on the remote commander.
3. Press the [↓]/[↑] buttons on the remote commander to select “Wireless Setup (built-in)”, and press the [⊕] button on the remote commander.
4. Press the [↓]/[↑] buttons on the remote commander to select “Scan”, and press the [⊕] button on the remote commander.
5. The system starts searching for access points, and displays a list of up available network name (SSID).
6. Check that access point (SSID) is displayed on the searching result.

Note: Refer to the instruction manual about details of the setting method.

2. Checking method of wired LAN operation

Check that access point is recognized surely.

Procedure:

1. Connect the main unit to the router or the hub, etc. with the LAN cable.
2. Press the [HOME] button on the remote commander to enter the home menu.
3. Press the [↓]/[↑]/[←]/[→] buttons on the remote commander to select “Network Settings”→“Internet Settings”, and press the [⊕] button on the remote commander.
4. Press the [↓]/[↑] buttons on the remote commander to select “Wired Setup”, and press the [⊕] button on the remote commander.
5. Press the [↓]/[↑]/[←]/[→] buttons on the remote commander to select “Auto”, and press the [⊕] button on the remote commander.
6. Press the [→] button on the remote commander.
7. Press the [↓]/[↑] buttons on the remote commander to select “Save & Connect”, and press the [⊕] button on the remote commander.
8. When “Internet Settings is now complete.” appears, then press the [⊕] button on the remote commander.
9. Press the [HOME] button on the remote commander to enter the home menu.
10. Press the [↓]/[↑]/[←]/[→] buttons on the remote commander to select “Network Settings”→“Internet Settings”, and press the [⊕] button on the remote commander.
11. Press the [↓]/[↑] buttons on the remote commander to select “Network Connection Diagnostics”, and press the [⊕] button on the remote commander.
12. Press the [←]/[→] buttons on the remote commander to select “Start”, and press the [⊕] button on the remote commander.
13. Check that IP address can be acquired.

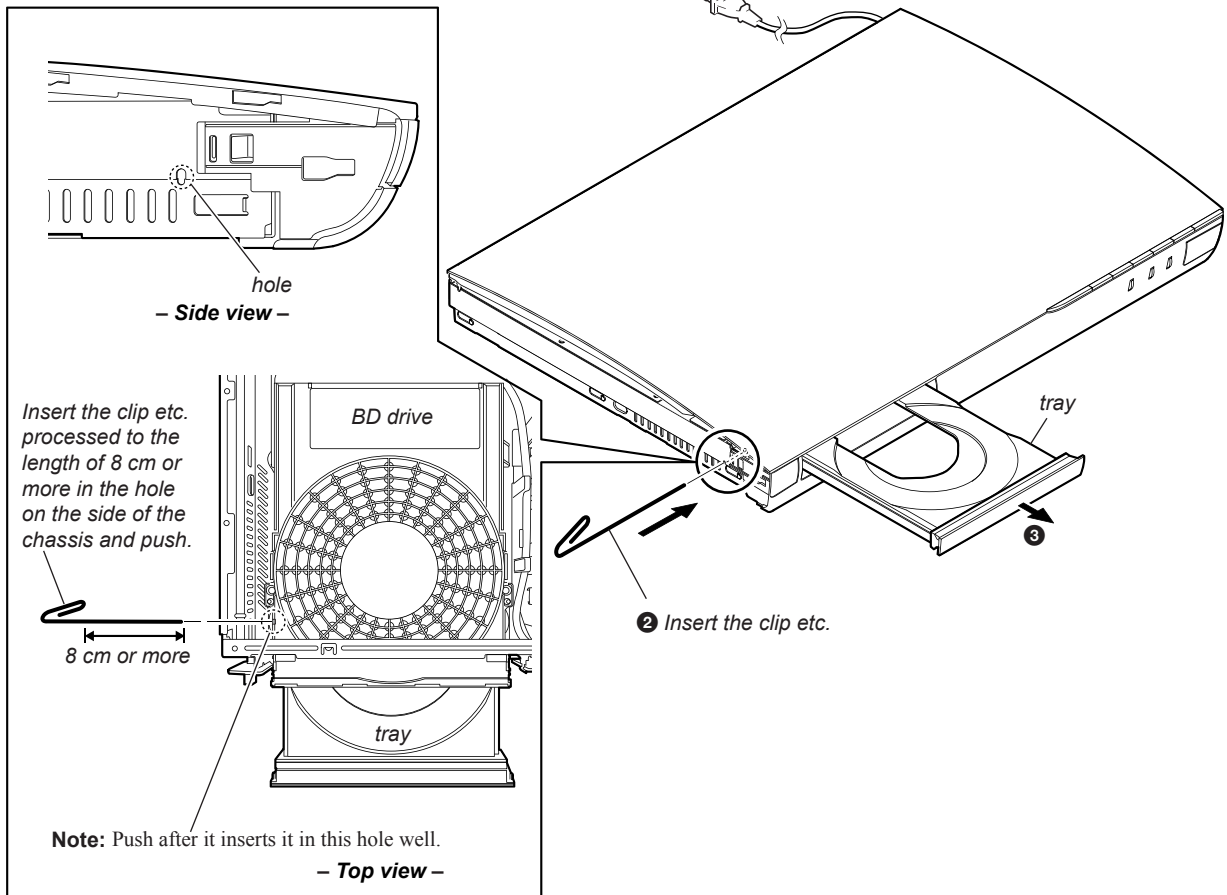
Note: Refer to the instruction manual about details of the setting method.

HOW TO OPEN THE TRAY WHEN POWER SWITCH TURN OFF

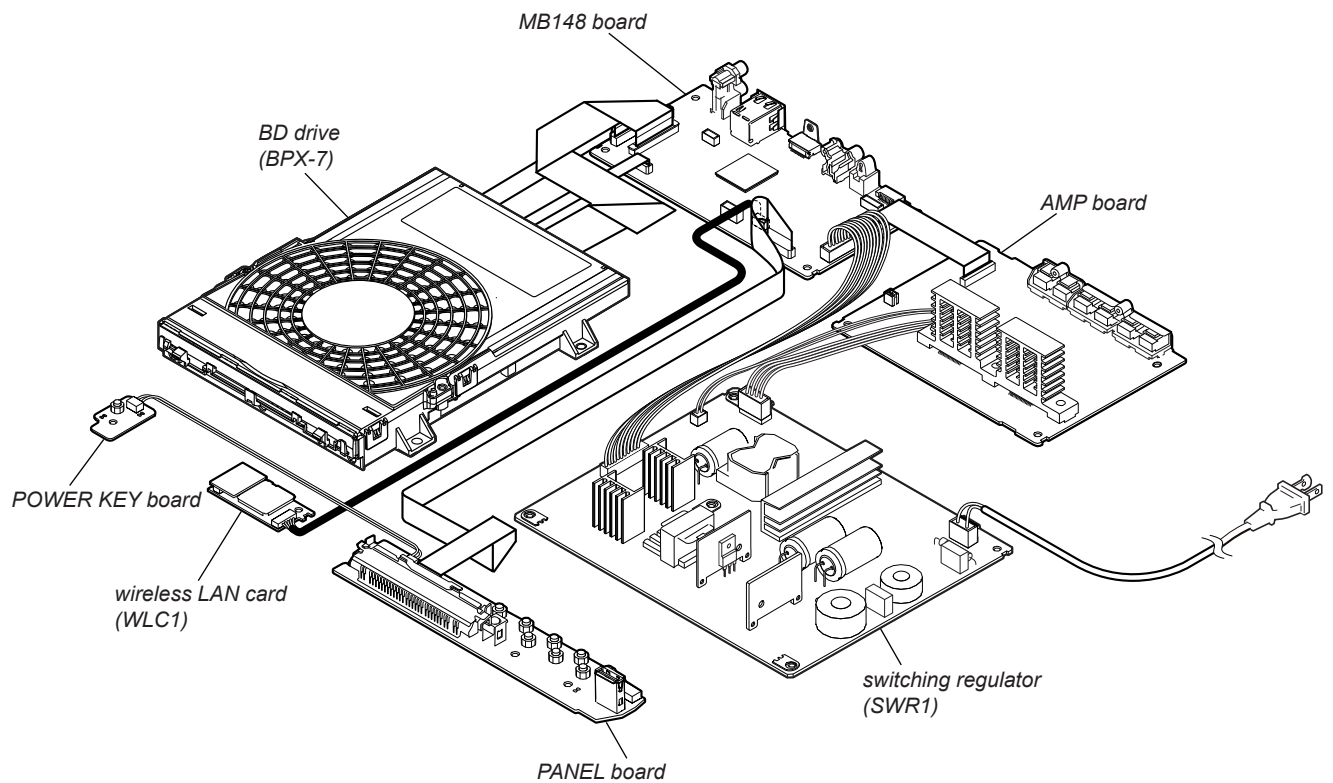
Note 1: After the left panel is removed, this work is done.

Note 2: Please prepare the thin wire (clip etc. processed to the length of 8 cm or more).

- ❶ Remove the panel, left.
(Illustration of disassembly is omitted.)



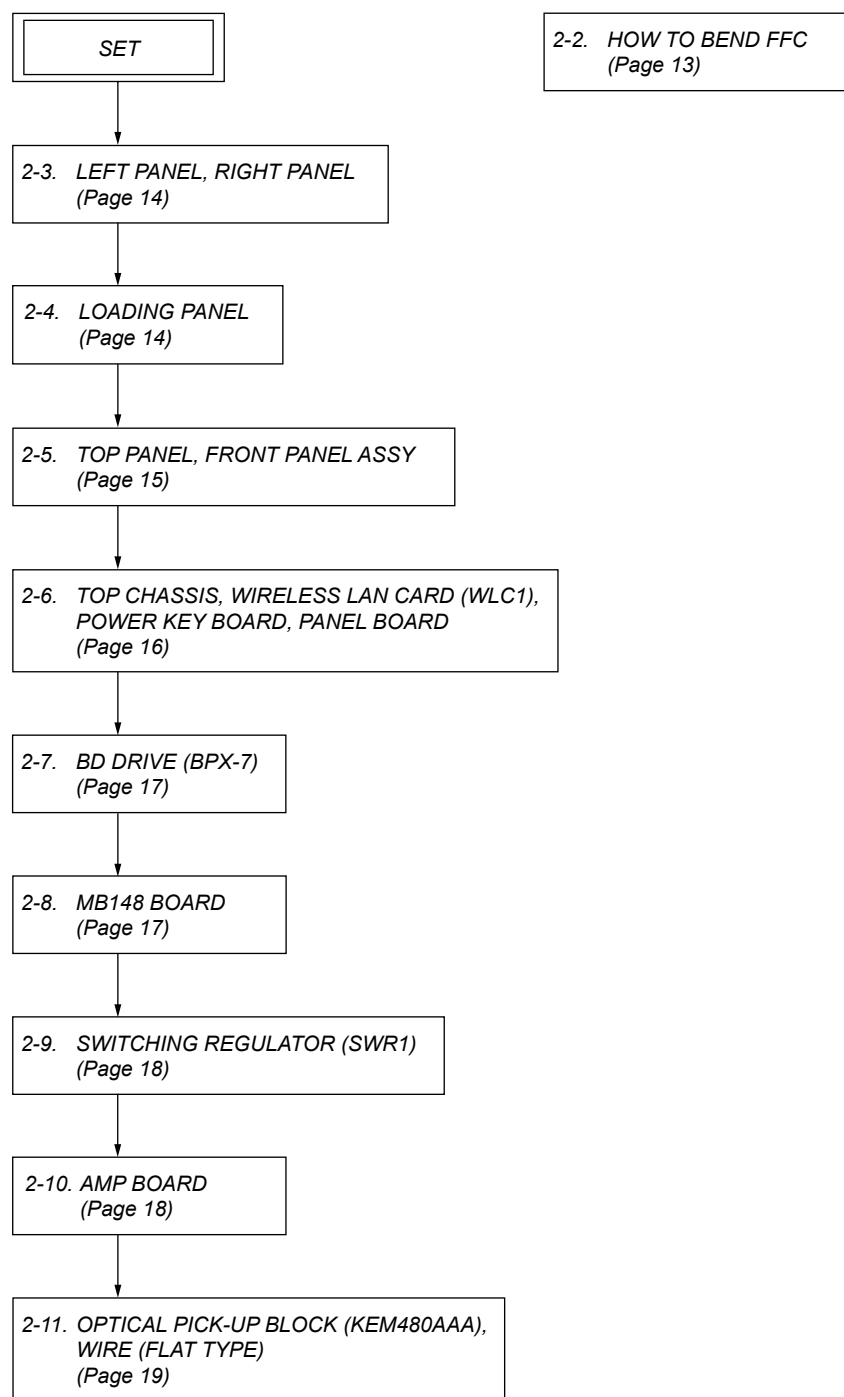
SERVICE POSITION



SECTION 2 DISASSEMBLY

- This set can be disassembled in the order shown below.

2-1. DISASSEMBLY FLOW

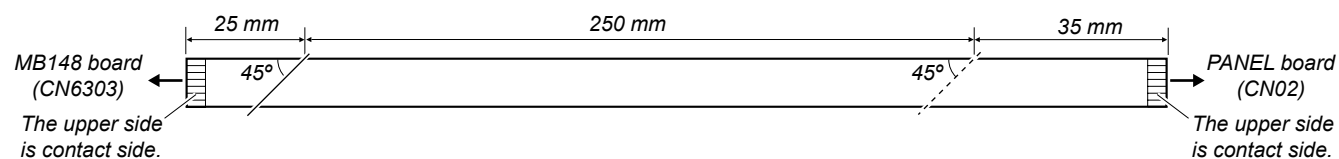


2-2. HOW TO BEND FFC

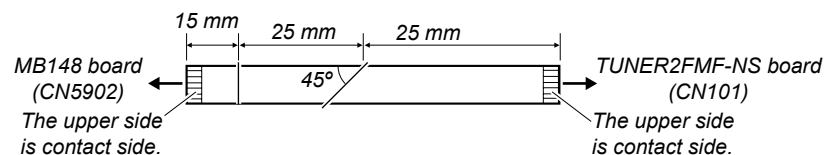
————— : Mountain fold

----- : Valley fold

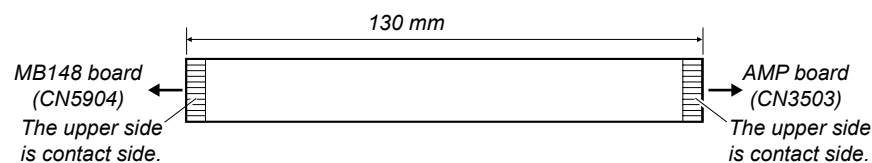
wire (flat type) (13 core), length 320 mm (Ref. No. 55)



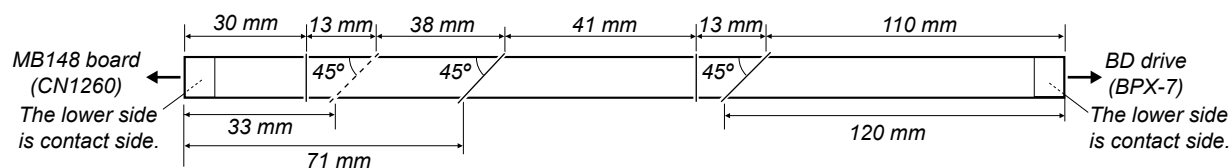
wire (flat type) (7 core), length 65 mm (Ref. No. 104)



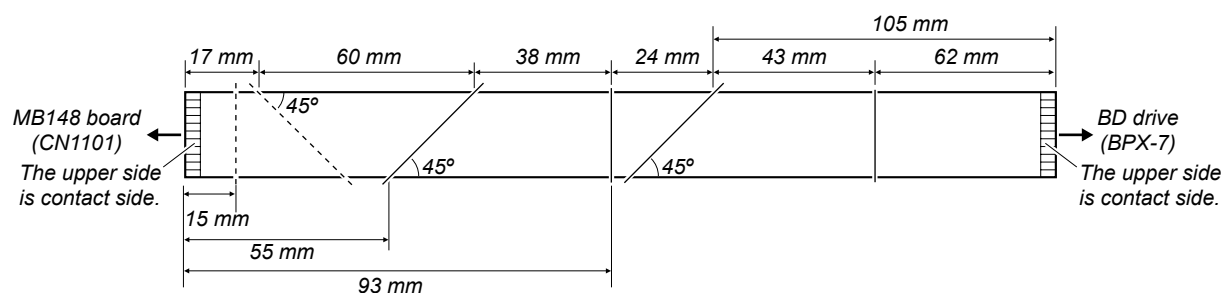
wire (flat type) (30 core), length 130 mm (Ref. No. 107)



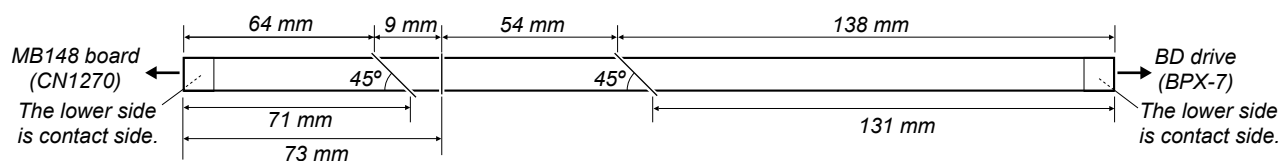
wire (flat type) (9 core), length 245 mm (Ref. No. 206)



flexible flat cable (45 core), length 244 mm (Ref. No. 207)

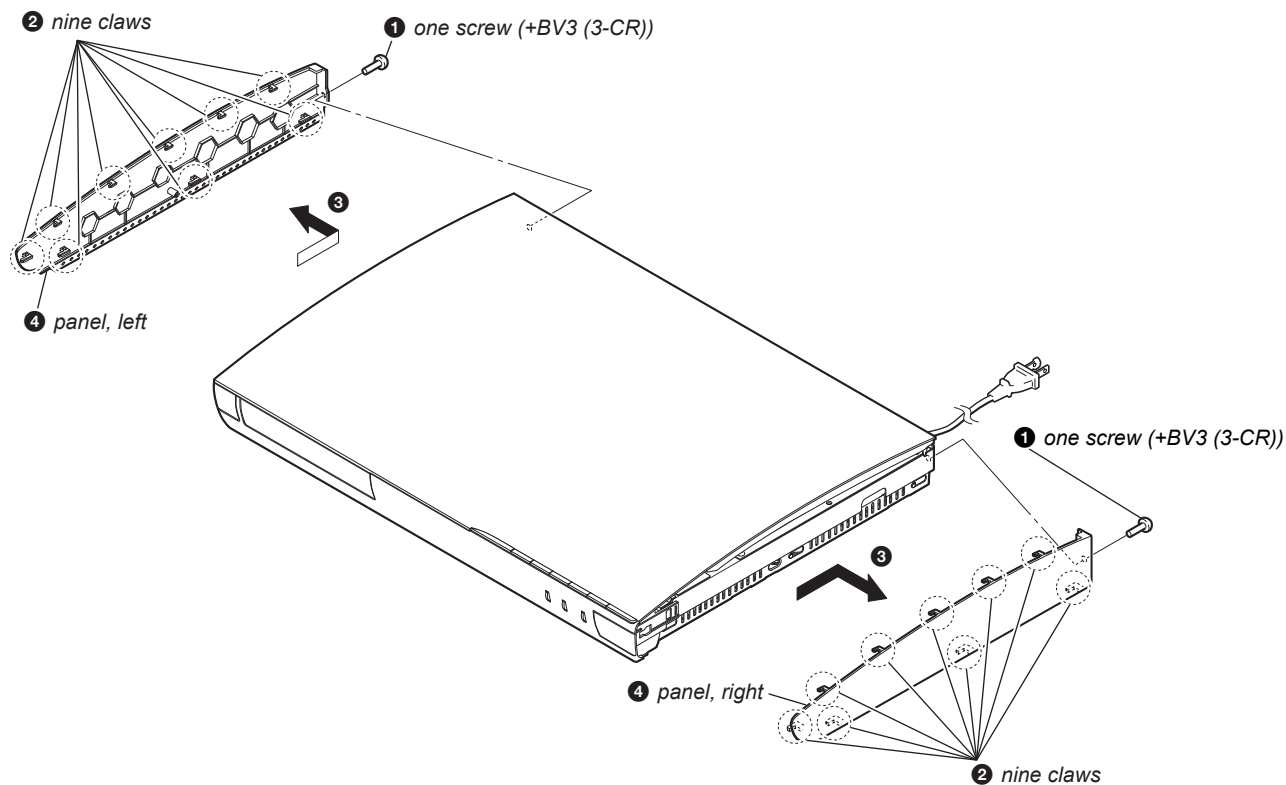


wire (flat type) (5 core), length 265 mm (Ref. No. 208)

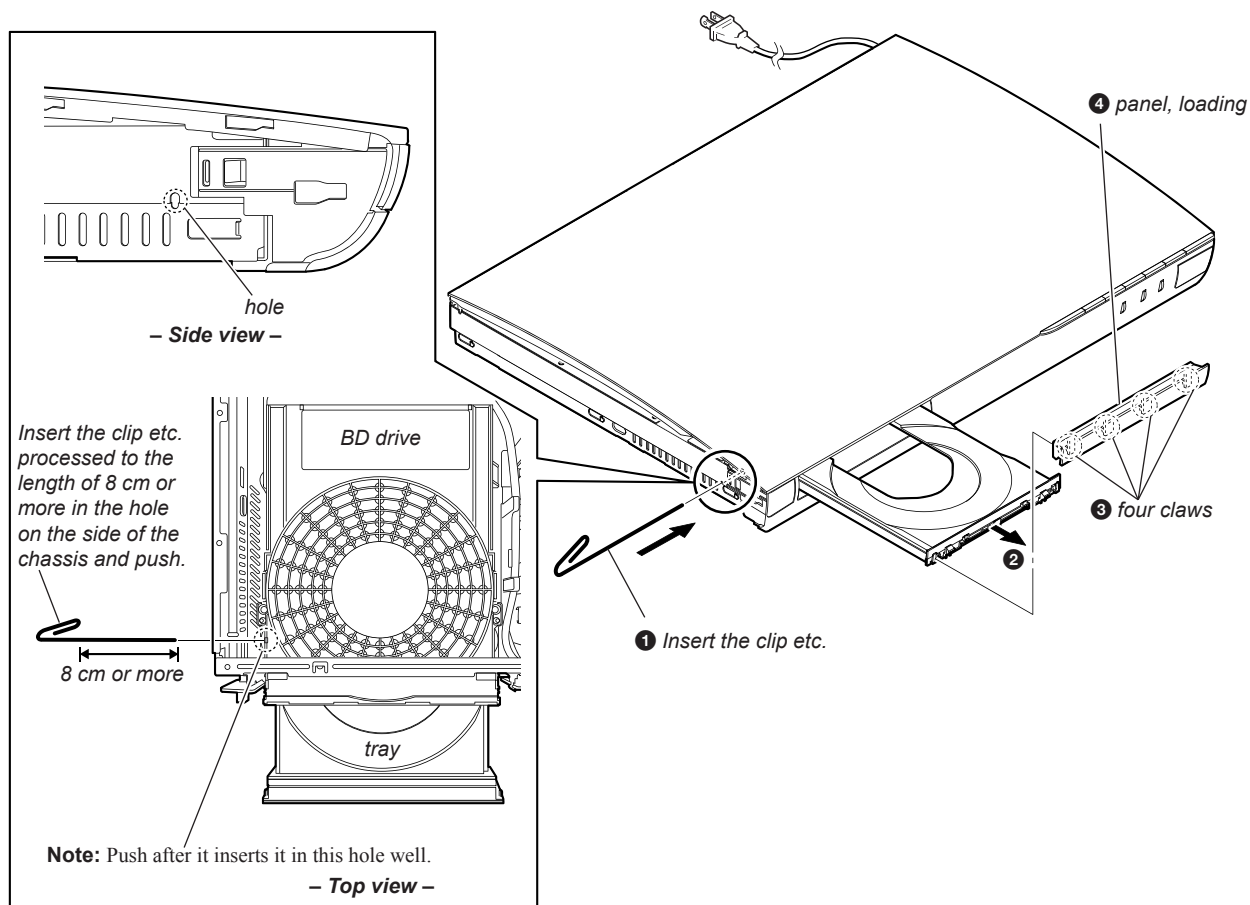


Note: Follow the disassembly procedure in the numerical order given.

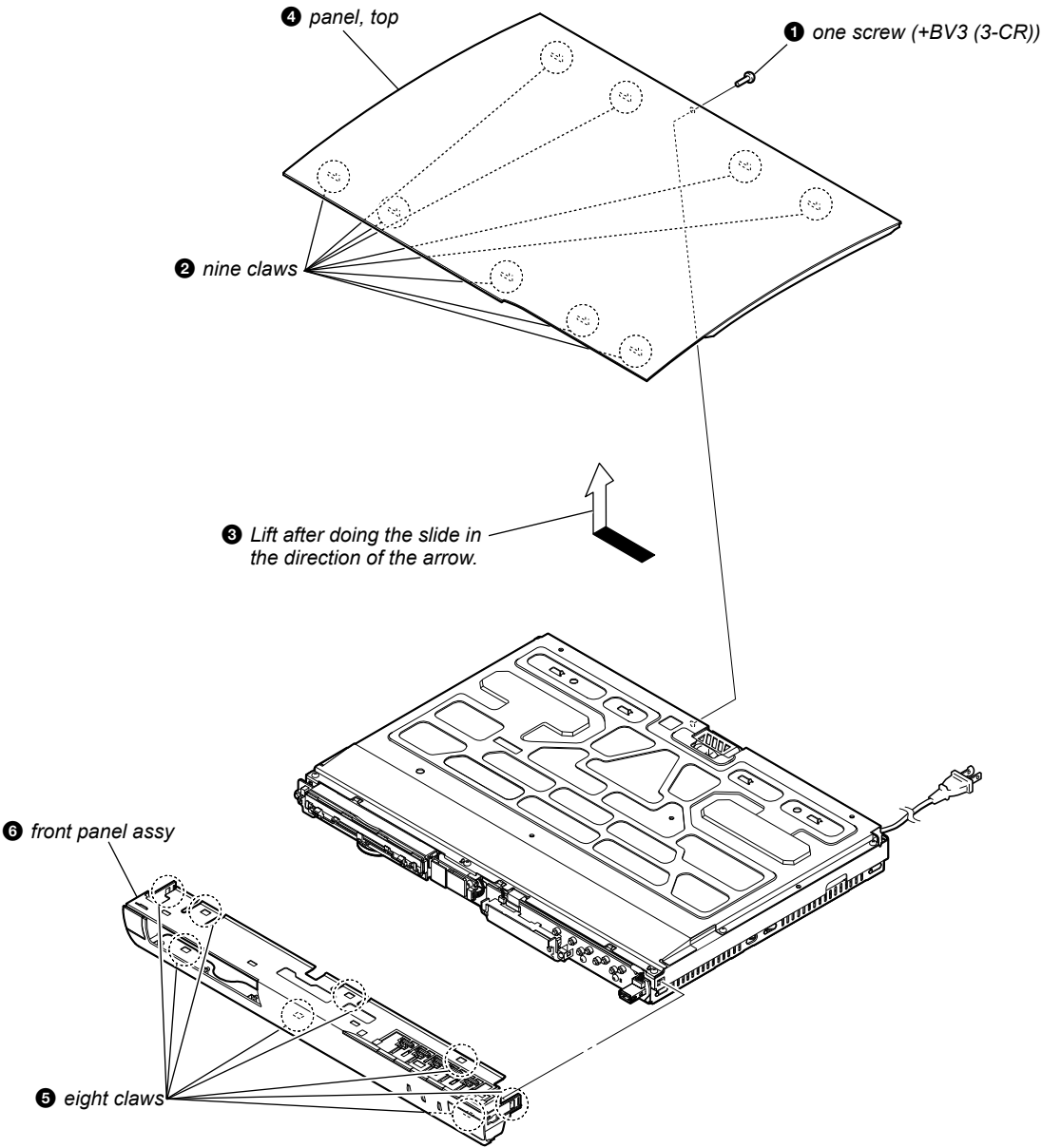
2-3. LEFT PANEL, RIGHT PANEL



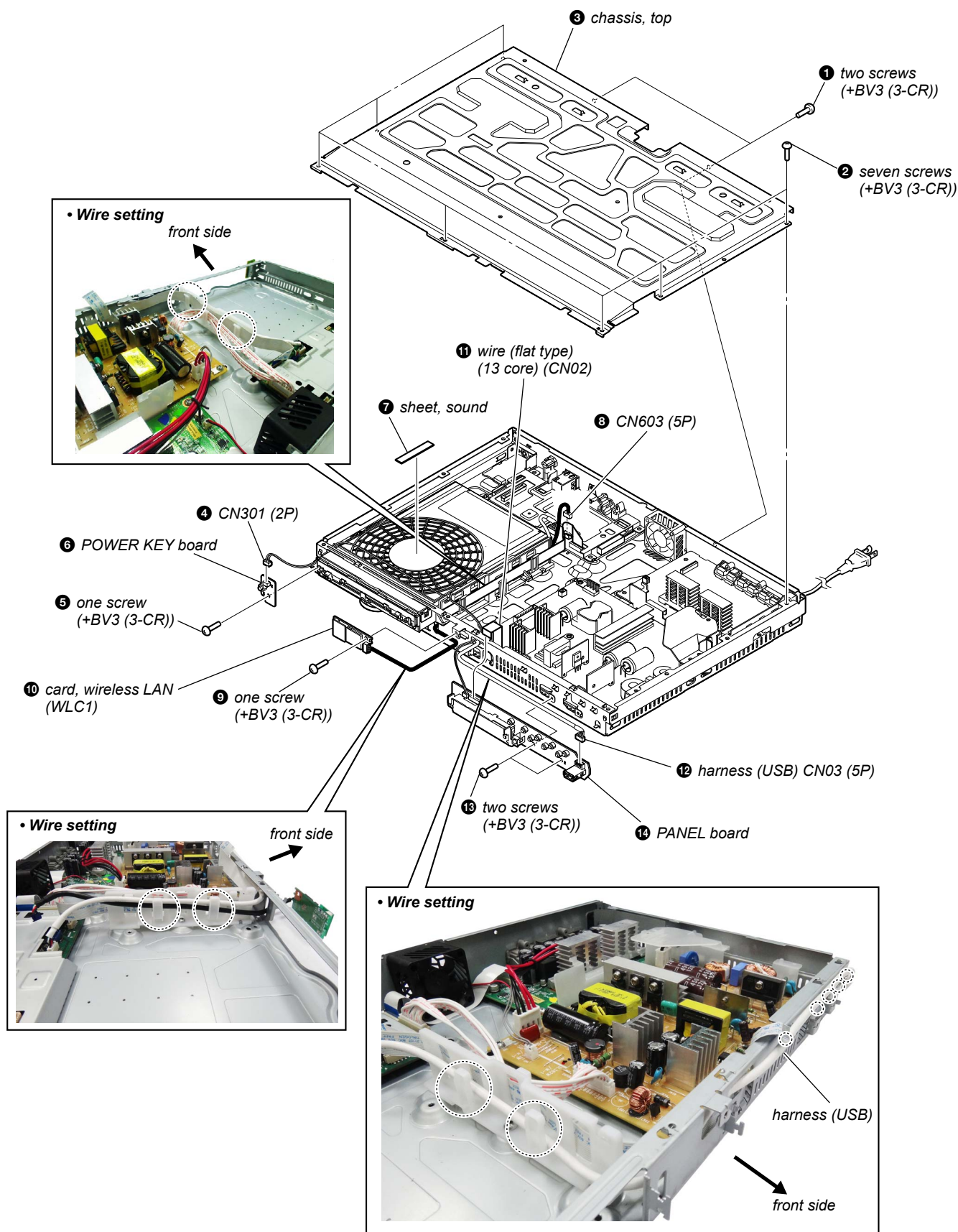
2-4. LOADING PANEL



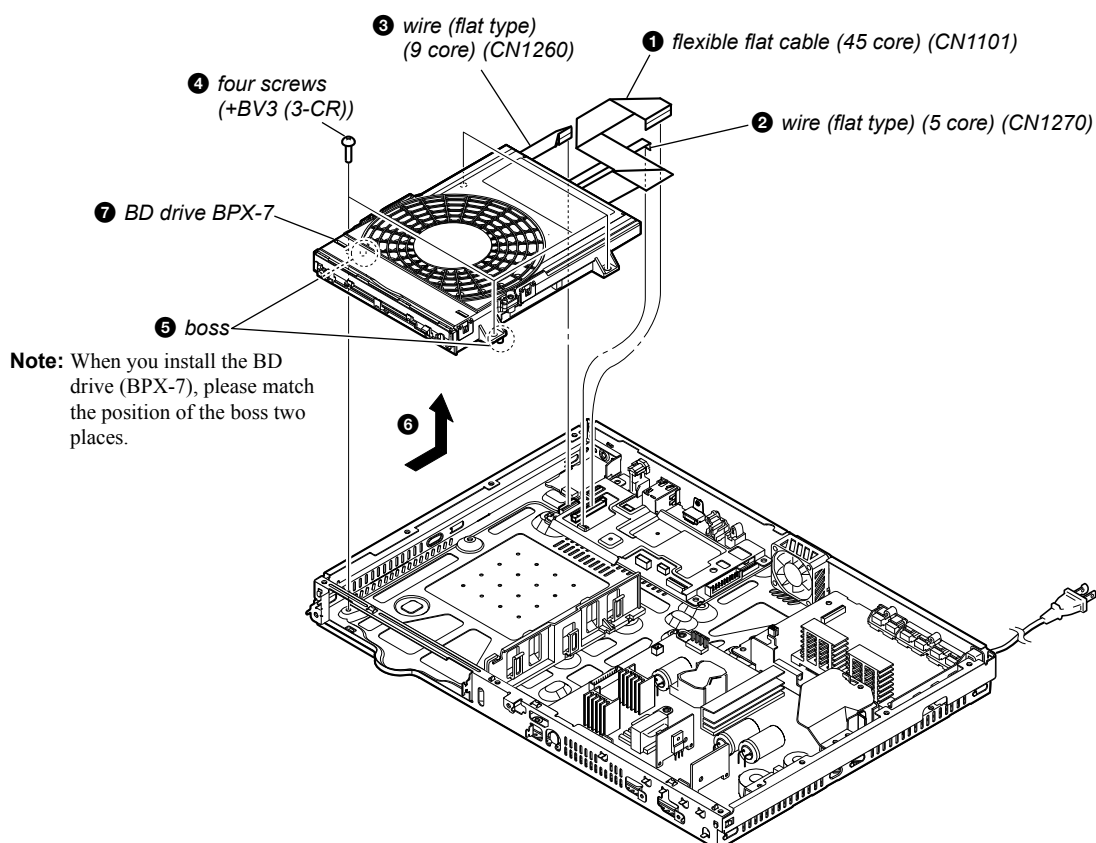
2-5. TOP PANEL, FRONT PANEL ASSY



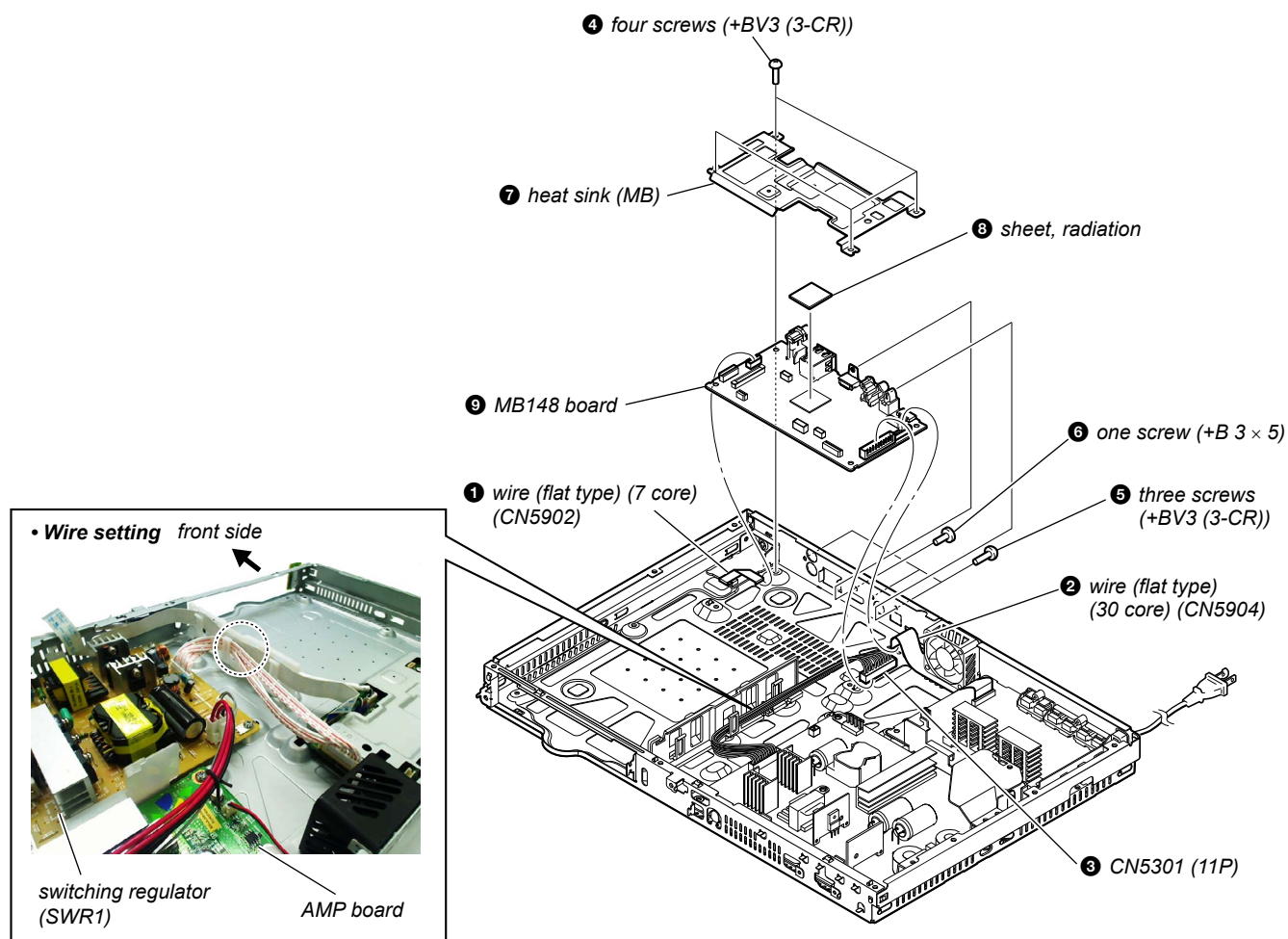
2-6. TOP CHASSIS, WIRELESS LAN CARD (WLC1), POWER KEY BOARD, PANEL BOARD



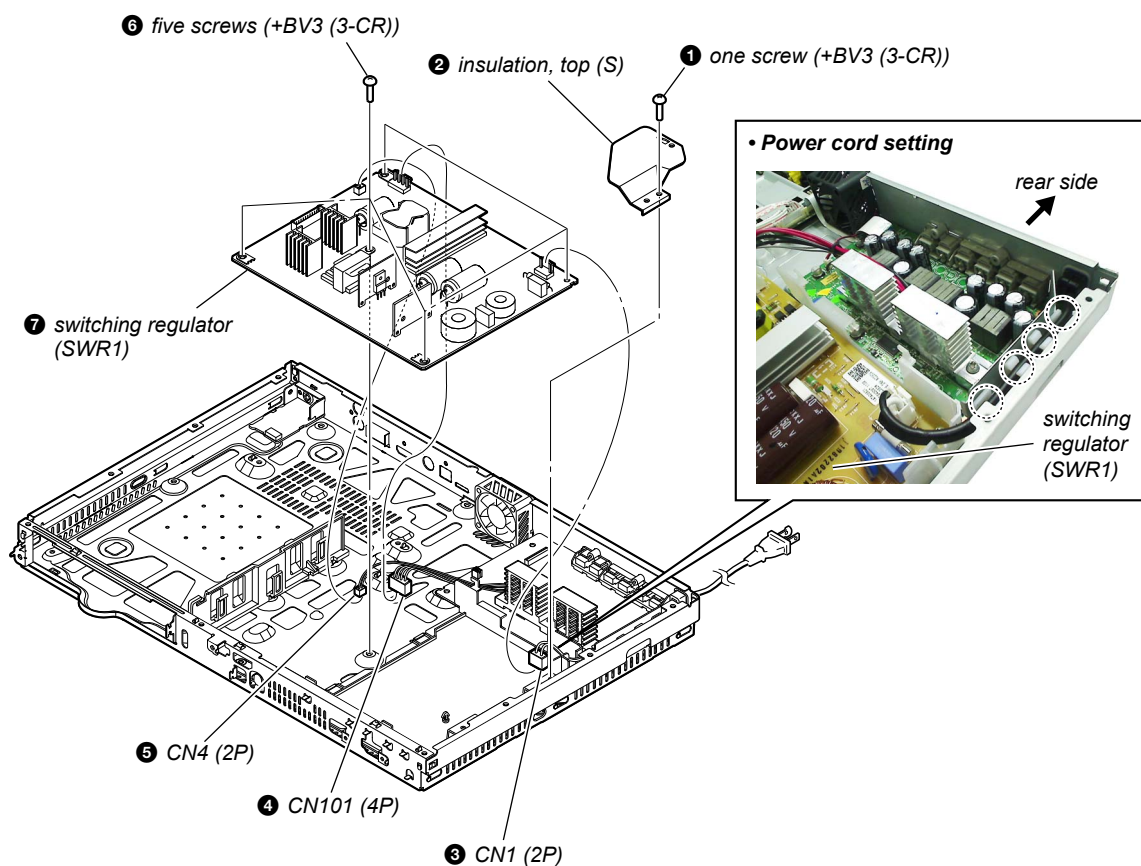
2-7. BD DRIVE (BPX-7)



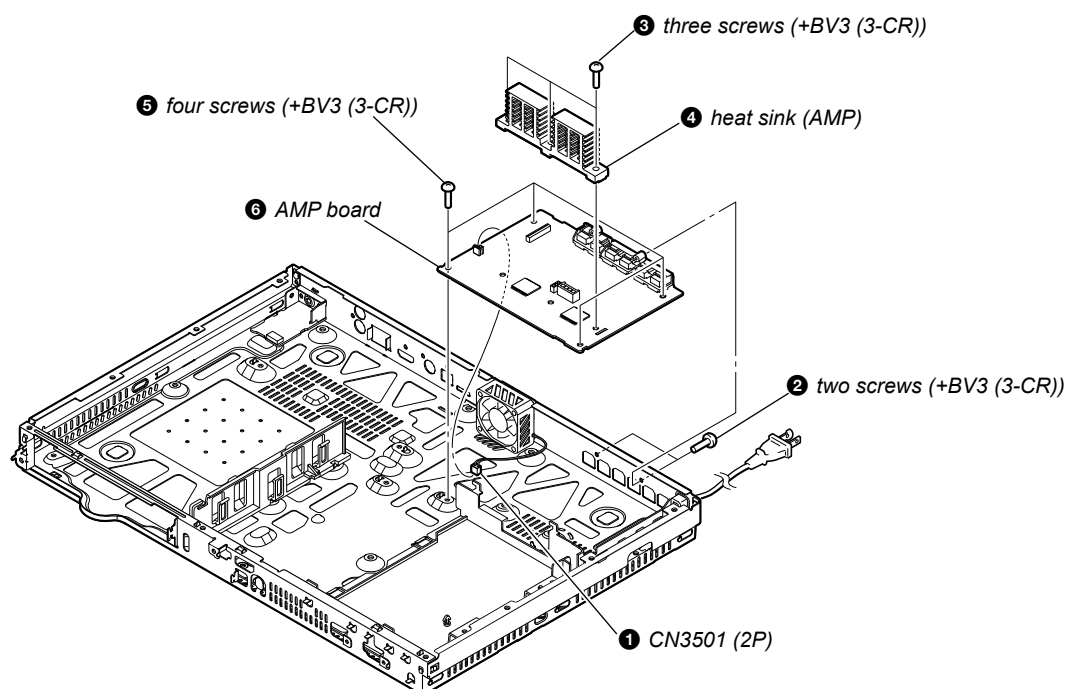
2-8. MB148 BOARD



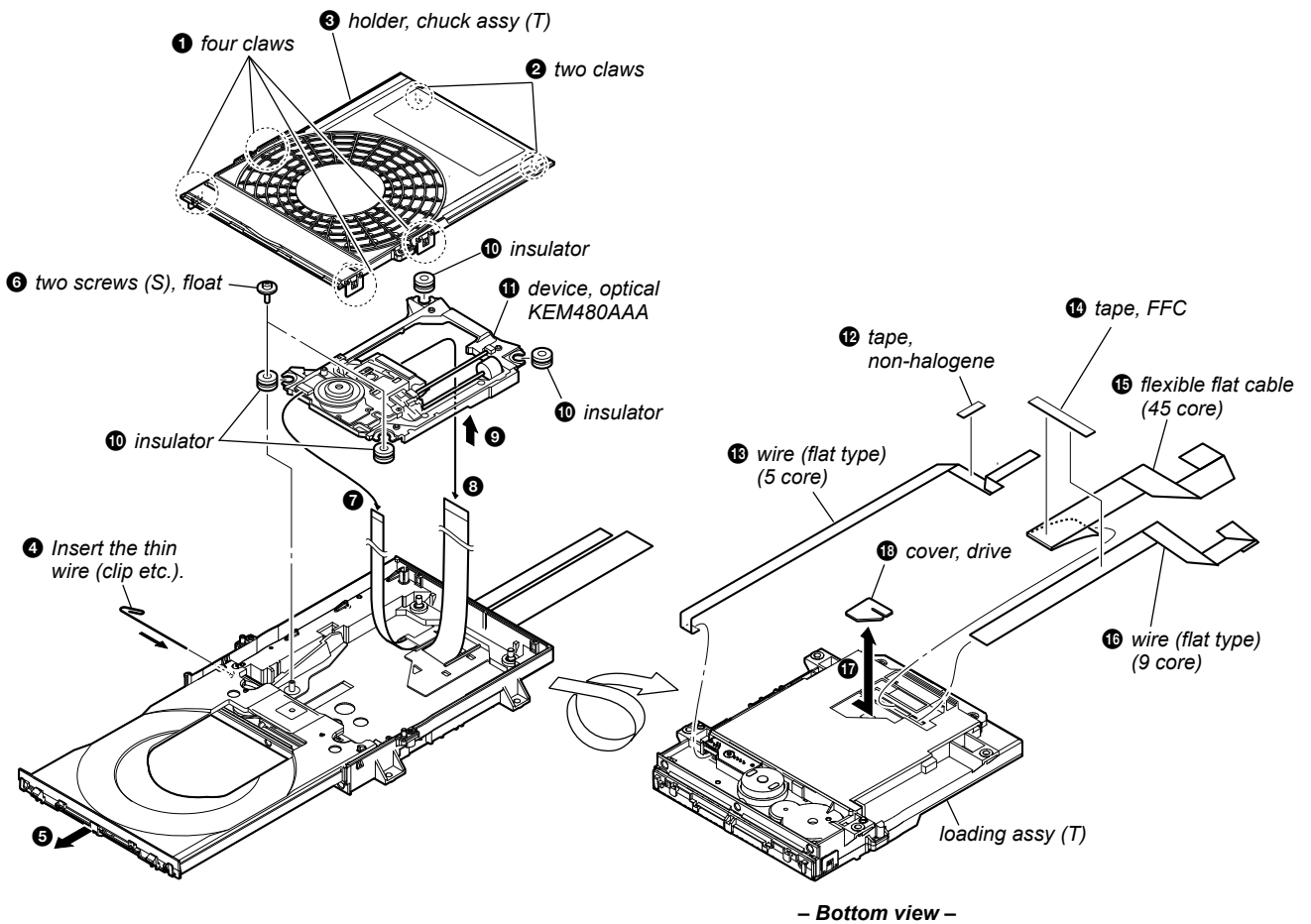
2-9. SWITCHING REGULATOR (SWR1)



2-10. AMP BOARD

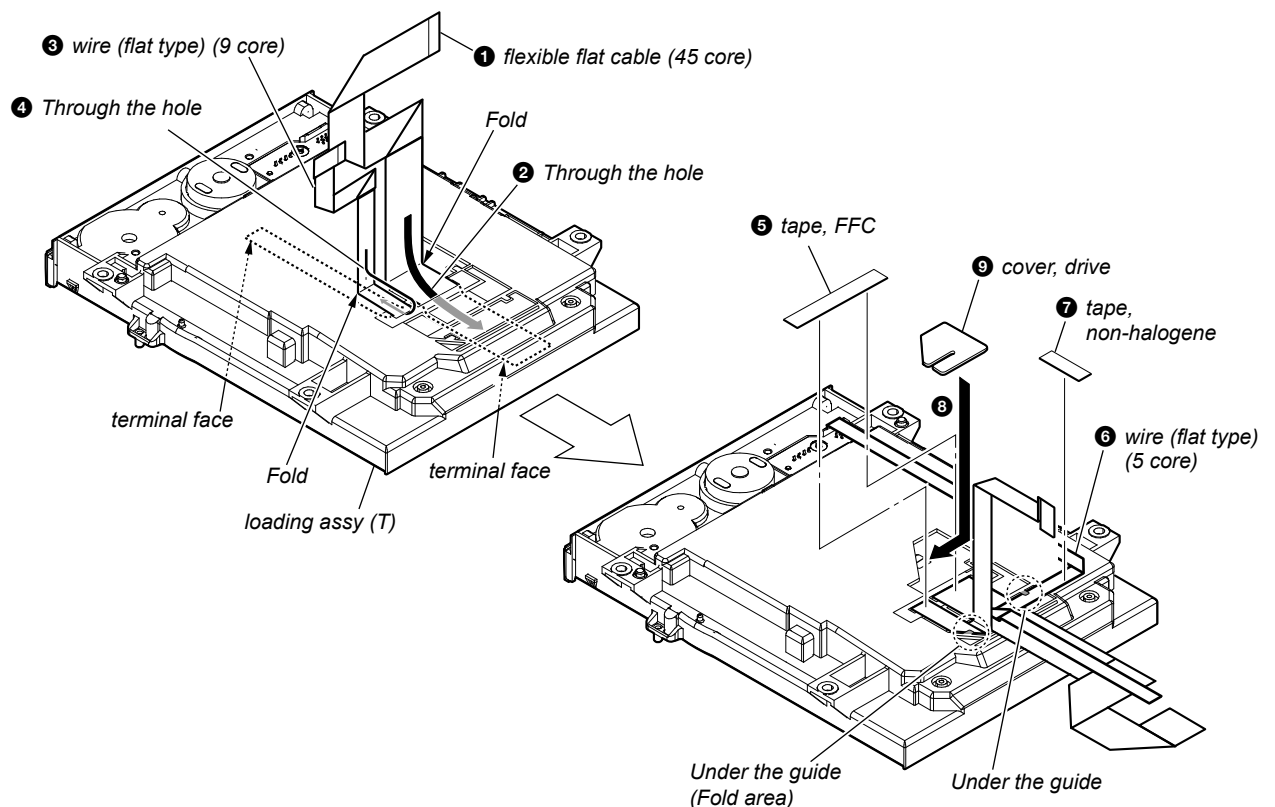


2-11. OPTICAL PICK-UP BLOCK (KEM480AAA), WIRE (FLAT TYPE)



• Installation of flexible flat cable (45 core), wire (flat type) (9 core) and wire (flat type) (5 core)

Note: This illustration sees the loading assy (T) from bottom side.



SECTION 3

TEST MODE

COLD RESET

The cold reset clears data except BD/DVD data stored in the RAM to initial conditions. Execute this mode when returning the unit to the customers.

Procedure:

1. Press the [I/⏻] button to turn the power on.
2. Press the [■] and [VOL -] buttons simultaneously and hold down (around 5 seconds).
3. The message “RESET” appears on fluorescent indicator tube, then becomes standby states.

DEMO MODE

This mode let you lock the disc tray. When this mode is activated, the disc will not eject when the [▲] button is pressed. The message “DEMO. LOCK” will be displayed on the fluorescent indicator tube.

Procedure:

1. Press the [I/⏻] button to turn the power on.
2. Press the [FUNCTION] button to select the “BD/DVD”.
3. Press the [■] and [▲] buttons simultaneously and hold down until “DEMO. LOCK” or “DEMO OFF” displayed on the fluorescent indicator tube (around 5 seconds).

Releasing method:

Press the [■] and [▲] buttons simultaneously and hold down until “DEMO OFF” displayed on the fluorescent indicator tube (around 5 seconds).

PANEL TEST

Procedure:

1. Press the [I/⏻] button to turn the power on.
2. Press button in order of the [DISPLAY] → [0] → [0] → [1] → [SUBTITLE] on the remote commander (Make the interval when each button is pressed within two seconds).
3. All segments in fluorescent indicator tube are lighted up. And half segments in fluorescent indicator tube are lighted up, others half segments in fluorescent indicator tube are lighted up, then all segments in fluorescent indicator tube are lighted up. This operation is repeated.
4. When all segments in fluorescent indicator tube are lighted up in the state of step 3, press the [VOL +] button on the remote commander and model information is displayed on the fluorescent indicator tube.
Each time the [VOL +] button on the remote commander is pressed, the display changes from destination information, STR version in this order, and returns to the model information display.
5. In the state of step 3, press the [FUNCTION] button on the remote commander and “K 0” is displayed on the fluorescent indicator tube.
“K 0” value increases whenever a button or sensor on the unit is pressed. However, once a button or sensor has been pressed, it is no longer taken into account.
All button and sensors on the unit are pressed, “OK” and “K 9” are alternately displayed on the fluorescent indicator tube.

Releasing method:

To release from this mode, press the [I/⏻] button.

FACTORY INITIALIZE

Return all of the unit setting to their factory defaults.

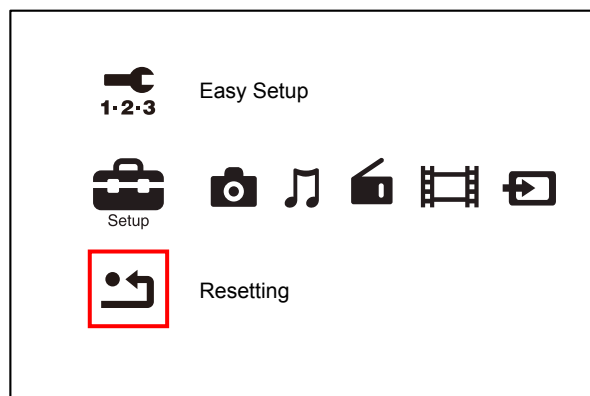
Note 1: Disconnect the following connections when you use this mode.

- USB
- LAN

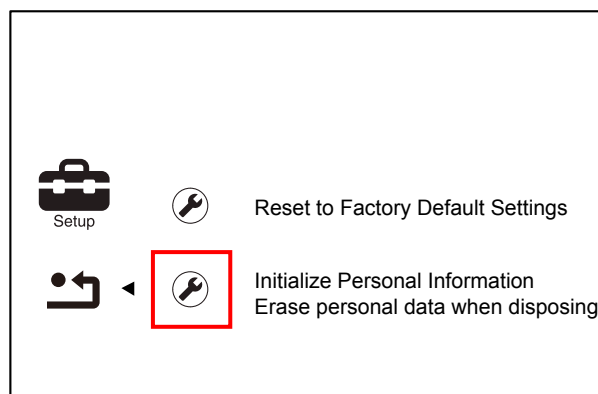
Note 2: The operation in this mode must use a remote commander and TV monitor.

Procedure:

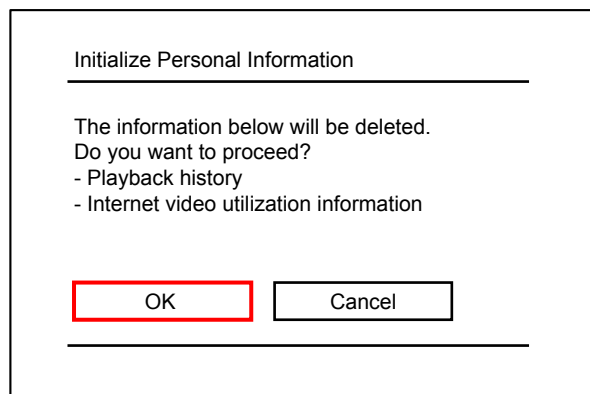
1. Press the [I/⏻] button to turn the power on.
2. Press the [HOME] button on the remote commander, and the home menu is displayed.
3. Select “Setup” → “Resetting”, and press the [⊕] button on the remote commander.



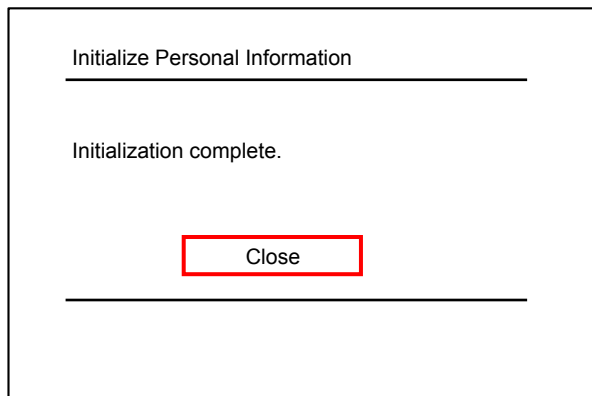
4. Select “Initialize Personal Information”, and press the [⊕] button on the remote commander.



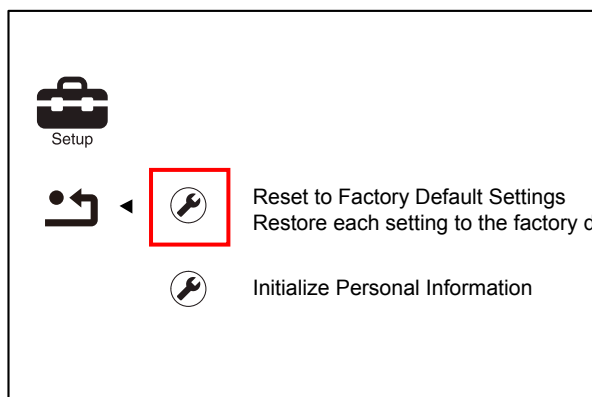
5. Select “OK”, and press the [⊕] button on the remote commander.



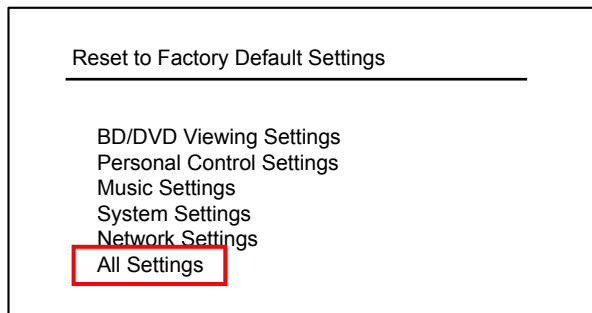
6. The message “Close” appears, and press the $\oplus$ button on the remote commander.



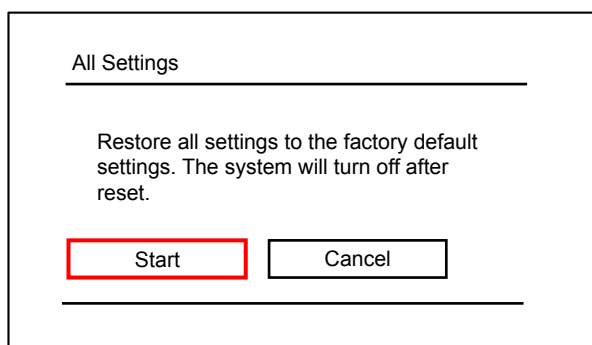
7. Select “Reset to Factory Default Settings”, and press the $\oplus$ button on the remote commander.



8. Select “All Settings”, and press the $\oplus$ button on the remote commander.



9. Select “Start”, and press the $\oplus$ button on the remote commander.



10. Initialization ends when the message “WELCOME” on the fluorescent indicator tube disappears.

BD SERVICE MODE

Note: The operation in this mode must use a remote commander and TV monitor.

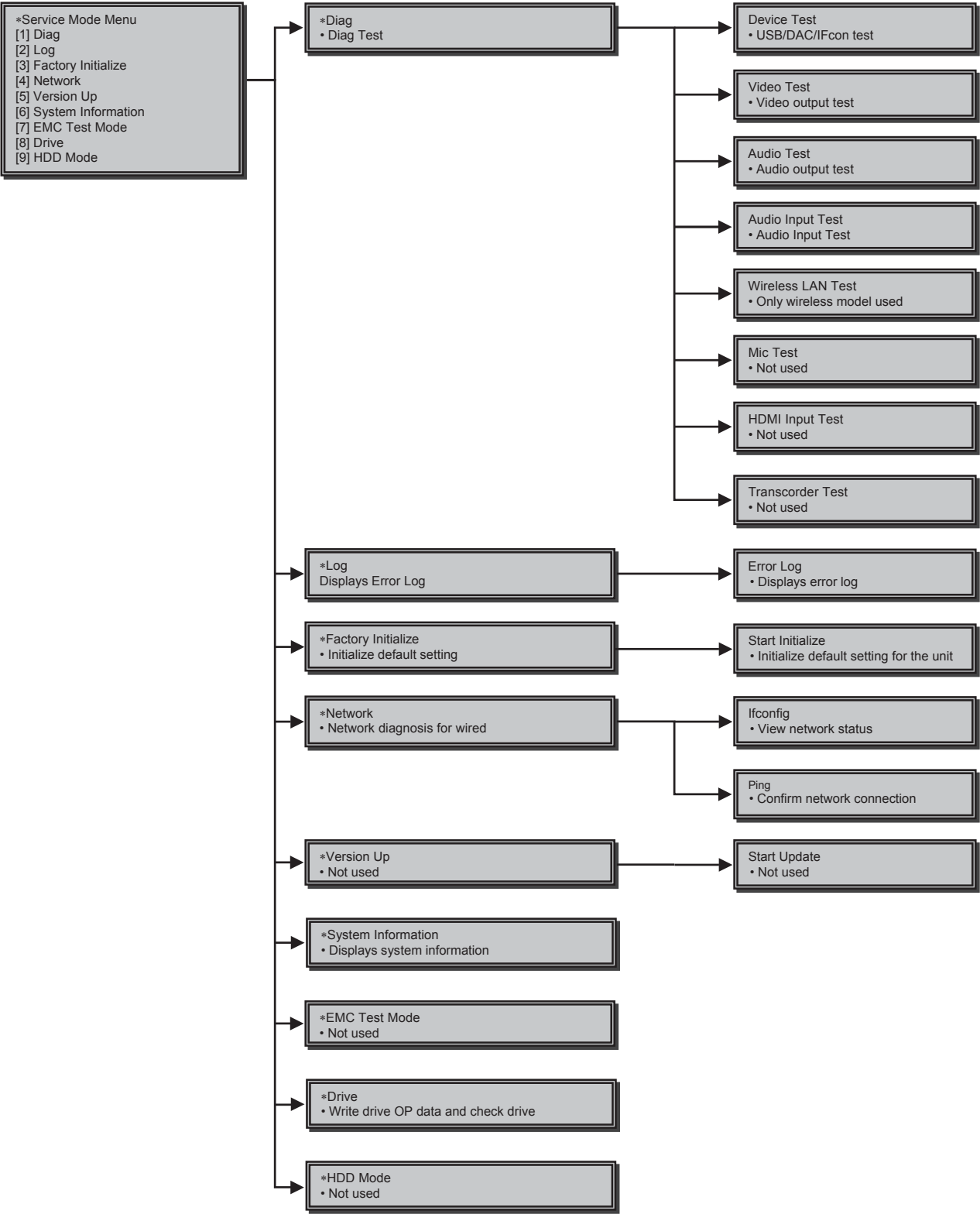
Setting method of the BD service mode:

1. Connect this unit with TV monitor.
2. Press the $\text{I/}\odot$ button to turn the power on.
3. Press button in order of the [DISPLAY] $\rightarrow$ [0] $\rightarrow$ [2] $\rightarrow$ [1] $\rightarrow$ [SUBTITLE] on the remote commander. (Make the interval when each button is pressed within two seconds)
4. Enter the BD service mode. The OSD menu on TV monitor can be operated by remote commander.

1. Main Functions

- **Diag**
Performs unit test of devices installed on the board.
- **Log**
Error log is displayed. Displayed contents can also be saved in an USB memory device.
- **Factory Initialize**
Restores the unit to its factory settings.
- **Network**
Checks the wired network connection.
- **Version Up**
Not used.
- **System Information**
Displays the system information of the unit.
Displays information such as the software version, drive information, etc.
- **EMC Test Mode**
Not used.
- **Drive**
Write drive OP data and check drive.

2. Menu Tree



3. Service Mode Menu (Top Menu)

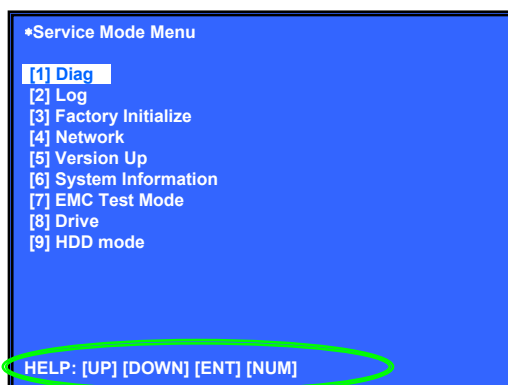
This is the top menu of service mode.

Each function is accessed from this screen.

Operation:

- [1] Moves to Diag screen
- [2] Moves to Log screen
- [3] Moves to Factory Initialize screen
- [4] Moves to Network screen
- [5] Moves to Version Up (DISC version update) screen (Not used)
- [6] Moves to System Information screen
- [7] Moves to EMC test mode screen (Not used)
- [8] Moves to Drive screen
- [9] Moves to HDD mode (Not used)
- [↑]/[↓] Moves the cursor
- [⊕] Moves to the screen of the item selected with the cursor

* Cursor is not displayed when the menu is first displayed.



HELP (currently available keys, etc.) is displayed

4. Diag (Device Test)

This screen is used to test devices mounted on the board.

Screen 1: Selects the test category

Operation:

- [←]/[→] Selects the category
- [↓]/[⊕] Moves to the selected category
- [RETURN] Returns to the service top menu

Screen 2: Device test

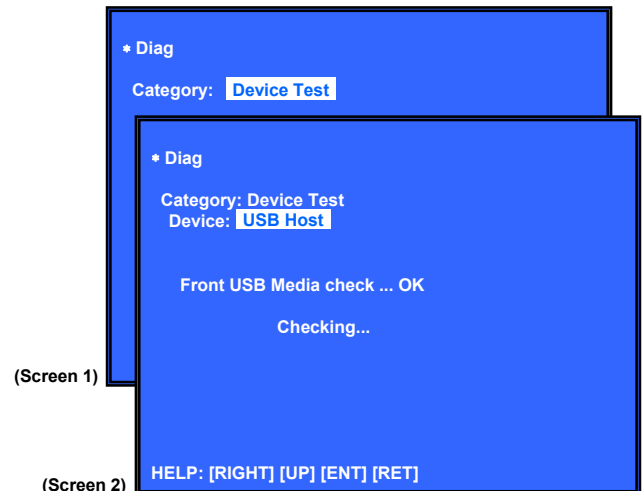
Selects the device to test after selecting Device Test in screen 1.

Operation:

- [←]/[→] Selects the device to test
- [⊕] Executes the test
- [↑] Returns to selection of test category
- [RETURN] Returns to selection of test category

List of test categories

Device Test
 Video Test
 Audio Test
 Audio Input Test
 Wireless LAN Test (Only wireless model used)
 Mic Test (Not used)
 HDMI Input Test (Not used)
 Transcoder Test (Not used)



- Device Test: List of devices
- USB Host : USB media check (front). Only one time.
- D/A Converter : DAC write check (non-verification).
- Ifcon : Not used in this unit.
- MIC : Not used in this unit.
- MFI : MFI read/write Check
- IPC : Not used
- External HDMI : Not used
- Transcoder : Not used

5. Diag (Video/Audio Test)

This screen performs video and audio tests.

Screen 1: When video test category is selected

Operation:

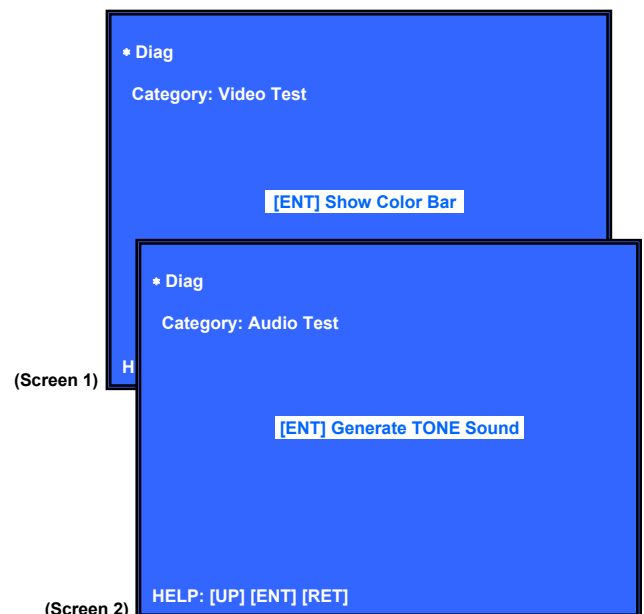
- [⊕] Shows/hides the color bar
- [↑]/[RETURN] Returns to the selection of test category

Screen 2: When audio test category is selected

Operation:

- [⊕] Plays back/stops the tone sound
- [↑]/[RETURN] Returns to the selection of test category

- Video test:
Outputs a color bar (composite & HDMI).
- Audio test:
TONE sound output (speaker & HDMI).



6. Diag (Audio Input Test)

This screen performs audio input test.

Screen 1: Select Audio Input Test Category

Operation:

- [←]/[→] Selects the category
- [↓]/[⊕] Activate the selected category
- [RETURN] Returns to the service top menu

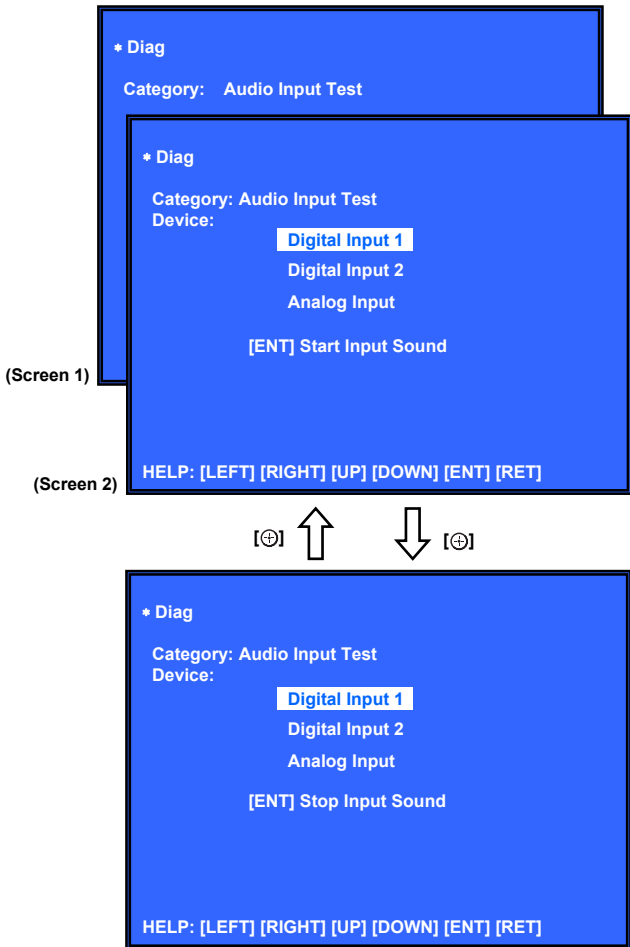
Screen 2: Select Input Device

After “Audio Input Test” selects in Screen 1, the device to test is chosen.

Operation:

- [↑]/[↓] Selects Device
- [⊕] Activate and Start Test/Stop
- [RETURN] Returns to the selection of test category

- Device Test : Device List
- Digital Input1 : CXD90008 is inputted from ARC
- Digital Input2 : CXD90008 is inputted from Optical
- Analog Input : CXD90008 is inputted from AUDIO
- Audio Input Test
- CXD90008 is inputted from DIR/analog and outputs LineOut (8ch) L and R.
- Check tone sound by speaker connected with LineOut (8ch) L and R.
- (Input; LPCM 48kHz 2ch)



7. Diag (Wireless LAN Test)

This screen performs wireless LAN test.

Note: This mode doesn't operate correctly. Never use this mode. Refer to “CHECKING METHOD OF NETWORK OPERATION” on page 10 when you confirm the wireless LAN operation.

8. Diag (MIC Input Test)

This screen performs MIC input test.

Note: Not used for the servicing.

9. Diag (HDMI Input Test)

This screen performs HDMI input test.

Note: Not used for the servicing.

10. Diag (Transcoder Test)

Note: Not used for the servicing.

11. Log: Error Log (Output of each Log)

This screen displays the contents of each log.

Note: Do not refer to the displayed date.

Screen 1: Selects log

Operation:

- [1]/[⊕] Moves to the Error Log output screen
- [RETURN] Returns to the top menu of the service mode

Screen 2: Displays the Error Log

Operation:

- [←] Returns to the previous page
- [→] Moves to the next page
- [RETURN] Returns to the screen (Screen 1) that selects the log type
- [RED] Writes the log contents to an USB memory device

- Viewing the log display

Error Log:

[174] 2010/01/01 00:00:08: [ErrCode:0902A4053002]
[Index number] [Date] [Time] [Error code]

About copying log to USB memory device:

Press the [RED] button in each log display screen with the USB memory device inserted into the unit.

Note: Please do not press the [RED] button immediately after USB memory is inserted.
Please do not pull out USB memory immediately after the [RED] button was pressed.

Error Log:

When “getErrLogFile.trm file” exists in the USB memory device, errlog.log file is output.



12. Factory Initialize (Factory Settings)

There is a possibility that this mode cannot be correctly executed.
Never use this mode.

13. Network (Network Test Diagnosis Screen: Ifconfig)

Network menu for the wired ethernet.

Screen 1: Ifconfig Test

Operation:

[⊕] Activate Ifconfig (Display network setting)
[➡] Select ping test
[RETURN] Returns to the top menu of the service mode

Screen 2: Ping Test

Operation:

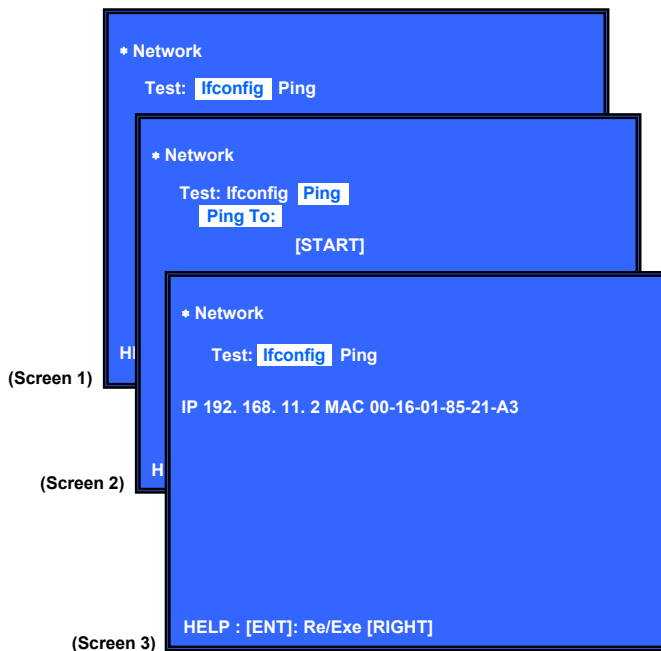
[←] Select Ifconfig test
[RETURN] Returns to the top menu of the service mode
(The details of a Ping test are “14. Network (Network Test Diagnosis Screen: Ping)”)

Screen 3: Ifconfig Test Active

Display Ifconfig command results.

Operation:

[⊕] Ifconfig retry
[➡] Select Ping test
[RETURN] Returns to the top menu of the service mode



14. Network (Network Test Diagnosis Screen: Ping)

Ping test for the wired ethernet.

Screen 1: Ping Test

Operation:

[←] Select Ifconfig test
[↓] Ping execution preparation
[RETURN] Returns to the top menu of the service mode

Screen 2: The IP address of the Ping point is set up (IP address input mode)

When “Ping to :>” is reversed, the [⊕] button is pressed and IP is inputted.

Operation:

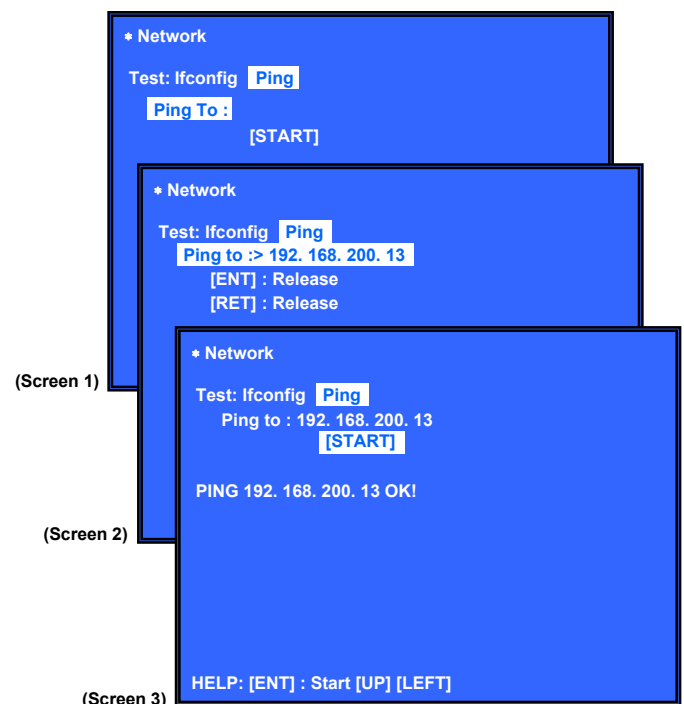
[⊕] Finish to input
[RETURN] Finish to input
[←] Finish to input and Select Ifconfig Test
[0] to [9] Input Character sting ‘0-9’
[TIME] Input Character sting ‘.’
[CLEAR] Backspace

Screen 3: Ping Test Active

When [START] is reversed, the [⊕] button is pressed and execute ping.

Operation:

[⊕] Activate ping test
[↑] The IP address of the ping point is set up
[RETURN] Returns to the top menu of the service mode



15. System Information (System Information Display)

This screen displays system information.

Screen 1: Basic Information

Operation:

[➡] Drive information (delta IOP of a drive is measured) and wireless device information (E385/E390/T39) displayed (go to Screen 2)
[RETURN] Returns to the top menu of the service mode

Screen 2: Drive and Wireless Information Menu

Operation:

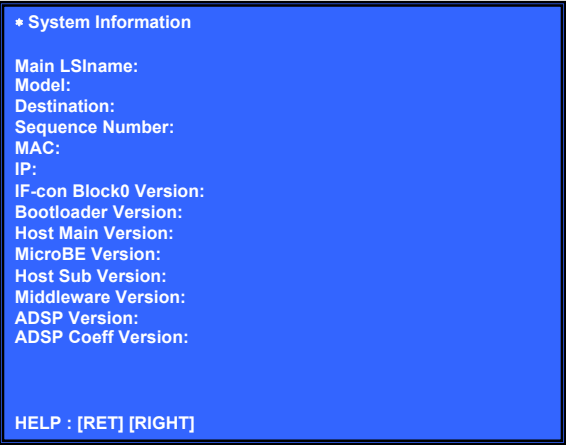
[←] Basic Information displayed (go to Screen 1)
[RETURN] Returns to the top menu of the service mode

When delta IOP is measured, it becomes impossible to use the Version Up function.

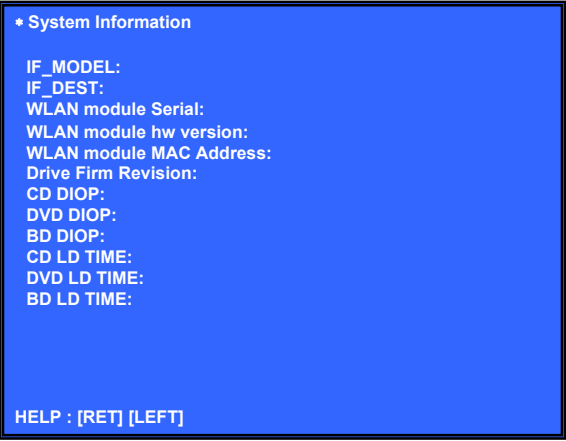
Contents List:

Main LSIname	
Model	
Destination	
Sequence Number	
MAC	
IP	
IFCON	IFCON Version
Bootloader	Bootloader Version
Host Main	Host Main Version
MicroBE	MicroBE Version
Host Sub	Host Sub Version
Middleware	Middleware Version
ADSP	ADSP Version
ADSP Coeff	ADSP Coeff Version
2nd DSP	2nd DSP Version (not used)
IF Model	
IF Dest	
WLAN module Serial	
WLAN module hw Version	
WLAN module MAC Address	
Drive Firm Revision	
CD DIOP	Delta IOP
DVD DIOP	Delta IOP
BD DIOP	Delta IOP
CD LD TIME	LD Time
DVD LD TIME	LD Time
BD LD TIME	LD Time

(Screen 1)



(Screen 2)



16. Drive

This menu is used to operate the drive using drive-related diagnostic and tools.

Screen 1: Selecting items under *Drive

Operation:

- [←]/[→] Selects the category
- [↓]/[⊕] Activate the selected category
- [RETURN] Returns to the top menu of the service mode
- [1] Drive OP data Write
- [2] Servo Parameter Check Menu (Not used)
- [3] Servo Signal Check Menu (Not used)
- [4] S-Curve Check Menu (Not used)
- [5] Readability Check Menu (Not used)
- [6] OP Position Check Menu (Not used)
- [7] OP Check Menu (Not used)
- [8] Load Eject Aging (Not used)
- [9] Spindle Control Check Menu (Not used)
- [10] FA Test Mode (Not used)

Screen 2: To start OP data Write

Operation:

- [⊕] Starts

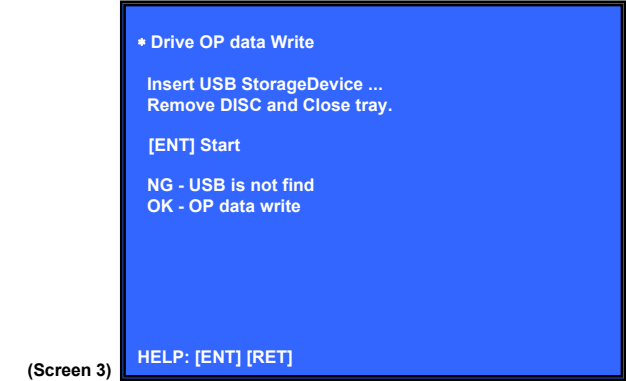
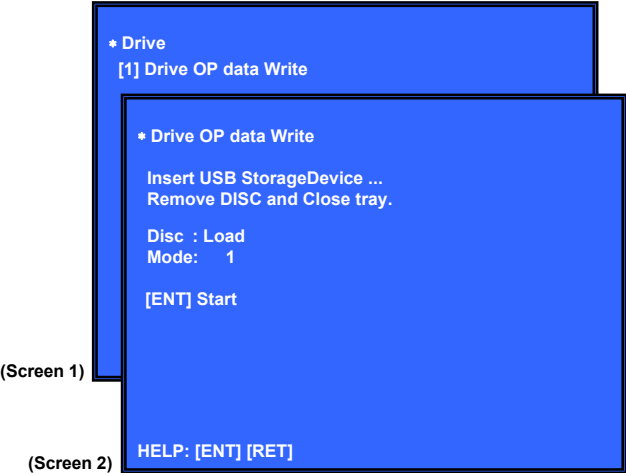
Screen 3: Show OP data Write result

Operation:

- NG - USB is not find
- OK - OP data write

- Drive Test

For drive operating check. Service is only use Drive OP data write to rewrite OP data after change of new CPU.



CONFIRMATION ITEM**1. Playback Operation Confirmation****1-1. Test Disc**

Part No.	Description	Layer
J-6090-199-A	BLX-104	Single Layer
J-6090-200-A	BLX-204	Dual Layer
J-2501-307-A	CD (HLX-A1)	
J-2501-305-A	HLX-513	Single Layer (NTSC)
J-2501-306-A	HLX-514	Dual Layer (NTSC)
J-6090-077-A	HLX-506	Single Layer (PAL)
J-6090-078-A	HLX-507	Dual Layer (PAL)

Note: Refer to the service manual of BDP-BX1/S350 (Part No. 9-883-989-1□) (page 1-3 to 1-14E) for the use of BLX-104/204.

Operation and Display:**1. BLX-104****Procedure:**

1. Select 23.976Hz/1080p.
2. Play "4.Motion picture".
3. Check whether player can play back or not.
4. Check each outputs.
Video:
Composite/S Video/component/HDMI.
Audio:
Speaker out.
- * When 1080/24p monitor is nothing, 1080i (59.94Hz or 50Hz) can use instead of 1080/24p.
However this is temporary correspondence.
- * When the output of HDMI is 1080p, the signal of Composite/S Video/Component are not output.
It is necessary to lower the output of HDMI to 1080i or less.

2. BLX-204**Procedure:**

1. Select 1080i (59.94Hz or 50Hz).
2. Play "4.Motion picture".
3. Check whether player can play back or not.
(Check the picture and sound output)

3. CD (HLX-A1)**Procedure:**

Check whether player can play back or not.
(Check the sound output)

4. HLX-513/514 (NTSC), HLX-506/507 (PAL)**Procedure:**

1. After displayed Main Menu, select "1.Video Signal".
2. Play "1.Color bar 100%".
(Check the picture and sound output)
3. Return to Menu.
4. Play "Demonstration 4:3" or "Demonstration 16:9".
(Check the picture and sound output)

1-2. Playback operation confirmation

Confirm operation in each signal/output mode of test disc (BLX-104/204) according to the content of the repair.

Note: "AV Sync." doesn't operate.

2. Networking Confirmation

Confirm it according to the following procedure when you confirm the connection of the network.

Note: Do not execute "Network Connection Diagnostics" of "Network Settings" of the home menu with only the router connected.

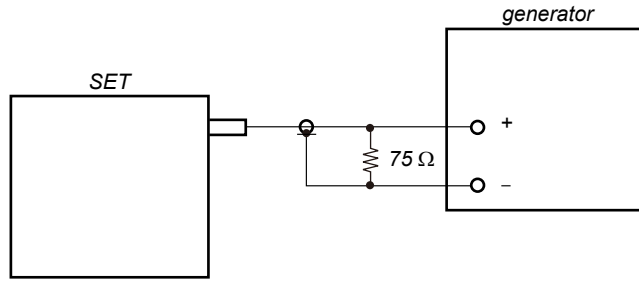
Procedure:

1. Connect the router with the unit with LAN cable.
2. Turn on the power of the unit and the router.
3. Press the [HOME] button on the remote commander, and the home menu is displayed.
4. Select "Setup" → "Network Settings" → "Internet Settings", and press the [⊕] button on the remote commander.
5. Select "View Networks Status" and press the [⊕] button on the remote commander.
6. Confirm IP address are displayed in "IP Address", "Subnet Mask" and "Default Gateway".

Physical Connection:	XXXX
Internet Access:	XXXX
IP Address Setting:	XXXX
IP Address:	XXX.XXX.XXX.XXX
Subnet Mask:	XXX.XXX.XXX.XXX
Default Gateway:	XXX.XXX.XXX.XXX
DNS Settings:	XXXX
Primary DNS:	XXX.XXX.XXX.XXX
Secondary DNS:	XXX.XXX.XXX.XXX
MAC Address:	XXX.XXX.XXX.XXX

SECTION 4 ELECTRICAL CHECK

FM AUTO STOP CHECK



Procedure:

1. Turn the power on.
2. Input the following signal from Signal Generator to FM antenna input directly.

Carrier frequency : A = 87.5 MHz, B = 98 MHz, C = 108 MHz

Deviation : 75 kHz

Modulation : 1 kHz

ANT input : 35 dBu (EMF)

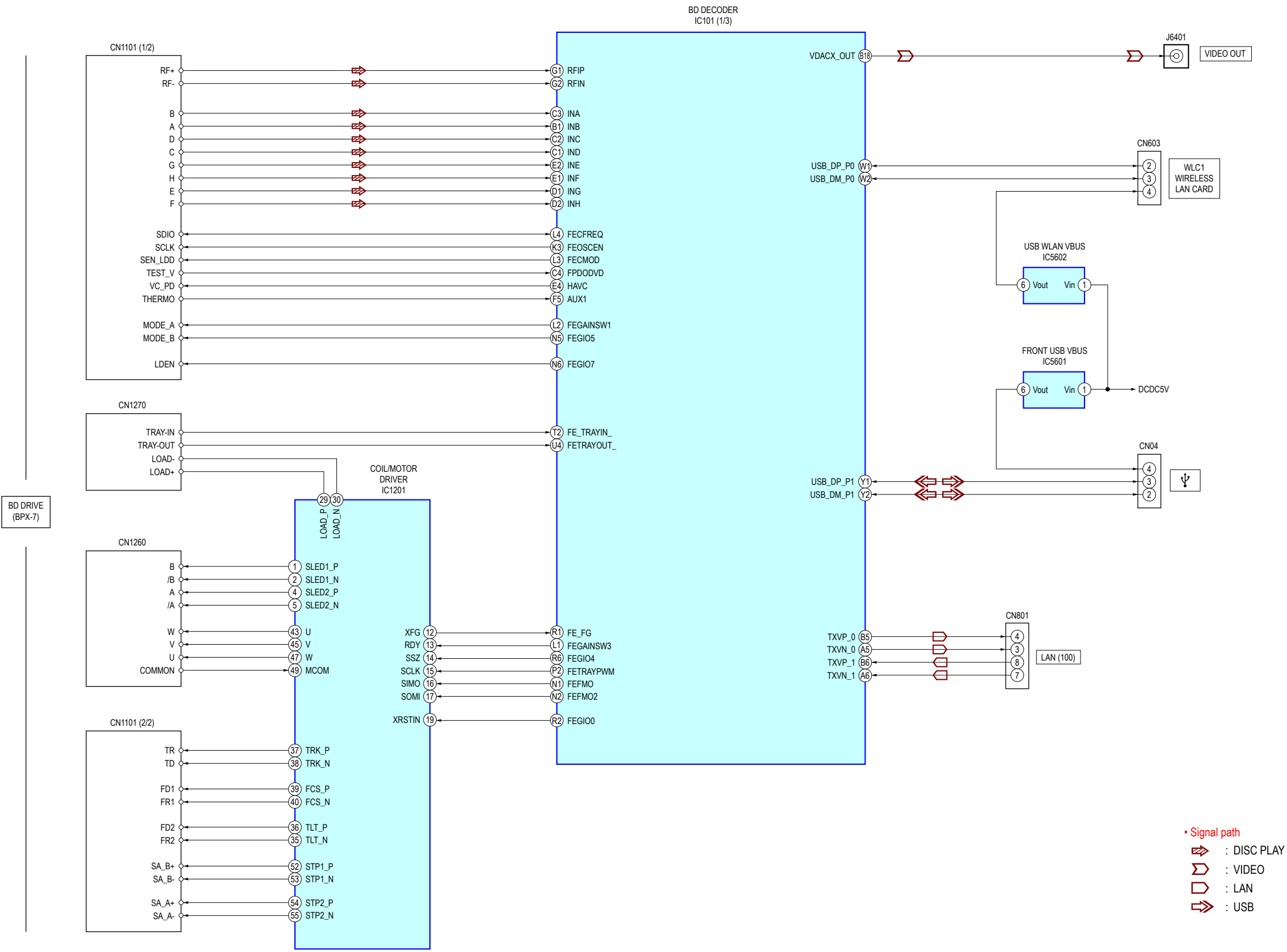
Note: Please use 75 ohm “coaxial cable” to connect SG and the set. You cannot use video cable for checking.
Please use SG whose output impedance is 75 ohm.

3. Set to FM tuner function and scan the input FM signal with automatic scanning.
4. Confirm that input Frequency of A, B and C detected and automatic scanning stops.

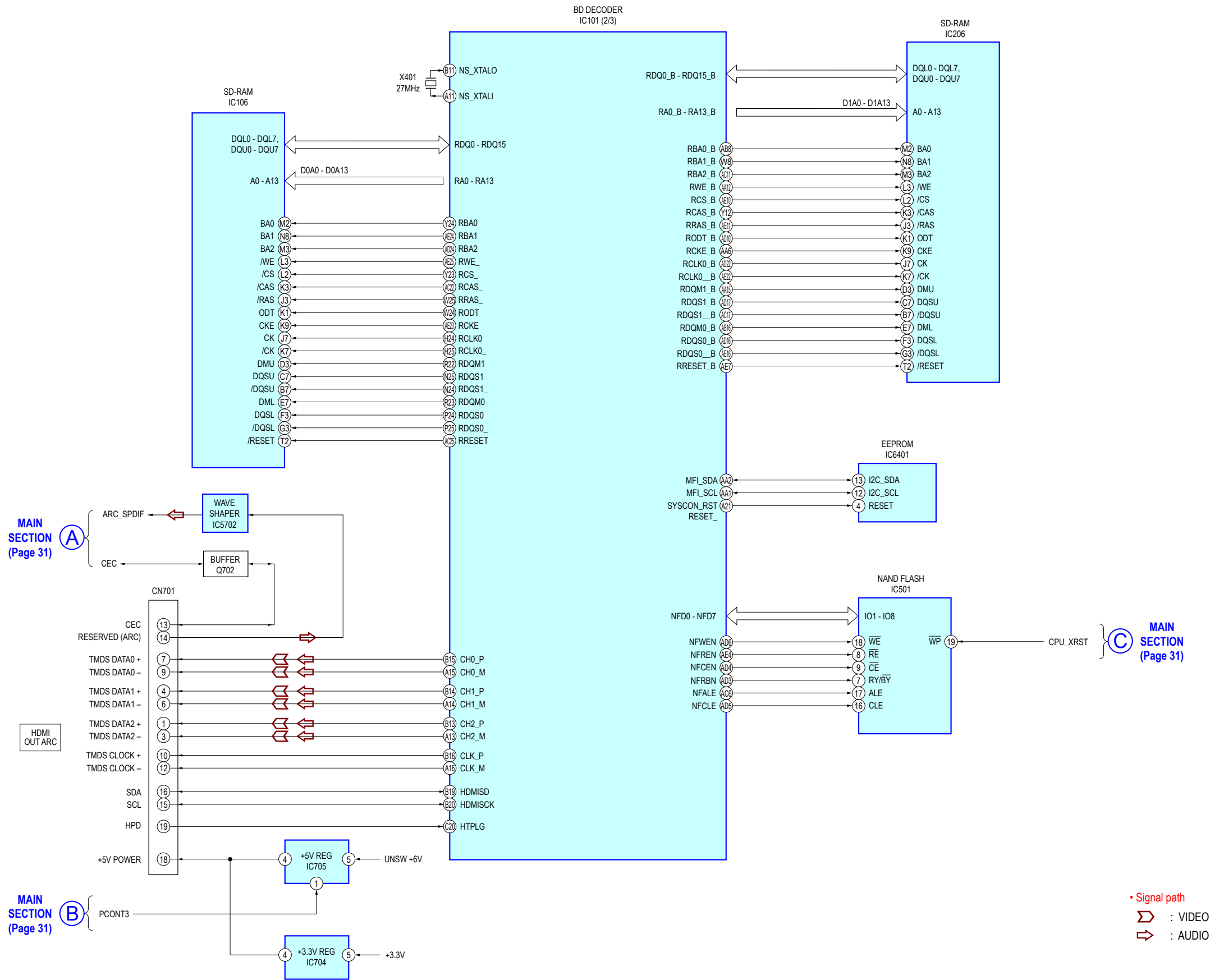
The stop of automatic scanning means “The station signal is received in good condition”.

SECTION 5
DIAGRAMS

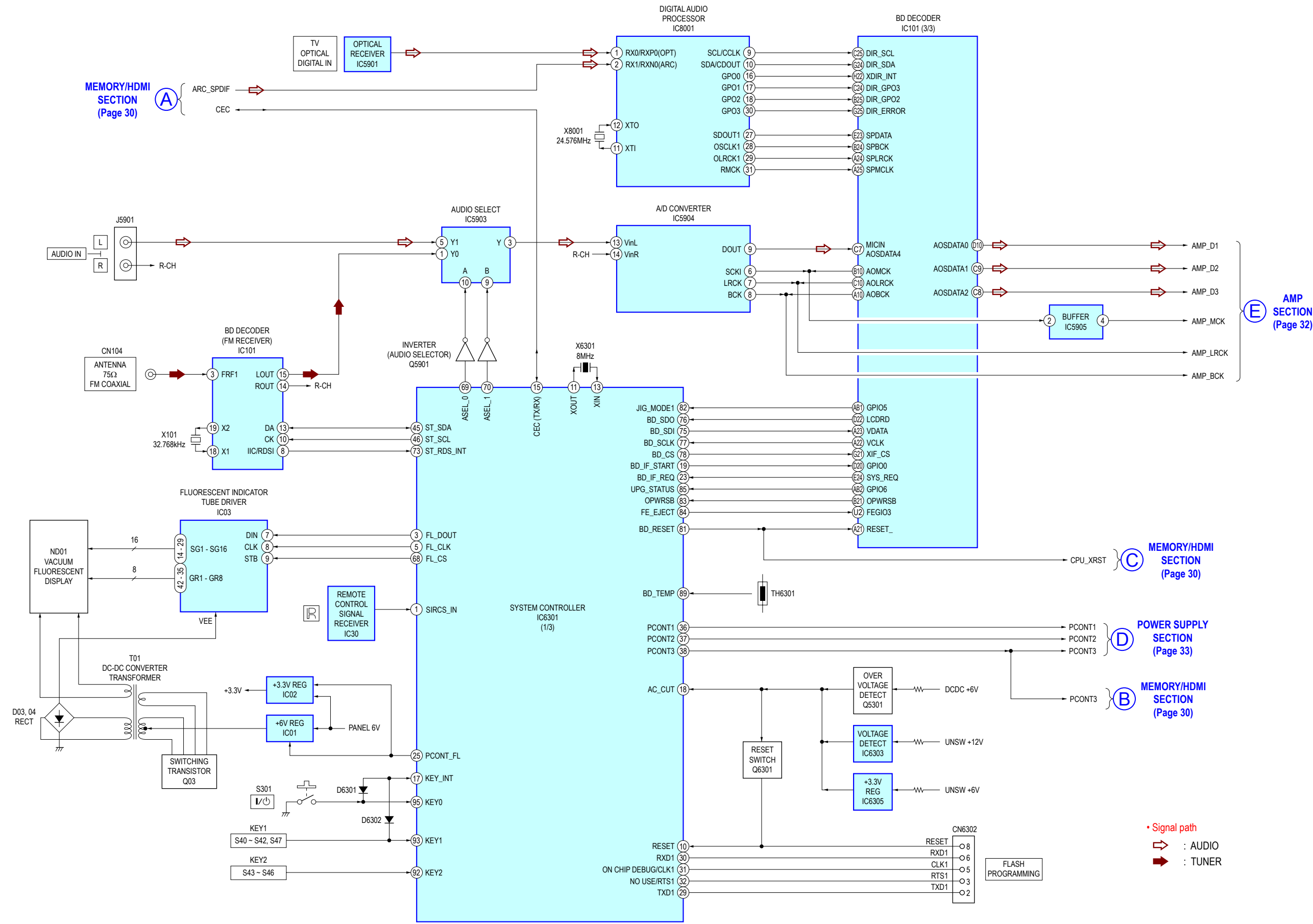
5-1. BLOCK DIAGRAM - SERVO Section -



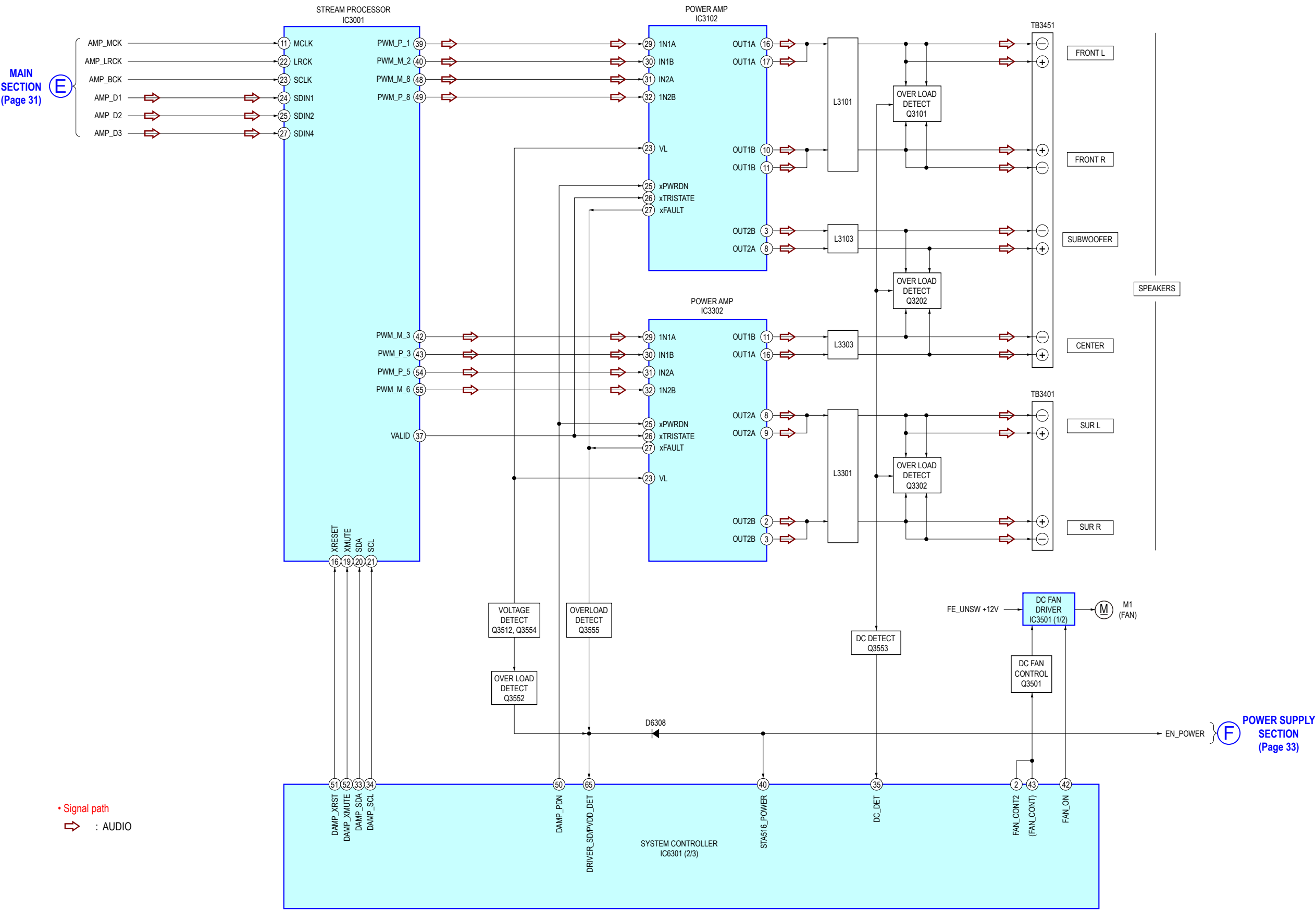
5-2. BLOCK DIAGRAM - MEMORY/HDMI Section -



5-3. BLOCK DIAGRAM - MAIN Section -



5-4. BLOCK DIAGRAM - AMP Section -



THIS NOTE IS COMMON FOR PRINTED WIRING BOARDS AND SCHEMATIC DIAGRAMS.
(In addition to this, the necessary note is printed in each block.)

For Printed Wiring Boards.

Note:

- — : Parts extracted from the component side.
- : Parts extracted from the conductor side.
- △ : Internal component.
- : Pattern from the side which enables seeing.
(The other layers' patterns are not indicated.)

Caution:

Pattern face side: Parts on the pattern face side seen (Conductor Side) from the pattern face are indicated.

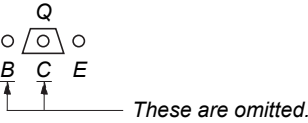
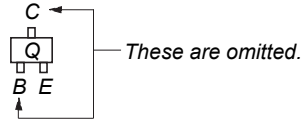
Parts face side: Parts on the parts face side seen from (Component Side) the parts face are indicated.

Caution:

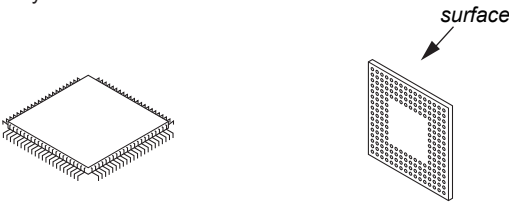
Pattern face side: Parts on the pattern face side seen (SIDE B) from the pattern face are indicated.

Parts face side: Parts on the parts face side seen from (SIDE A) the parts face are indicated.

- MB148 board is multi-layer printed board. However, the patterns of intermediate-layers have not been included in this diagrams.
- Abbreviation
CND : Canadian model
- Indication of transistor.



- Lead layouts



Lead layout of conventional IC CSP (Chip Size Package)

* Replacement of IC106 and IC206 on the MB148 board used in this unit requires a special tool.

Note: When the AMP board is replaced, spread the compound referring to “NOTE OF REPLACING THE IC3102 AND IC3302 ON THE AMP BOARD AND THE COMPLETE AMP BOARD” on servicing notes (page 10).

For Schematic Diagrams.

Note:

- All capacitors are in μF unless otherwise noted. (p: pF) 50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and 1/4 W or less unless otherwise specified.
- △ : internal component.
- □ : panel designation.

Note: The components identified by mark △ or dotted line with mark △ are critical for safety. Replace only with part number specified.

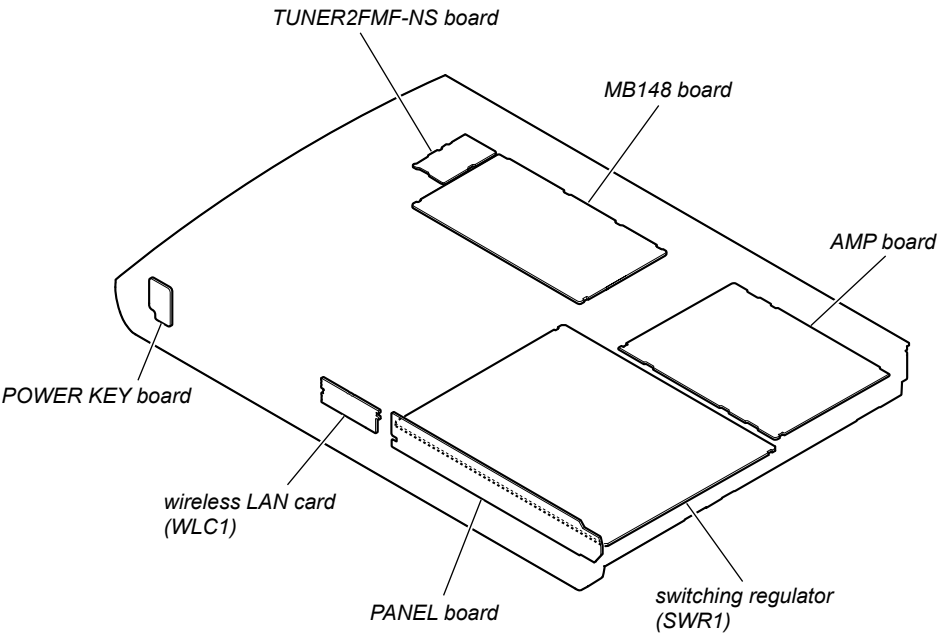
Note: Les composants identifiés par une marque △ sont critiques pour la sécurité. Ne les remplacer que par une pièce portant le numéro spécifié.

- — : B+ Line.
- - - - : B- Line.
- Voltages and waveforms are dc with respect to ground under no-signal conditions.
no mark: TUNER
* : Impossible to measure
- Voltages are taken with VOM (Input impedance 10 M Ω).
Voltage variations may be noted due to normal production tolerances.
- Waveforms are taken with a oscilloscope.
Voltage variations may be noted due to normal production tolerances.
- Circled numbers refer to waveforms.
- Signal path.
⇒ : AUDIO
➡ : TUNER
⇒ : USB
⇒ : VIDEO
⇒ : LAN
⇒ : DISC PLAY
- Abbreviation
CND : Canadian model
- The voltage and waveform of CSP (chip size package) cannot be measured, because its lead layout is different from that of conventional IC.

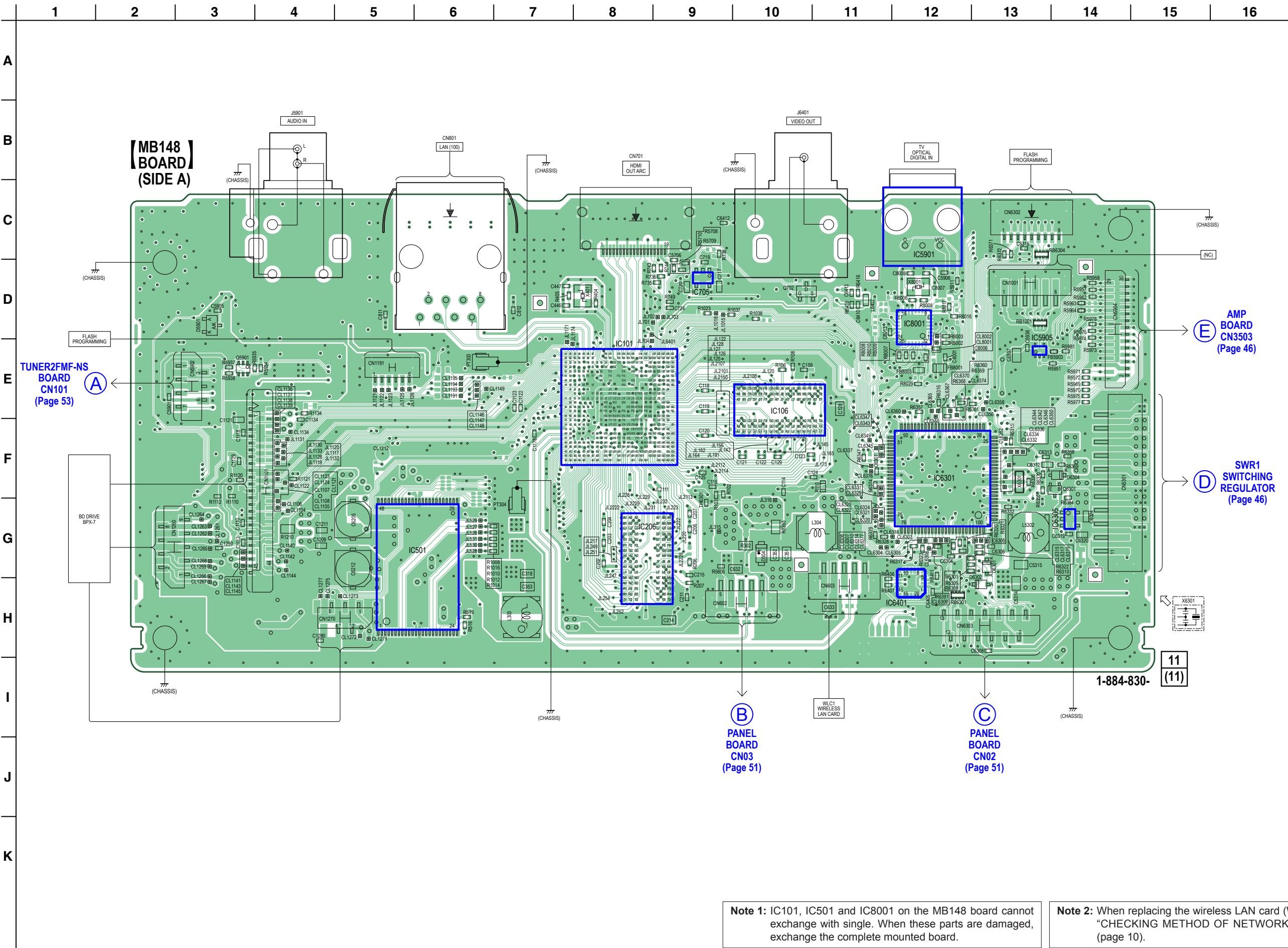
* Replacement of IC106 and IC206 on the MB148 board used in this unit requires a special tool.

Note: When the AMP board is replaced, spread the compound referring to “NOTE OF REPLACING THE IC3102 AND IC3302 ON THE AMP BOARD AND THE COMPLETE AMP BOARD” on servicing notes (page 10).

• Circuit Boards Location



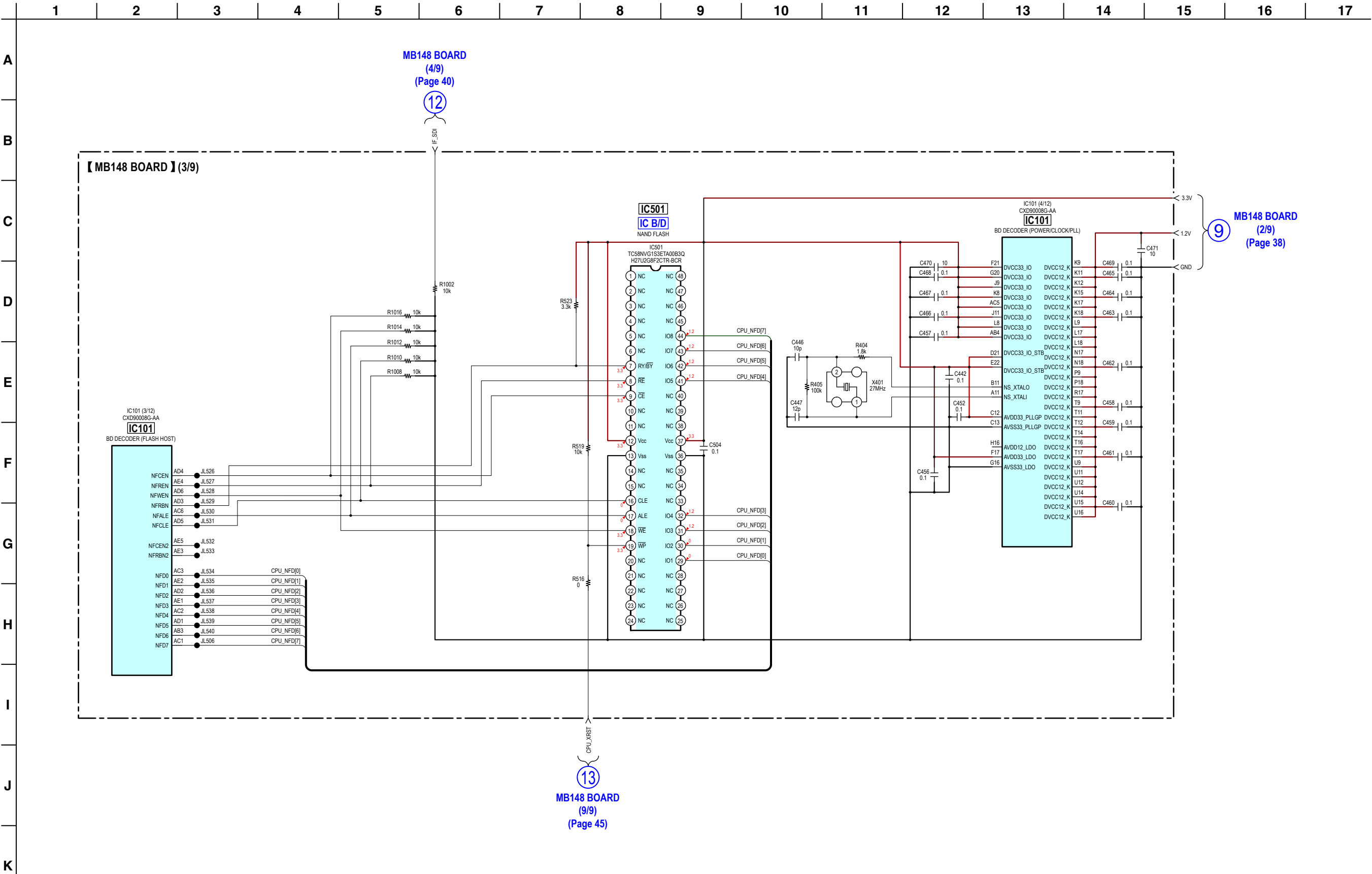
5-6. PRINTED WIRING BOARD - MB148 Board (Side A) - • See page 34 for Circuit Boards Location. • : Uses unleaded solder.



Note 1: IC101, IC501 and IC8001 on the MB148 board cannot exchange with single. When these parts are damaged, exchange the complete mounted board.

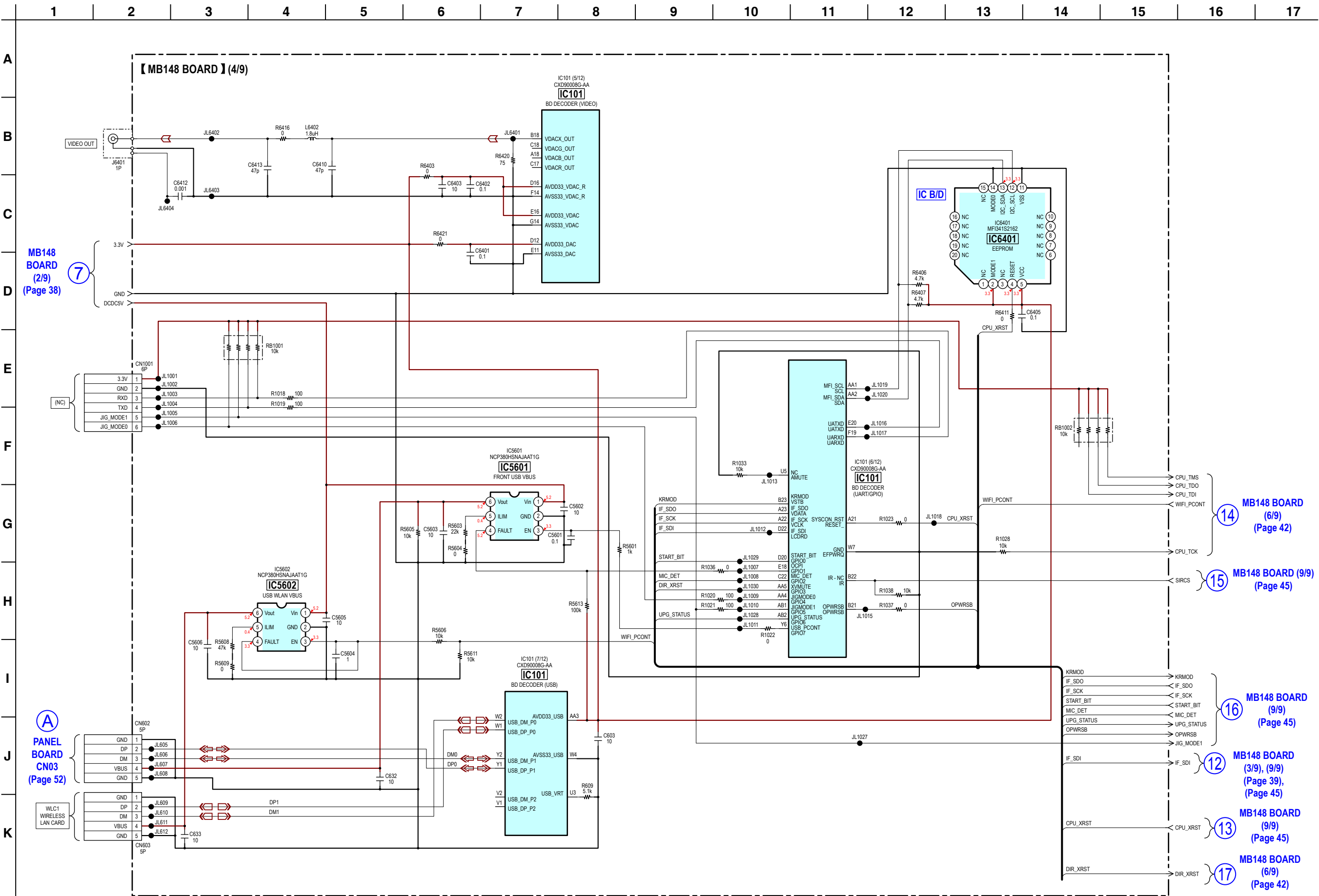
Note 2: When replacing the wireless LAN card (WLC1), refer to "CHECKING METHOD OF NETWORK OPERATION" (page 10).

5-10. SCHEMATIC DIAGRAM - MB148 Board (3/9) - • See page 55 for IC Block Diagrams. • See page 61 for IC Pin Function Descriptions.



Note: IC101 and IC501 on the MB148 board cannot exchange with single. When these parts are damaged, exchange the complete mounted board.

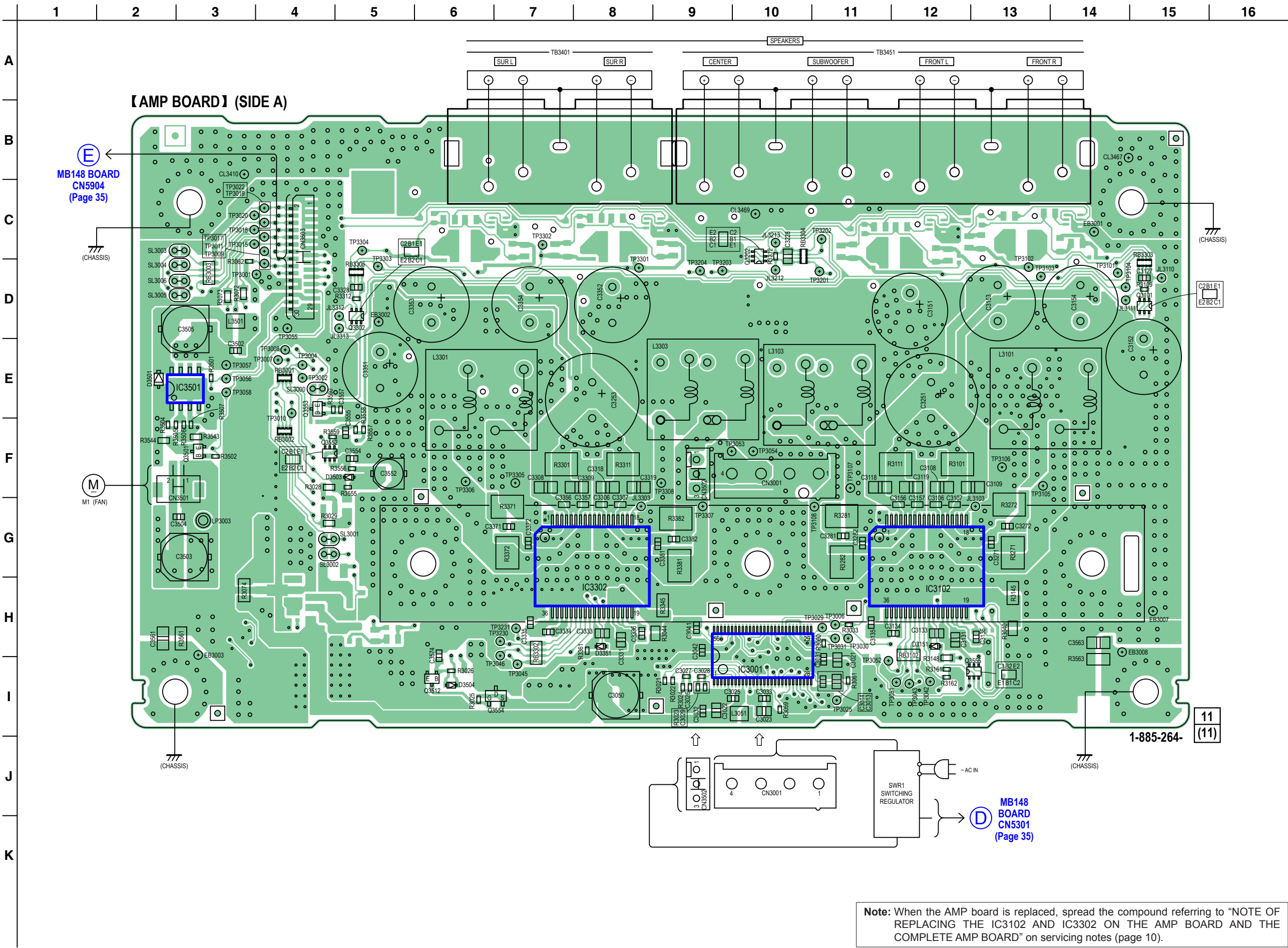
5-11. SCHEMATIC DIAGRAM - MB148 Board (4/9) - • See page 55 for IC Block Diagrams. • See page 61 for IC Pin Function Descriptions.



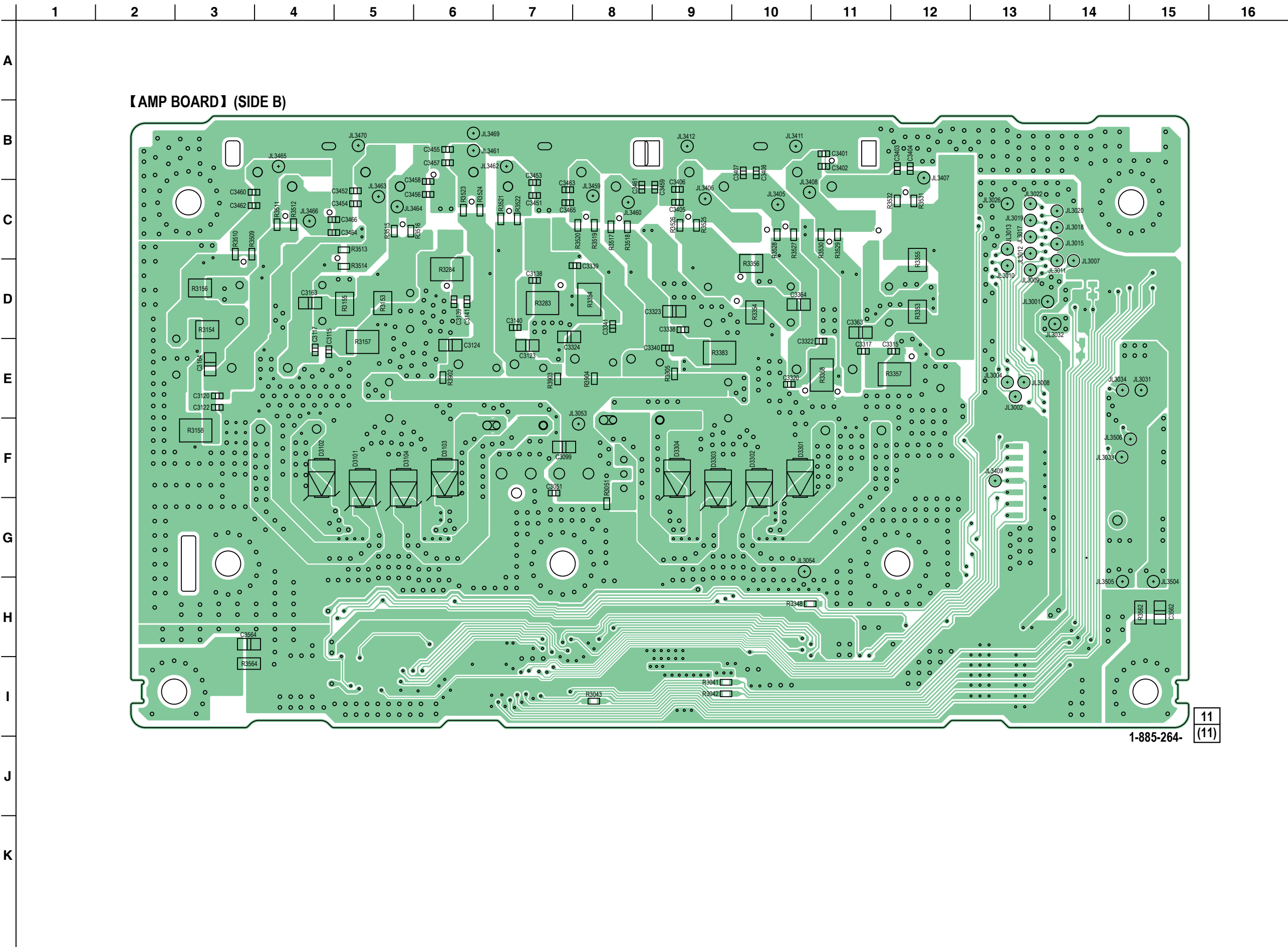
Note 1: IC101 on the MB148 board cannot exchange with single. When this part is damaged, exchange the complete mounted board.

Note 2: When replacing the wireless LAN card (WLC1), refer to "CHECKING METHOD OF NETWORK OPERATION" (page 10).

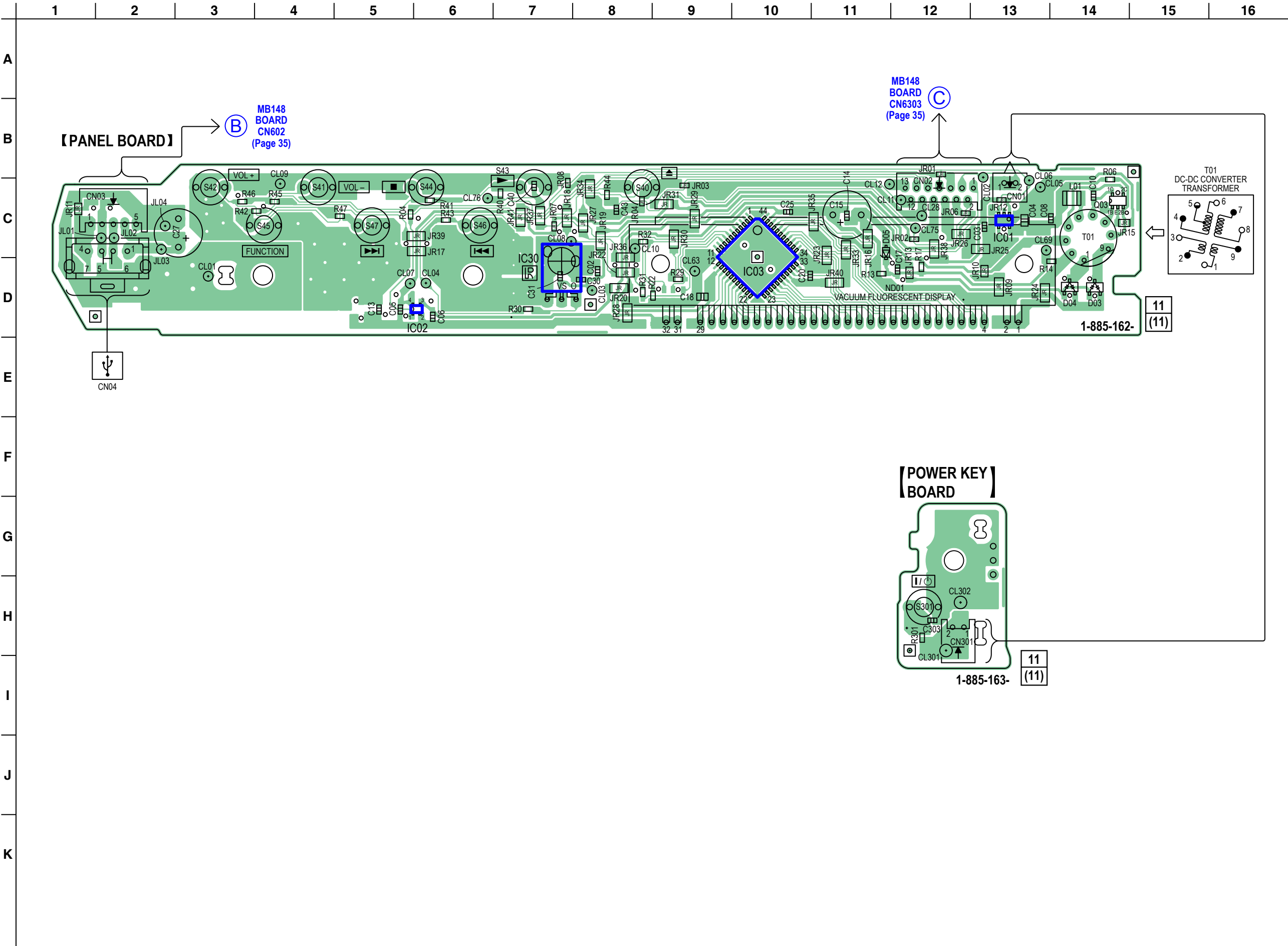
5-17. PRINTED WIRING BOARD - AMP Board (Side A) - • See page 34 for Circuit Boards Location. • : Uses unleaded solder.



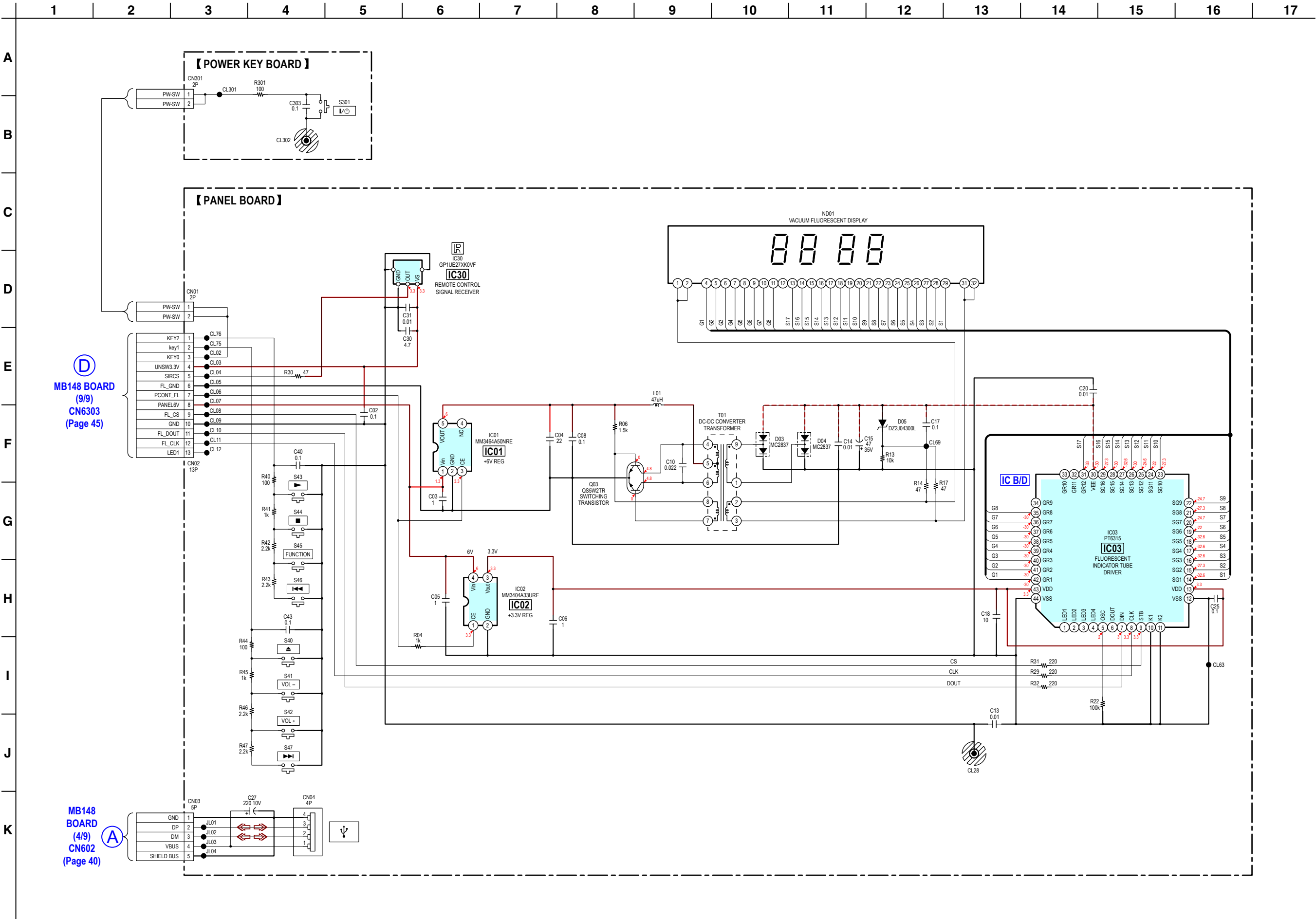
5-18. PRINTED WIRING BOARD - AMP Board (Side B) - • See page 34 for Circuit Boards Location. • : Uses unleaded solder.



5-22. PRINTED WIRING BOARDS - PANEL Section - • See page 34 for Circuit Boards Location. • : Uses unleaded solder.

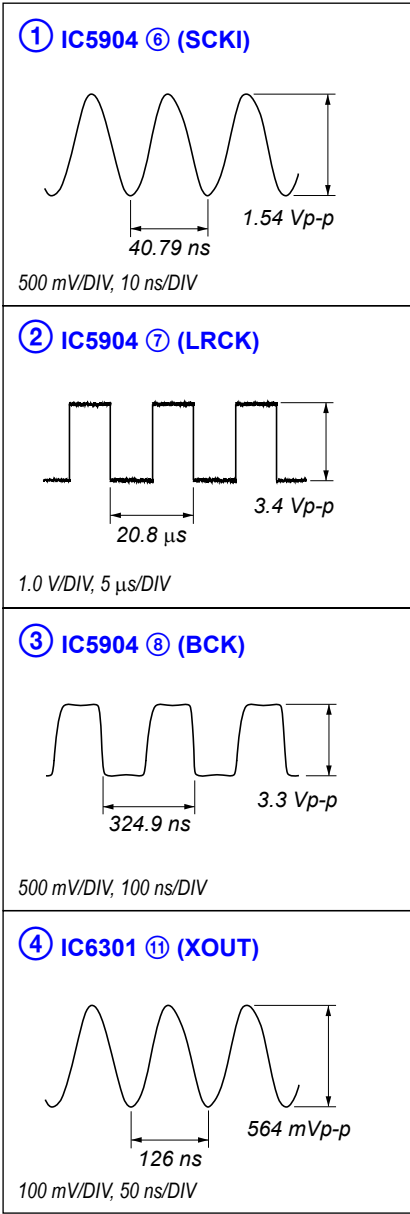


5-23. SCHEMATIC DIAGRAM - PANEL Section - • See page 55 for IC Block Diagrams.



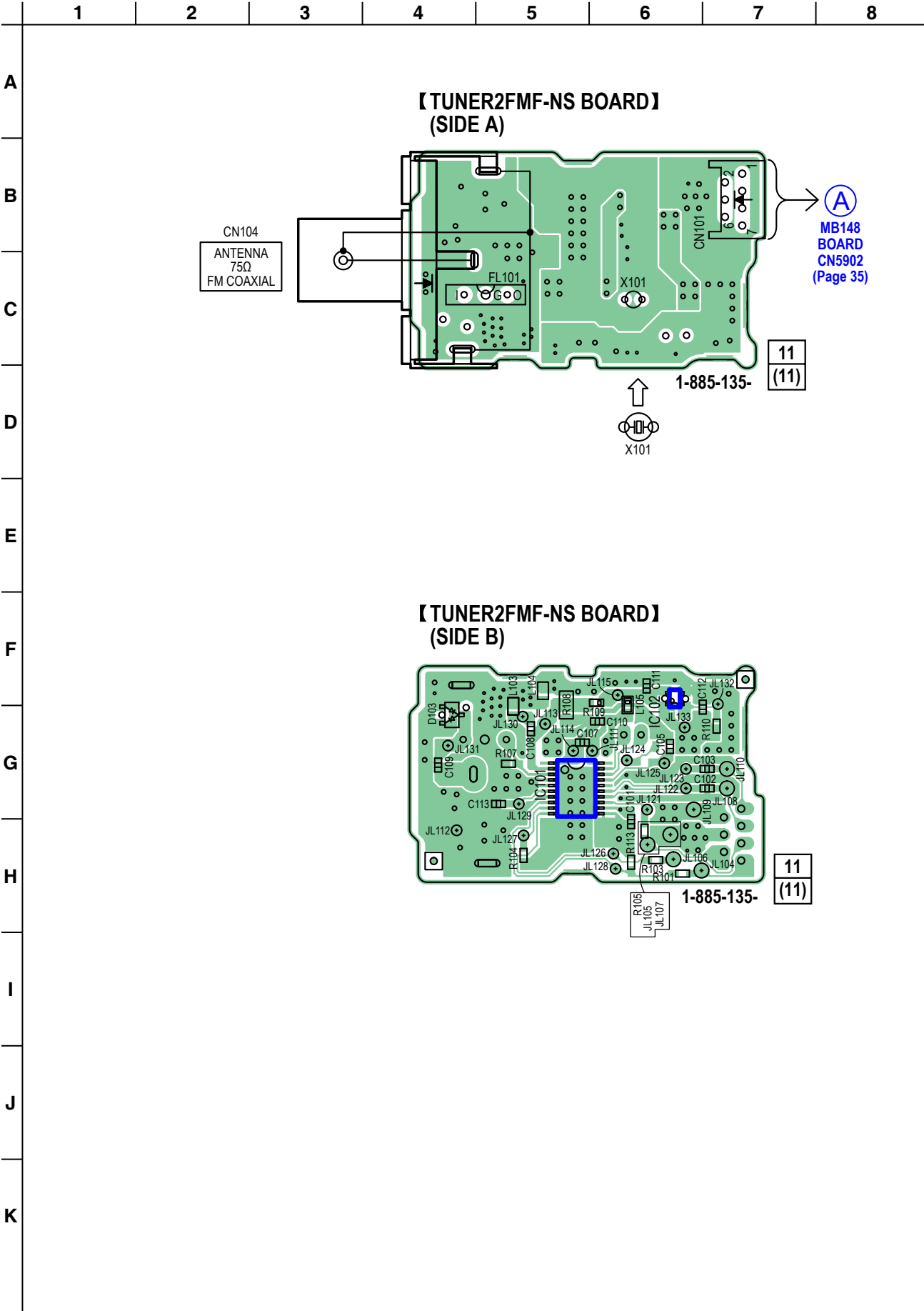
• Waveforms

– MB148 Board –

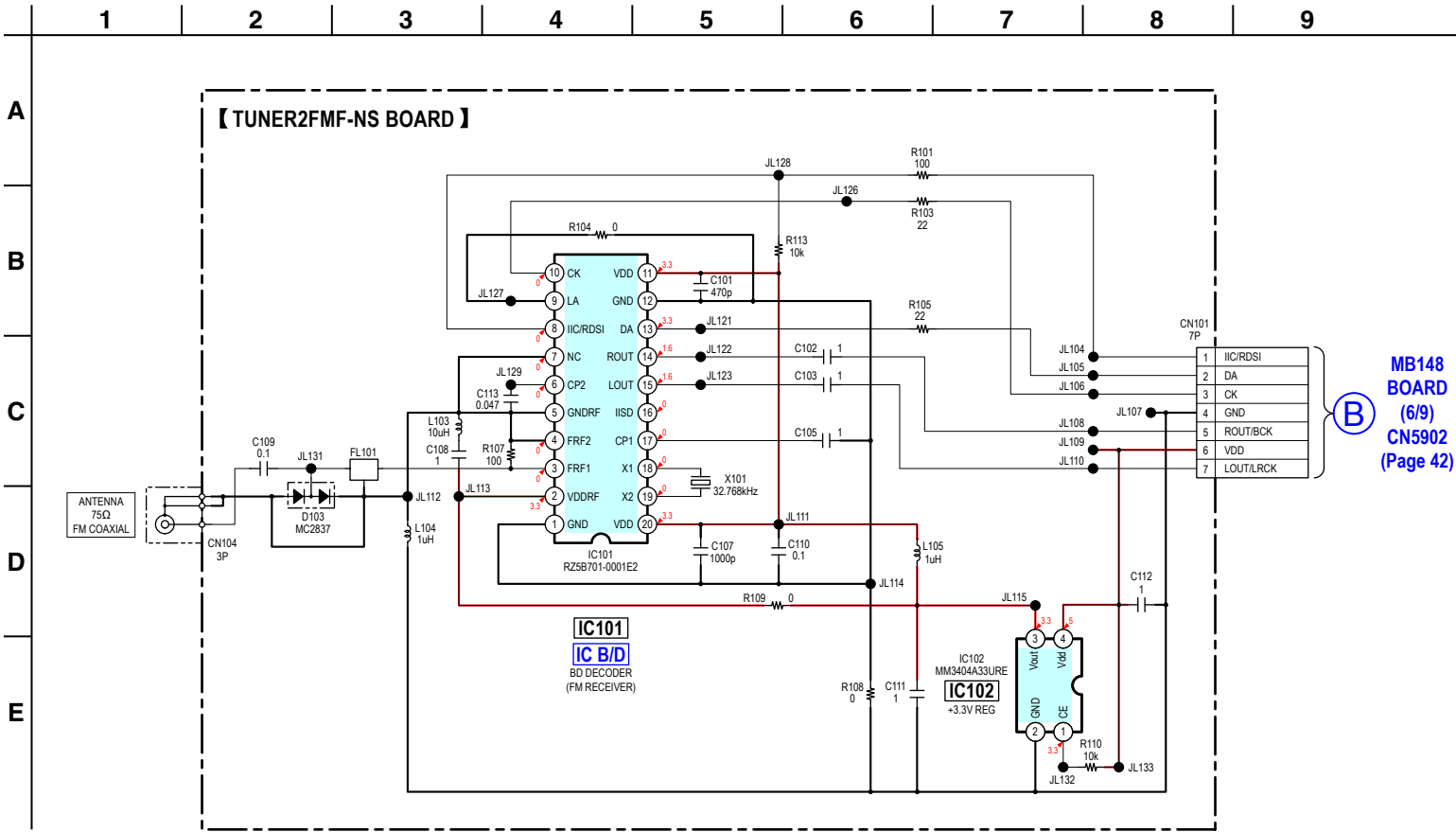


5-24. PRINTED WIRING BOARD - TUNER2FMF-NS Board -

• See page 34 for Circuit Boards Location. • : Uses unleaded solder.

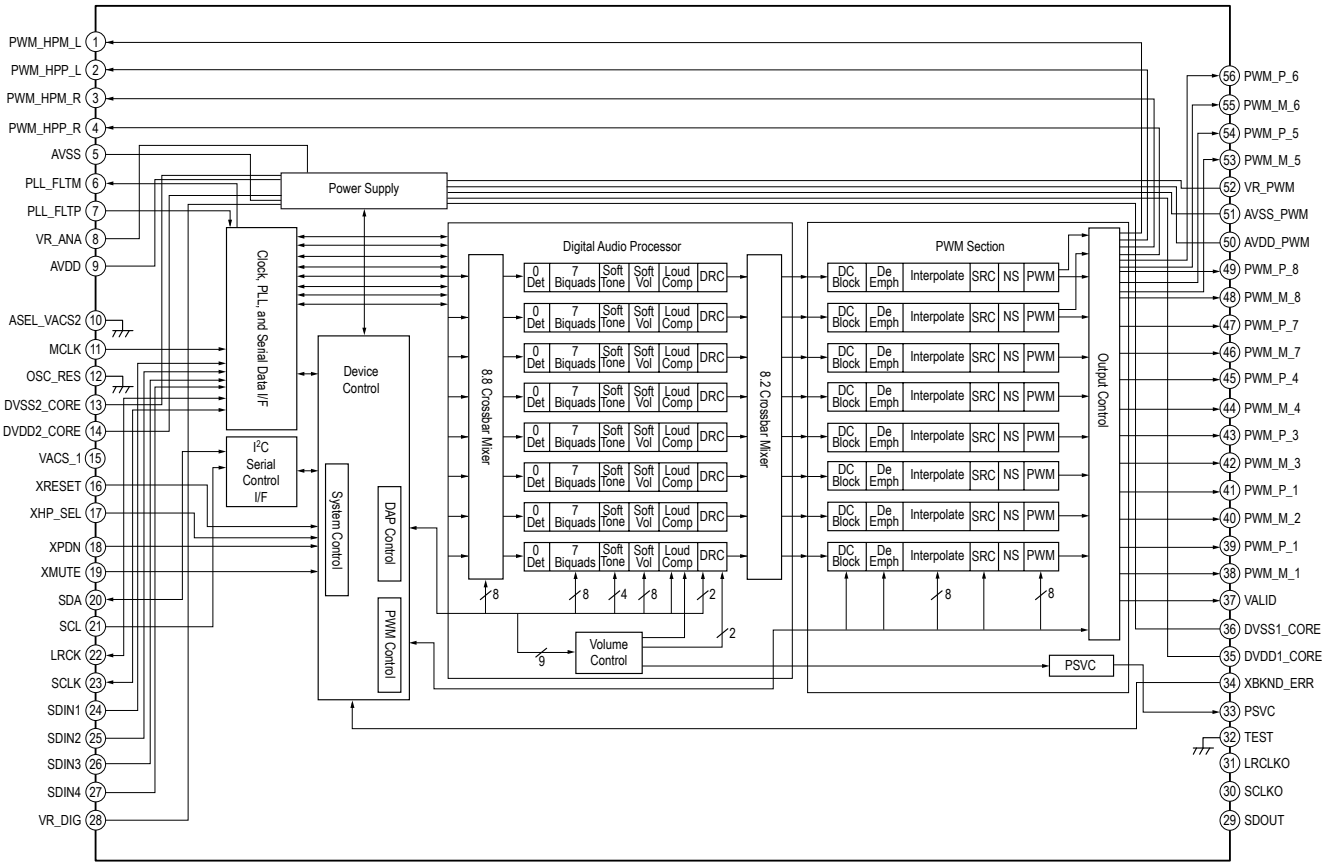


5-25. SCHEMATIC DIAGRAM - TUNER2FMF-NS Board - • See page 55 for IC Block Diagrams.

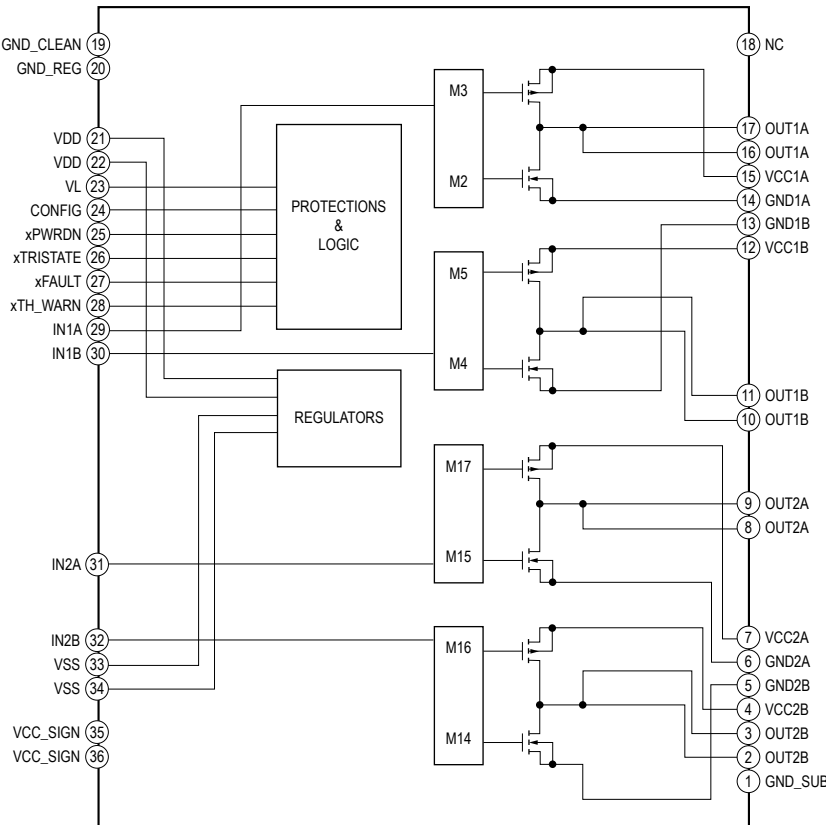


• IC Block Diagrams

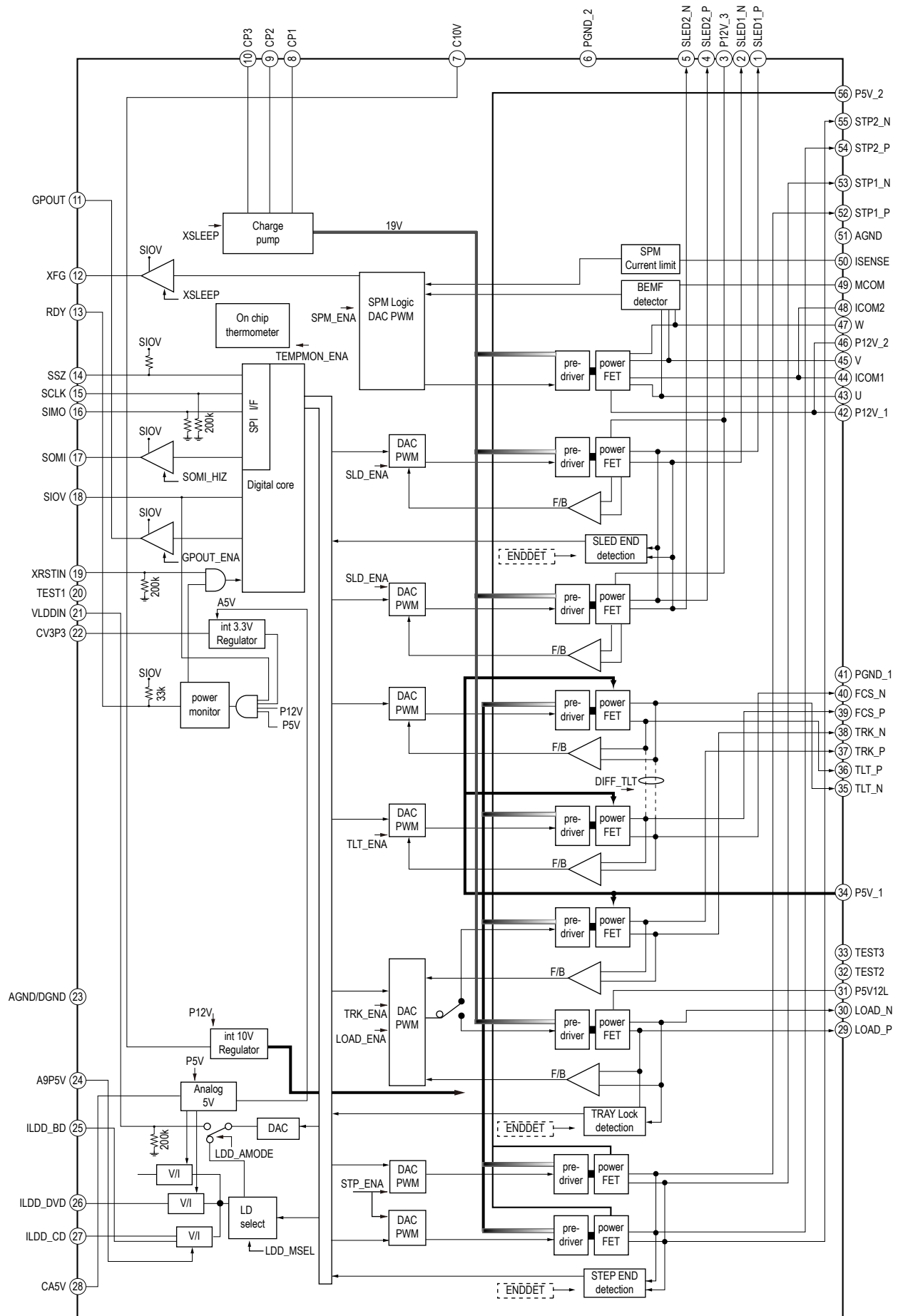
IC3001 TAS5538DGGR (AMP BOARD (1/3))



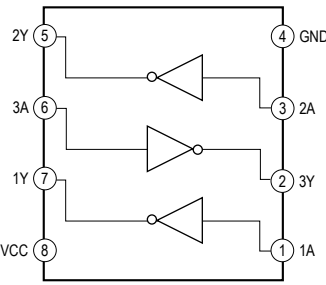
IC3102 STA516B13TR-A (AMP BOARD (3/3))
IC3302 STA516B13TR-A (AMP BOARD (2/3))



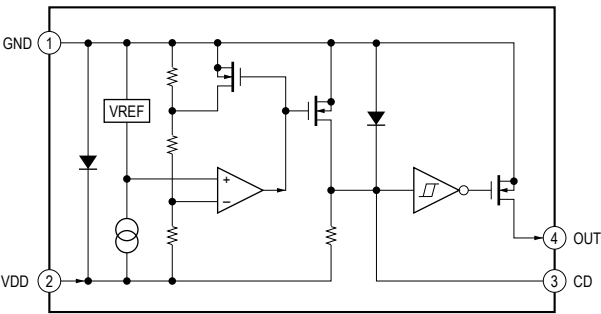
IC1201 TPIC2050RDFDRG4 (MB148 BOARD (8/9))



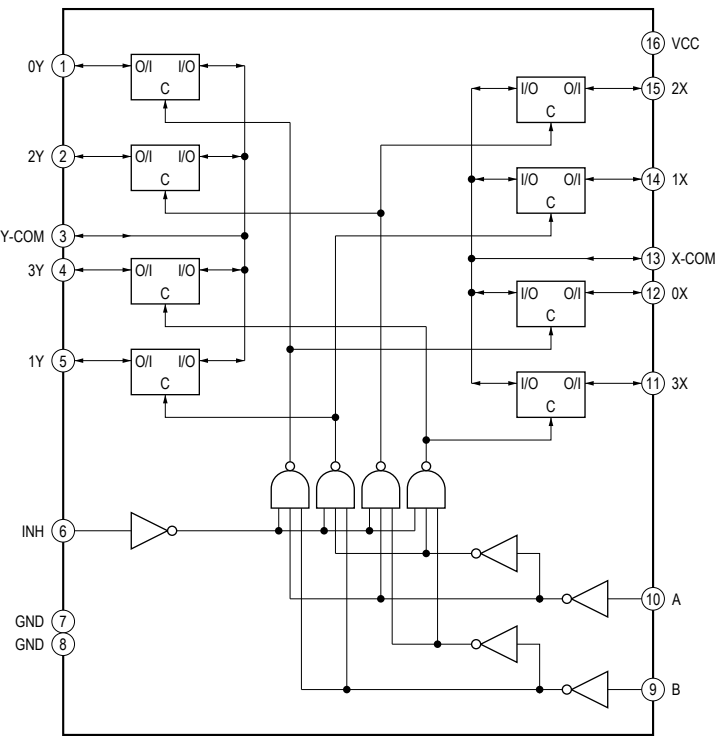
IC5702 TC7WHU04FK (MB148 BOARD (5/9))



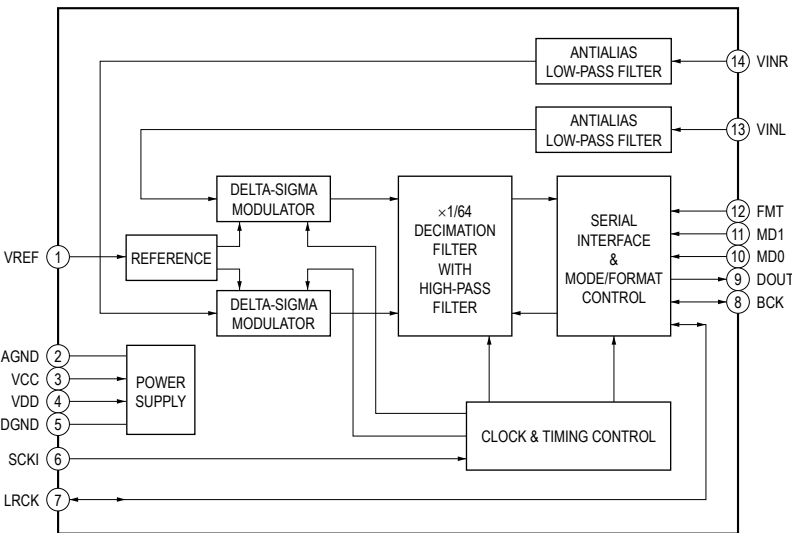
IC6303 PST8429UL (MB148 BOARD (9/9))



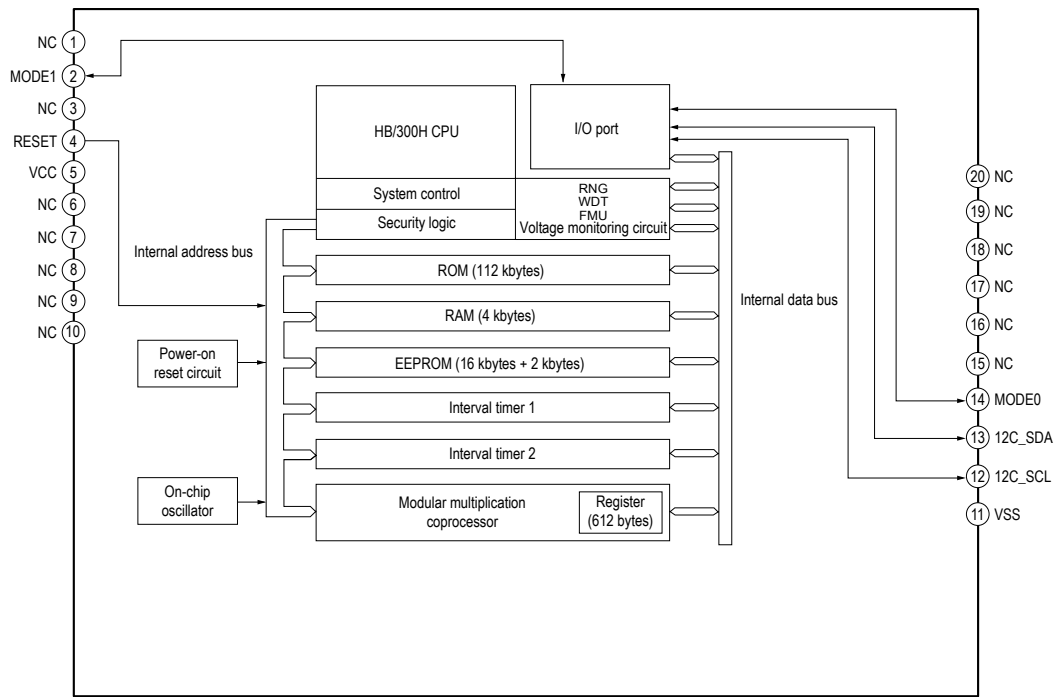
IC5903 TC74VHC4052AFT (EK) (MB148 BOARD (6/9))



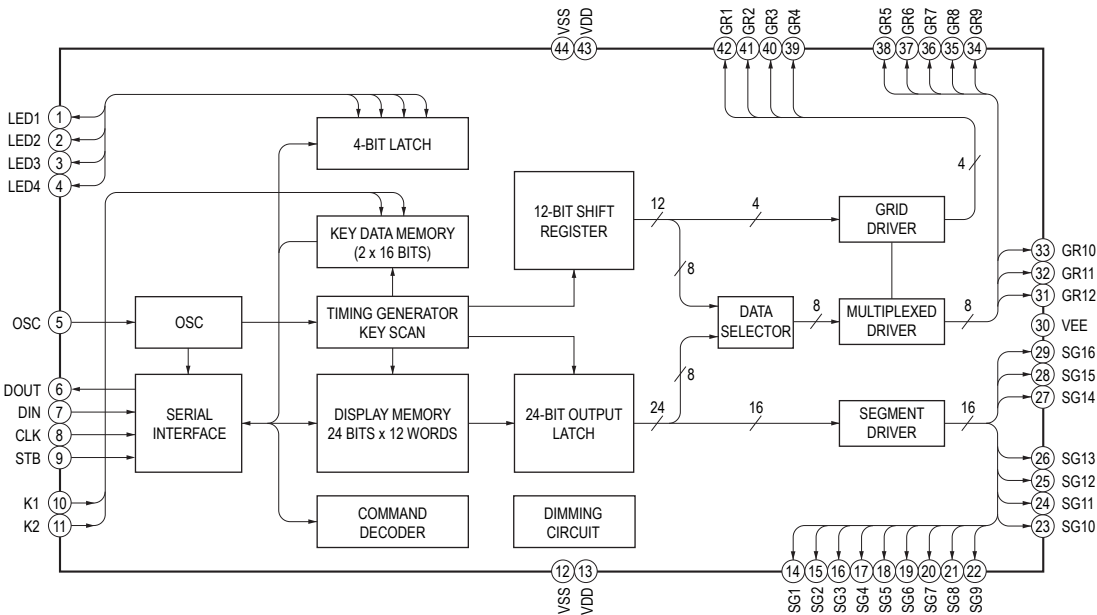
IC5904 PCM1808PWR (MB148 BOARD (6/9))



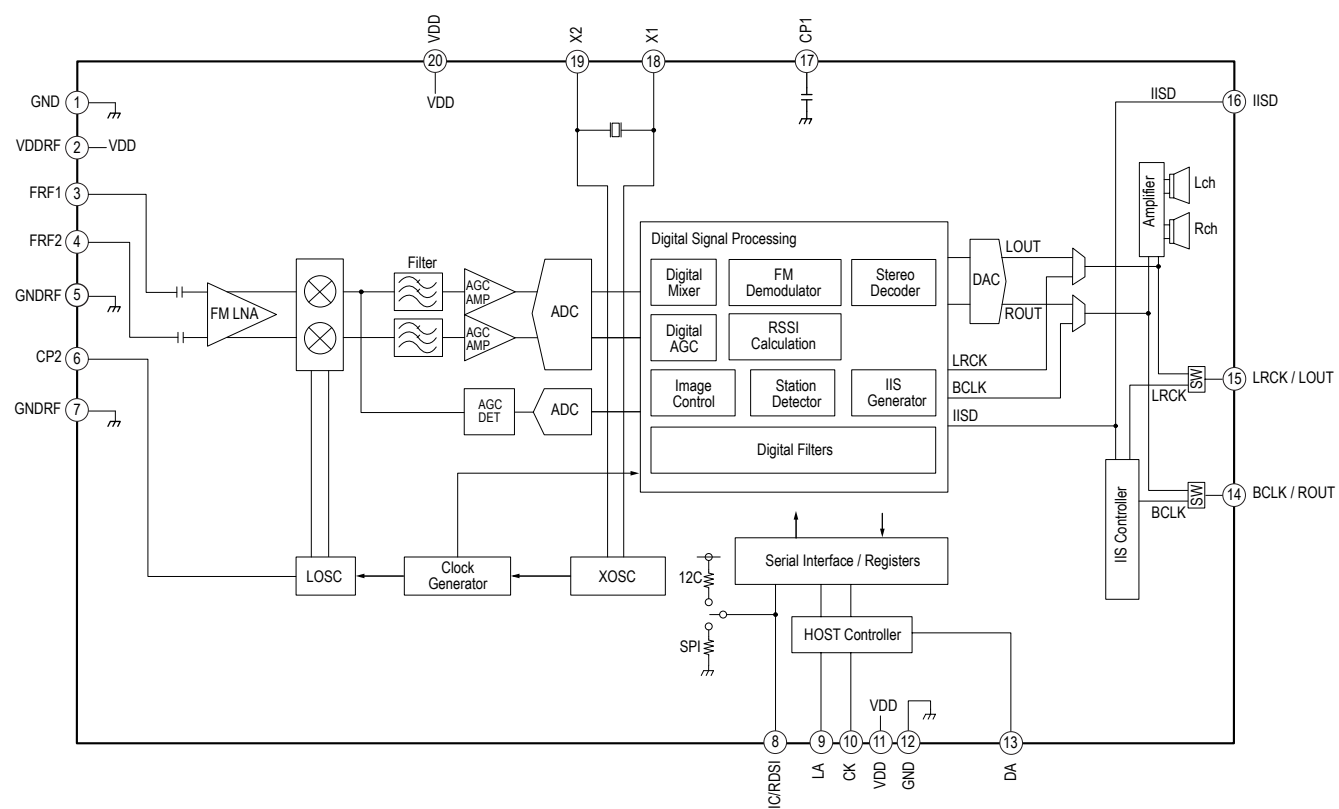
IC6401 MFI341S2162 (MB148 BOARD (4/9))



IC03 PT6315 (PANEL BOARD)



IC101 RZ5B701-0001E2 (TUNER2FMF-NS BOARD)



• IC Pin Function Descriptions

MB148 BOARD (1/9), (3/9), (4/9), (5/9), (6/9), (7/9) IC101 CXD90008G-AA (BD DECODER)

Pin No.	Pin Name	I/O	Description
A1	TRINC	I	Input of Tracking Signal (C)
A2	TRINA	I	Input of Tracking Signal (A)
A3	FPDOCD	I	Laser Power Monitor Input for CD APC / Differential negative input
A4	VWDC3O	O	Output Voltage 3 of Laser Diode Control in APC
A5	TXVN_0	I/O	Ethernet TD -
A6	TXVN_1	I/O	Ethernet RD -
A7	SPDIF	I/O	SPDIF digital audio output
A8	NO_USE	-	Not used
A9	NO_USE	-	Not used
A10	AOBCK ALO	I/O	Audio output bit clock
A11	NS_XTALI	-	3.3V power for standby
A12	NO_USE	-	Not used
A13	CH2_M	O	HDMI TX data 2 differential pair (M)
A14	CH1_M	O	HDMI TX data 1 differential pair (M)
A15	CH0_M	O	HDMI TX data 0 differential pair (M)
A16	CLK_M	O	HDMI TX clock differential pair (M)
A17	NO_USE	-	Not used
A18	VDACG_OUT	O	DAC output
A19	CEC	I/O	HDMI CEC
A20	NO_USE	-	Not used
A21	RESET_	I	Power on reset
A22	IF_SCK VCLK	I/O	VFD clock
A23	IF_SDO VDATA	I/O	VFD data
A24	SPLRCK	I/O	SPDIF input left-right clock
A25	SPMCLK	I/O	SPDIF input master clock
B1	INB	I	Input of Main Beam Signal (B)
B2	TRIND	I	Input of Tracking Signal (D)
B3	TRINB	I	Input of Tracking Signal (B)
B4	FVREF	O	Output of Voltage Reference
B5	TXVP_0	I/O	Ethernet TD +
B6	TXVP_1	I/O	Ethernet RD +
B7	REXT	O	External reference resistor
B8	NO_USE	-	Not used
B9	NO_USE	-	Not used
B10	AOMCL ARO	I/O	Audio output master clock
B11	NS_XTALO	I	27MHz Crystal In
B12	NO_USE	-	Not used
B13	CH2_P	O	HDMI TX data 2 differential pair (P)
B14	CH1_P	O	HDMI TX data 1 differential pair (P)
B15	CH0_P	O	HDMI TX data 0 differential pair (P)
B16	CLK_P	O	HDMI TX clock differential pair (P)
B17	NO_USE	-	Not used
B18	VDACX_OUT	O	DAC output
B19	HDMISD	I/O	HDMI I2C data
B20	HDMISCK	I/O	HDMI I2C clock
B21	OPWRSB	I/O	Power Control
B22	NC IR	I/O	Infrared input
B23	KRMOD VSTB	I/O	GPIO

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Pin No.	Pin Name	I/O	Description
B24	SPBCK	I/O	SPDIF input bit clock
B25	DIR_GPO2	I/O	GPIO
C1	IND	I	Input of Main Beam Signal (D)
C2	INC	I	Input of Main Beam Signal (C)
C3	INA	I	Input of Main Beam Signal (A)
C4	FPDODVD	I	Laser Power Monitor Input for DVD APC / Differential positive input
C5	NO_USE	-	Not used
C6	VWDD2O	O	Output Voltage 2 of Laser Diode Control in APC
C7	MICIN AOSDATA4	I/O	Audio line input data
C8	AOSDATA2	I/O	Audio output serial data 2
C9	AOSDATA1	I/O	Audio output serial data 1
C10	AOLRCK	I/O	Audio output left-right clock
C11	NO_USE	-	Not used
C12	AVDD33_PLLGP	-	3.3 V Analog Power for PLL Group
C13	AVSS33_PLLGP	-	Analog Ground for PLL Group
C14	NO_USE	-	Not used
C15	AVDD12_HDMI_D	-	1.2 V Analog Power
C16	AVDD12_HDMI_C	-	1.2 V Analog Power
C17	VDACR_OUT	O	DAC output
C18	VDACG_OUT	O	DAC output
C19	NO_USE	-	Not used
C20	HTPLG	I/O	HDMI Hot Plug
C21	NO_USE	-	Not used
C22	MIC_DET GPIO2	I/O	GPIO
C23	NO_USE	-	Not used
C24	DIR_GPO3	I/O	GPIO
C25	DIR_SCL	I/O	GPIO
D1	ING	I	Input of Main Beam Signal (G)
D2	INH	I	Input of Main Beam Signal (H)
D3	NO_USE	-	Not used
D4	NO_USE	-	Not used
D5	NO_USE	-	Not used
D6	NO_USE	-	Not used
D7	NO USE	-	Not used
D8	NO_USE	-	Not used
D9	NO_USE	-	Not used
D10	AOSDATA0	I/O	Audio output serial data 0
D11	NO_USE	-	Not used
D12	AVDD33_DAC	-	3.3 V Analog Power
D13	NO_USE	-	Not used
D14	AVDD33_HDMI	-	3.3 V Analog Power
D15	NO_USE	-	Not used
D16	AVDD33_VDAC_R	-	3.3 V Analog Power
D17	NO_USE	-	Not used
D18	NO_USE	-	Not used
D19	NO_USE	-	Not used
D20	START_BIT GPIO0	I/O	GPIO
D21	DVCC33_IO_STB	-	3.3 V digital IO power for Stand-By Module
D22	IF_SDI LCDRD	I/O	GPIO
D23	NO_USE	-	Not used
D24	WIFI_PCONT	I/O	GPIO
D25	TDI	I/O	JTAG ICE data in

Pin No.	Pin Name	I/O	Description
E1	INF	I	Input of Main Beam Signal (F)
E2	INE	I	Input of Main Beam Signal (E)
E3	AVDD33_1	-	3.3 V Power Pin
E4	HAVC	O	Decoupling Pin for Reference Voltage of Main and Sub Beams
E5	VDAC0	O	Output of General DAC
E6	RSTI	I	Front-End Power on reset
E7	NO_USE	-	Not used
E8	NO_USE	-	Not used
E9	AVSS33_PLL	-	PLL /BG analog ground
E10	NO_USE	-	Not used
E11	AVSS33_DAC	-	Analog Ground
E12	NO_USE	-	Not used
E13	NO_USE	-	Not used
E14	AVSS33_HDMI	-	3.3 V Analog Power
E15	NO_USE	-	Not used
E16	AVDD33_VDAC	-	3.3 V Analog Power
E17	NO_USE	-	Not used
E18	OCPI GPIO1	I/O	GPIO
E19	NO_USE	-	Not used
E20	UATXD	I/O	Reserved for debug-only Uart pins. (1) 1st RS232 TX (2) T8032 RS232 TX
E21	NO_USE	-	Not used
E22	DVCC33_IO_STB	-	3.3 V digital IO power for Stand-By Module
E23	SPDATA	I/O	SPDIF input data
E24	SYS_REQ	I/O	GPIO
E25	TCK	I/O	JTAG ICE clock
F1	FEMPXOUT3	O	Multiplexer Output 3 for Signal Monitoring. The pin is not allowed to pull-up in circuit layout. Alternative function: Internal monitored signal output / General output.
F2	FEMPXOUT2	O	Multiplexer Output 2 for Signal Monitoring. The pin is not allowed to pull-up in circuit layout. Alternative function: Internal monitored signal output / General output.
F3	NO_USE	-	Not used
F4	FEMPXOUT1	O	Multiplexer Output 1 for Signal Monitoring. The pin is not allowed to pull-up in circuit layout. Alternative function: Internal monitored signal output / General output.
F5	AUX1	I/O	Auxiliary Input Alternative function: Signal Monitoring
F6	AGND33_1	-	Analog Ground
F7	AGND33_3	-	Analog Ground
F8	NO_USE	-	Not used
F9	AVDD33_COM	-	PLL / BG 3.3 V analog power
F10	NO_USE	-	Not used
F11	AVSS12_REC	-	ADC analog ground
F12	NO_USE	-	Not used
F13	AVSS33_LD	-	Line driver analog ground
F14	AVSS33_VDAC_R	-	Analog Ground
F15	NO_USE	-	Not used
F16	NO_USE	-	Not used
F17	AVDD33_LD0	-	3.3 V power for standby
F18	NO_USE	-	Not used
F19	UARXD	I/O	Reserved for debug-only Uart pins. (1) 1st RS232 RX (2) T8032 RS232 RX
F20	NO_USE	-	Not used
F21	DVCC33_IO	-	3.3 V digital IO power
F22	NC (PD)	-	Not used
F23	NO_USE	-	Not used

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Pin No.	Pin Name	I/O	Description
F24	TMS	I/O	GPIO
F25	NC (PD)	-	Not used
G1	RFIP	I	Differential Input of AC Coupling RF SUM Signal (Positive)
G2	RFIN	I	Differential Input of AC Coupling RF SUM Signal (Negative)
G3	NO_USE	-	Not used
G4	NO_USE	-	Not used
G5	NO_USE	-	Not used
G6	NO_USE	-	Not used
G7	NO_USE	-	Not used
G8	NO_USE	-	Not used
G9	AVDD33_LD	-	Line driver 3.3 V analog power
G10	NO_USE	-	Not used
G11	AVDD12_REC	-	ADC analog power
G12	NO_USE	-	Not used
G13	NO_USE	-	Not used
G14	AVSS33_VDAC	-	Analog Ground
G15	NO_USE	-	Not used
G16	AVSS33_LDO	-	Analog ground for standby
G17	NO_USE	-	Not used
G18	NO_USE	-	Not used
G19	NO_USE	-	Not used
G20	DVCC33_IO	-	3.3 V digital IO power
G21	XIF_CS	I/O	GPIO
G22	NO_USE	-	Not used
G23	TDO	I/O	JTAG ICE data out
G24	DIR_SDA	I/O	GPIO
G25	DIR_ERROR	I/O	GPIO
H1	RFIP2	I	Differential Input of AC Coupling RF SUM Signal (Positive)
H2	RFIN2	I	Differential Input of AC Coupling RF SUM Signal (Negative)
H3	V14	O	Output of Voltage Reference (1.4 V)
H4	FOO	O	Focus servo output. PDM output of focus servo compensator.
H5	AGND12_1	-	Analog Ground
H6	NO_USE	-	Not used
H7	AGND12_2	-	Analog Ground
H8	NO_USE	-	Not used
H9	NO_USE	-	Not used
H10	NO_USE	-	Not used
H11	NO_USE	-	Not used
H12	NO_USE	-	Not used
H13	NO_USE	-	Not used
H14	DGND12_K	-	Digital ground
H15	NO_USE	-	Not used
H16	AVDD12_LDO	-	1.2 V standby power
H17	NO_USE	-	Not used
H18	NO_USE	-	Not used
H19	NO_USE	-	Not used
H20	NO_USE	-	Not used
H21	DGND12_K	-	Digital ground
H22	XDIR_INT	I/O	GPIO
H23	NO_USE	-	Not used
H24	RCLK0	O	Memory clock 0 positive
H25	RCLK0_	O	Memory clock 0 negative
J1	NO_USE	-	Not used
J2	TRO	O	Tracking servo output. PDM output of tracking servo compensator.
J3	TLO	O	Tilt servo output
J4	AVDD12_2	-	1.2 V Power Pin

Pin No.	Pin Name	I/O	Description
J5	NO_USE	-	Not used
J6	AVDD33_3	-	3.3 V Power Pin
J7	NO_USE	-	Not used
J8	NO_USE	-	Not used
J9	DVCC33_IO	-	3.3 V digital IO power
J10	NO_USE	-	Not used
J11	DVCC33_IO	-	3.3 V digital IO power
J12	NO_USE	-	Not used
J13	NO_USE	-	Not used
J14	NO_USE	-	Not used
J15	NO_USE	-	Not used
J16	NO_USE	-	Not used
J17	NO_USE	-	Not used
J18	NO_USE	-	Not used
J19	NO_USE	-	Not used
J20	DDRVCCIO	-	1.5 V digital IO power
J21	NO_USE	-	Not used
J22	DGND12_K	-	Digital ground
J23	RDQ5	I/O	Memory data bit 5
J24	RDQ7	I/O	Memory data bit 7
J25	NO_USE	-	Not used
K1	NO_USE	-	Not used
K2	NO_USE	-	Not used
K3	FEOSCEN	I/O	High frequency modulation enable signal output, or LDD serial interface CLK or I2C SCL. The pin is spike-free at power-on stage.
K4	NO_USE	-	Not used
K5	AVDD12_1	-	1.2 V Power Pin
K6	NO_USE	-	Not used
K7	AGND33_2	-	Analog Ground
K8	DVCC33_IO	-	3.3 V digital IO power
K9	DVCC12_K	-	1.2V digital power
K10	NO_USE	-	Not used
K11	DVCC12_K	-	1.2V digital power
K12	DVCC12_K	-	1.2V digital power
K13	NO_USE	-	Not used
K14	NO_USE	-	Not used
K15	DVCC12_K	-	1.2V digital power
K16	NO_USE	-	Not used
K17	DVCC12_K	-	1.2V digital power
K18	DVCC12_K	-	1.2V digital power
K19	DDRVCCIO	-	1.5 V digital IO power
K20	NO_USE	-	Not used
K21	DGND12_K	-	Digital ground
K22	RDQ4	I/O	Memory data bit 4
K23	RDQ6	I/O	Memory data bit 6
K24	NO_USE	-	Not used
K25	NO_USE	-	Not used
L1	FEAINSW3	O	Read gain switch 3.
L2	FEAINSW1	O	Read gain switch 1.
L3	FECMOD	I/O	High frequency modulation mode selection signal output, or LDD serial interface command enable. The pin is spike-free at power-on stage.
L4	FECFREQ	I/O	Frequency selection signal output, or LDD serial interface data or I2C SDA. The pin is spike-free at power-on stage.
L5	DGND12_K	-	Digital ground
L6	DGND12_K	-	Digital ground

Pin No.	Pin Name	I/O	Description
L7	DGND12_K	-	Digital ground
L8	DVCC3_IO	-	3.3 V digital IO power
L9	DVCC12_K	-	1.2V digital power
L10	DGND12_K	-	Digital ground
L11	DGND12_K	-	Digital ground
L12	DGND12_K	-	Digital ground
L13	DGND12_K	-	Digital ground
L14	DGND12_K	-	Digital ground
L15	DGND12_K	-	Digital ground
L16	DGND12_K	-	Digital ground
L17	DVCC12_K	-	1.2V digital power
L18	DVCC12_K	-	1.2V digital power
L19	DDRVCCIO	-	1.5 V digital IO power
L20	DDRVCCIO	-	1.5 V digital IO power
L21	DGND12_K	-	Digital ground
L22	RDQ14	I/O	Memory data bit 14
L23	RDQ13	I/O	Memory data bit 13
L24	NO_USE	-	Not used
L25	NO_USE	-	Not used
M1	NO_USE	-	Not used
M2	FEGAINSW2	O	Read gain switch 2.
M3	FEFMO4	I/O	Feed motor 4 control. DAC output. Alternative function: Auxiliary servo input
M4	NO_USE	-	Not used
M5	NO_USE	-	Not used
M6	NO_USE	-	Not used
M7	NO_USE	-	Not used
M8	NO_USE	-	Not used
M9	NO_USE	-	Not used
M10	DGND12_K	-	Digital ground
M11	NO_USE	-	Not used
M12	DGND12_K	-	Digital ground
M13	NO_USE	-	Not used
M14	DGND12_K	-	Digital ground
M15	NO_USE	-	Not used
M16	DGND12_K	-	Digital ground
M17	NO_USE	-	Not used
M18	NO_USE	-	Not used
M19	NO_USE	-	Not used
M20	NO_USE	-	Not used
M21	NO_USE		

Pin No.	Pin Name	I/O	Description
N9	NO_USE	-	Not used
N10	DGND12_K	-	Digital ground
N11	DGND12_K	-	Digital ground
N12	NO_USE	-	Not used
N13	DGND12_K	-	Digital ground
N14	NO_USE	-	Not used
N15	DGND12_K	-	Digital ground
N16	DGND12_K	-	Digital ground
N17	DVCC12_K	-	1.2V digital power
N18	DVCC12_K	-	1.2V digital power
N19	DDRVCCIO	-	1.5 V digital IO power
N20	DDRVCCIO	-	1.5 V digital IO power
N21	NO_USE	-	Not used
N22	DGND12_K	-	Digital ground
N23	DGND12_K	-	Digital ground
N24	RDQS1_	I/O	Memory negative data strobe bit 1
N25	RDQS1	I/O	Memory positive data strobe bit 1
P1	NO_USE	-	Not used
P2	FETRYPWM	O	Tray DAC / PWM control input. Controlled by uP.
P3	FEDMO	O	Disk motor control output. DAC output.
P4	NO_USE	-	Not used
P5	NO_USE	-	Not used
P6	NO_USE	-	Not used
P7	NO_USE	-	Not used
P8	NO_USE	-	Not used
P9	DVCC12_K	-	1.2V digital power
P10	DGND12_K	-	Digital ground
P11	NO_USE	-	Not used
P12	DGND12_K	-	Digital ground
P13	NO_USE	-	Not used
P14	DGND12_K	-	Digital ground
P15	NO_USE	-	Not used
P16	DGND12_K	-	Digital ground
P17	NO_USE	-	Not used
P18	DVCC12_K	-	1.2V digital power
P19	DDRVCCIO	-	1.5 V digital IO power
P20	NO_USE	-	Not used
P21	NO_USE	-	Not used
P22	NO_USE	-	Not used
P2			

Pin No.	Pin Name	I/O	Description
R11	DGND12_K	-	Digital ground
R12	DGND12_K	-	Digital ground
R13	DGND12_K	-	Digital ground
R14	DGND12_K	-	Digital ground
R15	DGND12_K	-	Digital ground
R16	NO_USE	-	Not used
R17	DVCC12_K	-	1.2V digital power
R18	NO_USE	-	Not used
R19	NO_USE	-	Not used
R20	DDRVCCIO	-	1.5 V digital IO power
R21	NO_USE	-	Not used
R22	RDQM1_B	O	Memory data mask bit 1
R23	RDQM0	O	Memory data mask bit 0
R24	RDQ10	I/O	Memory data bit 10
R25	NO_USE	-	Not used
T1	FEEJECT_	I/O	Eject / stop key input, active low. The pin is spike-free at power-on stage. Alternative function: General IO
T2	FE_TRAYIN_	I/O	Tray_is_in Input, A Logical Low Indicates the Tray is IN. Feedback Flag is from Tray Connector. The pin is spike-free at power-on stage. Alternative function: General IO
T3	FEGIO10	I/O	PC RS232 serial receive data. The pin is spike-free at power-on stage.
T4	NO_USE	-	Not used
T5	NO_USE	-	Not used
T6	NO_USE	-	Not used
T7	NO_USE	-	Not used
T8	NO_USE	-	Not used
T9	DVCC12_K	-	1.2V digital power
T10	NO_USE	-	Not used
T11	DVCC12_K	-	1.2V digital power
T12	DVCC12_K	-	1.2V digital power
T13	NO_USE	-	Not used
T14	DVCC12_K	-	1.2V digital power
T15	NO_USE	-	Not used
T16	DVCC12_K	-	1.2V digital power
T17	DVCC12_K	-	1.2V digital power
T18	DDRVCCIO	-	1.5 V digital IO power
T19	RVREFO	I	Memory VREF
T20</			

Pin No.	Pin Name	I/O	Description
U8	NO_USE	-	Not used
U9	DVCC12_K	-	1.2V digital power
U10	NO_USE	-	Not used
U11	DVCC12_K	-	1.2V digital power
U12	DVCC12_K	-	1.2V digital power
U13	NO_USE	-	Not used
U14	DVCC12_K	-	1.2V digital power
U15	DVCC12_K	-	1.2V digital power
U16	DVCC12_K	-	1.2V digital power
U17	NO_USE	-	Not used
U18	NO_USE	-	Not used
U19	NO_USE	-	Not used
U20	NO_USE	-	Not used
U21	RA1	O	Memory address bit 1
U22	DGND12_K	-	Digital ground
U23	RDQ3	I/O	Memory data bit 3
U24	NO_USE	-	Not used
U25	RDQ2	I/O	Memory data bit 2
V1	USB_DP_P2	I/O	USB port2 differential serial data bus (plus)
V2	USB_DM_P2	I/O	USB port2 differential serial data bus (minus)
V3	NO_USE	-	Not used
V4	NO_USE	-	Not used
V5	DGND12_K	-	Digital ground
V6	NO_USE	-	Not used
V7	NO_USE	-	Not used
V8	NO_USE	-	Not used
V9	NO_USE	-	Not used
V10	NO_USE	-	Not used
V11	NO_USE	-	Not used
V1			

Pin No.	Pin Name	I/O	Description
W13	NO_USE	-	Not used
W14	NO_USE	-	Not used
W15	DDRVCCIO	-	1.5 V digital IO power
W16	DDRVCCIO	-	1.5 V digital IO power
W17	NO_USE	-	Not used
W18	DDRVCCIO	-	1.5 V digital IO power
W19	NO_USE	-	Not used
W20	RA8	O	Memory address bit 8
W21	RA4	O	Memory address bit 4
W22	RA6	O	Memory address bit 6
W23	NO_USE	-	Not used
W24	RODT_B	O	Memory on die termination enable
W25	RRAS_B	O	Memory row address strobe
Y1	USB_DP_P1	I/O	USB port1 differential serial data bus (plus)
Y2	USB_DM_P1	I/O	USB port1 differential serial data bus (minus)
Y3	NO_USE	-	Not used
Y4	NO_USE	-	Not used
Y5	NO_USE	-	Not used
Y6	USB_PCONT GPIO7	I/O	GPIO
Y7	NO_USE	-	Not used
Y8	RA12_B	O	Memory address bit 12
Y9	NO_USE	-	Not used
Y10	RA1_B	O	Memory address bit 1
Y11	NO_USE	-	Not used
Y12	RCAS_B	O	Memory column address strobe
Y13	DGND12_K	-	Digital ground
Y14	DDRVCC		

Pin No.	Pin Name	I/O	Description
AA16	NO_USE	-	Not used
AA17	NO_USE	-	Not used
AA18	NO_USE	-	Not used
AA19	DGND12_K	-	Digital ground
AA20	RA12	O	Memory address bit 12
AA21	AVDD33_MEMPLL	-	3.3 V Analog Power for MEMPLL
AA22	NO_USE	-	Not Used
AA23	RA3	O	Memory address bit 3
AA24	RA5	O	Memory address bit 5
AA25	RA0	O	Memory address bit 0
AB1	JIGMODE1 GPIO5	I/O	GPIO
AB2	UPG_STATUS GPIO6	I/O	GPIO
AB3	NFD6	I/O	NAND Flash Data input/output bit6
AB4	DVCC33_IO	-	3.3 V digital IO power
AB5	NO_USE	-	Not used
AB6	RA10_B	O	Memory address bit 10
AB7	NO_USE	-	Not used
AB8	RBA0_B	O	Memory bank address bit 0
AB9	NO_USE	-	Not used
AB10	RA3_B	O	Memory address bit 3
AB11	RA13_B	O	Memory address bit 13
AB12	NO_USE	-	Not used
AB13	DGND12_K	-	Digital ground
AB14	RDQ11_B	I/O	Memory data bit 11
AB15	RDQ10_B	I/O	Memory data bit

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Pin No.	Pin Name	I/O	Description
AC20	RDQ7_B	I/O	Memory data bit 7
AC21	RDQ5_B	I/O	Memory data bit 5
AC22	RCAS_	O	Memory column address strobe
AC23	NO_USE	-	Not used
AC24	RA9	O	Memory address bit 9
AC25	RRESET	O	Memory reset
AD1	NFD5	I/O	NAND Flash Data input/output bit5
AD2	NFD2	I/O	NAND Flash Data input/output bit2
AD3	NFRBN	I/O	NAND Flash ready / busy
AD4	NFCEN	I/O	NAND Flash chip enable
AD5	NFCLE	I/O	NAND Flash command latch enable
AD6	NFWEN	I/O	NAND Flash write enable
AD7	RA9_B	O	Memory address bit 9
AD8	RA7_B	O	Memory address bit 7
AD9	NO_USE	-	Not used
AD10	RODT_B	O	Memory on die termination enable
AD11	NO_USE	-	Not used
AD12	NO_USE	-	Not used
AD13	RDQ2_B	I/O	Memory data bit 2
AD14	NO_USE	-	Not used
AD15	NO_USE	-	Not used
AD16	RDQS0_B	I/O	Memory positive data strobe bit 0
AD17	RDQS1_B	I/O	Memory positive data strobe bit 1
AD18	RDQ15_B	I/O	Memory data bit 15
AD19	NO_USE		

MB148 BOARD (1/9) IC106, IC206 K4B2G1646C-HCH9 (SD-RAM)

Pin No.	Pin Name	I/O	Description
A1	VDDQ	-	DQ Power Supply: 1.5V +/-0.075V
A2	DQU5	I/O	Data Input/output: Bi-directional data bus.
A3	DQU7	I/O	Data Input/output: Bi-directional data bus.
A4	NO_USE	-	Not used
A5	NO_USE	-	Not used
A6	NO_USE	-	Not used
A7	DQU4	I/O	Data Input/output: Bi-directional data bus.
A8	VDDQ	-	DQ Power Supply: 1.5V +/-0.075V
A9	VSS	-	Ground
B1	VSSQ	-	DQ Ground
B2	VDD	-	Power Supply: 1.5V +/-0.075
B3	VSS	-	Ground
B4	NO_USE	-	Not used
B5	NO_USE	-	Not used
B6	NO_USE	-	Not used
B7	$\overline{\text{DQS}}_{\text{U}}$	I/O	Data Strobe: Output with read data, input with write data. Edge-aligned with read data, centered in write data. For the x16, $\overline{\text{DQSL}}$: corresponds to the data on DQL0-DQL7 ; DQSU corresponds to the data on DQU0-DQU7 . The data strobe DQS , DQSL and DQSU are paired with differential signals DQS , DQSL and DQSU , respectively, to provide differential pair signaling to the system during reads and writes. DDR3 SDRAM supports differential data strobe only and does not support single-ended.
B8	DQU6	I/O	Data Input/output: Bi-directional data bus.

Pin No.	Pin Name	I/O	Description
E9	VDDQ	-	DQ Power Supply: 1.5V +/-0.075V
F1	VDDQ	-	DQ Power Supply: 1.5V +/-0.075V
F2	DQL2	I/O	Data Input/output: Bi-directional data bus.
F3	DQSL	I/O	Data Strobe: Output with read data, input with write data. Edge-aligned with read data, centered in write data. For the x16, DQSL: corresponds to the data on DQL0-DQL7; DQSU corresponds to the data on DQU0-DQU7. The data strobe DQS, DQSL and DQSU are paired with differential signals DQS, DQSL and DQSU, respectively, to provide differential pair signaling to the system during reads and writes. DDR3 SDRAM supports differential data strobe only and does not support single-ended.
F4	NO_USE	-	Not used
F5	NO_USE	-	Not used
F6	NO_USE	-	Not used
F7	DQL1	I/O	Data Input/output: Bi-directional data bus.
F8	DQL3	I/O	Data Input/output: Bi-directional data bus.
F9	VSSQ	-	DQ Ground
G1	VSSQ	-	DQ Ground
G2	DQL6	I/O	Data Input/output: Bi-directional data bus.
G3	$\overline{\text{DQSL}}$	I/O	Data Strobe: Output with read data, input with write data. Edge-aligned with read data, centered in write data. For the x16, DQSL: corresponds to the data on DQL0-DQL7; DQSU corresponds to the data on DQU0-DQU7. The data strobe DQS, DQSL and DQSU are paired with differential signals DQS, DQSL and DQSU, respectively, to provide differential pair signaling to the system during reads and writes. DDR3 SDRAM supports differential data strobe only and does not support single-ended.
G4	NO_USE	-	Not used
G5	NO_USE	-	Not used
G6	NO_USE	-	Not used
G7	VDD	-	Power Supply: 1.5V +/-0.075
G8	VSS	-	Ground
G9	VSSQ	-</	

Pin No.	Pin Name	I/O	Description
K9	CKE	I	Clock Enable: CKE HIGH activates, and CKE LOW deactivates, internal clock signal and device input buffers and output drivers. Talking CKE LOW provides Precharge Power-Down and Self Refresh operation (all banks idle), or Active Power-Down (Row Active in any bank). CKE is asynchronous for self refresh exit. After V _{REFCA} has become stable during the power on and initialization sequence, it must be maintained during all operations (including Self-Refresh). CKE must be maintained high throughout read and write accesses. Input buffers, excluding CK, CK, ODT and CKE are disabled during power-down. Input buffers, excluding CKE, are disabled during Self-Refresh.
L1	NC	-	No Connect: No internal electrical connection is present.
L2	$\overline{CS}$	I	Chip Select: All commands are masked when $\overline{CS}$ is registered HIGH. $\overline{CS}$ provides for external Rank selection on system with multiple Ranks. $\overline{CS}$ is considered part of the command code.
L3	$\overline{WE}$	I	Command Input: $\overline{WE}$ (along with $\overline{CS}$) define the command being entered.
L4	NO_USE	-	Not used
L5	NO_USE	-	Not used
L6	NO_USE	-	Not used
L7	A10	I	Address inputs: Provided the row address for active commands and the column address for Read/Write commands to select one location out of the memory array in the respective bank. The address inputs also provide the op-code during Mode Register Set commands. Autoprecharge: A10 is sampled during Read/Write commands to determine whether Auto-precharge should be performed to the accessed bank after the Read/Write operation. (HIGH: Autoprecharge; LOW: No Autoprecharge) A10 is sampled during a Precharge command to determine the Precharge applies to one bank (A10 LOW) or all banks (A10 HIGH). If only one bank is to be precharged, the bank is selected by bank addresses.
L8	ZQ	-	Reference Pin for ZQ calibration
L9	NC	-	No Connect: No internal electrical connection is present.
M1	VSS	-	Ground
M2	BA0	I	Bank Address Inputs: BA0 define to which bank an Active, Read, Write or Precharge command is being applied. Bank address

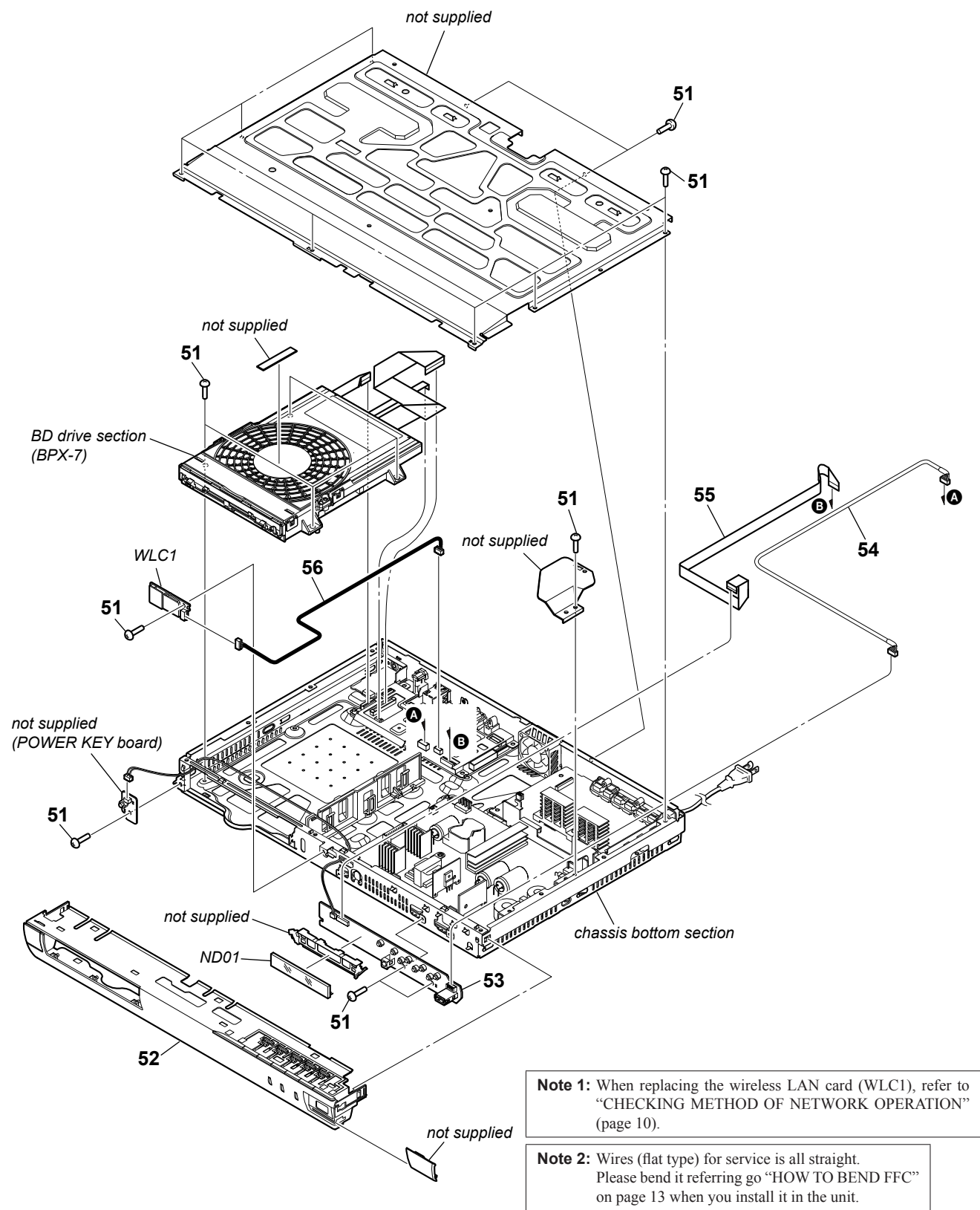
Pin No.	Pin Name	I/O	Description
P4	NO_USE	-	Not used
P5	NO_USE	-	Not used
P6	NO_USE	-	Not used
P7	A1	I	Address inputs: Provided the row address for active commands and the column address for Read/Write commands to select one location out of the memory array in the respective bank. The address inputs also provide the op-code during Mode Register Set commands.
P8	A4	I	Address inputs: Provided the row address for active commands and the column address for Read/Write commands to select one location out of the memory array in the respective bank. The address inputs also provide the op-code during Mode Register Set commands.
P9	VSS	-	Ground
R1	VDD	-	Power Supply: 1.5V +/-0.075
R2	A7	I	Address inputs: Provided the row address for active commands and the column address for Read/Write commands to select one location out of the memory array in the respective bank. The address inputs also provide the op-code during Mode Register Set commands.
R3	A9	I	Address inputs: Provided the row address for active commands and the column address for Read/Write commands to select one location out of the memory array in the respective bank. The address inputs also provide the op-code during Mode Register Set commands.
R4	NO_USE	-	Not used
R5	NO_USE	-	Not used
R6	NO_USE	-	Not used
R7	A11	I	Address inputs: Provided the row address for active commands and the column address for Read/Write commands to select one location out of the memory array in the respective bank. The address inputs also provide the op-code during Mode Register Set commands.
R8	A6	I	Address inputs: Provided the row address for active commands and the column address for Read/Write commands to select one location out of the memory array in the respective bank. The address inputs also provide the op-code during Mode Register Set commands.
R9	VDD	-	Power Supply: 1.5V +/-0.075
T1	VSS	-	Ground
T2	$\overline{\text{RESET}}$	I	Active Low Asynchronous Reset: Reset is active when $\overline{\text{RESET}}$ is LOW, and inactive when $\overline{\text{RESET}}$ is HIGH. $\overline{\text{RESET}}$ must be HIGH during normal operation. $\overline{\text{RESET}}$ is CMOS rail to rail signal with DC high and low at 80% and 20% of V_{DD} , example, 1.20

MB148 BOARD (9/9) IC6301 R5F3650KCDFB (SYSTEM CONTROLLER)

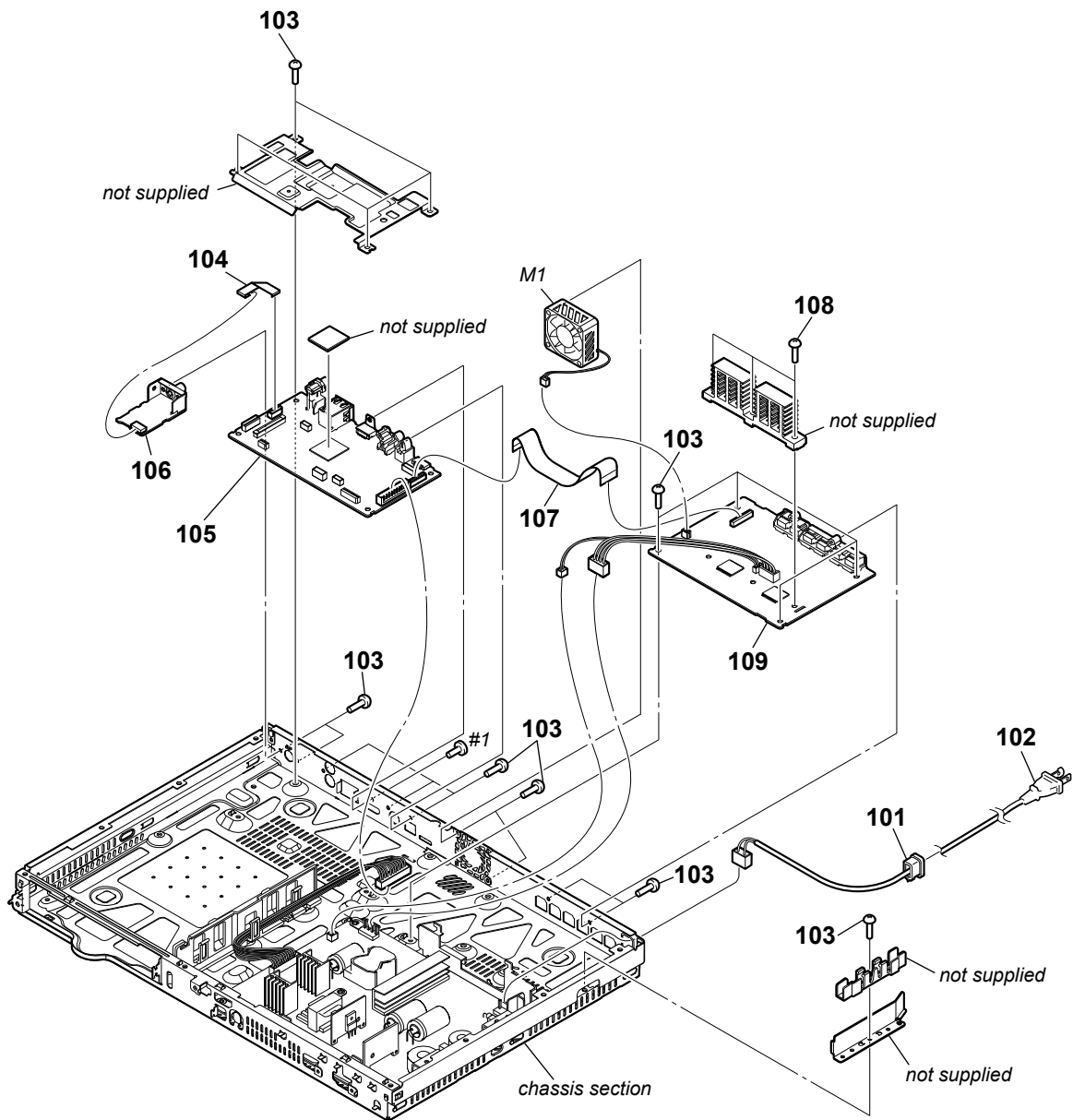
Pin No.	Pin Name	I/O	Description
1	SIRCS_IN	I	SIRCS signal input from the remote control receiver
2	FAN CONT2	O	Fan motor speed control signal output terminal control by DA converter Port output higher, speed slower
3	FL_D_OUT	O	Serial data output to the fluorescent indicator tube driver
4	NO_USE	-	No used
5	FL_CLK	O	Serial data transfer clock signal output to the fluorescent indicator tube driver
6	BYTE	I	External data bus width selection signal input terminal
7	CNVss	I	Processor mode selection signal input terminal
8	NO_USE	-	No used
9	NO_USE	-	No used
10	RESET	I	System reset signal input from the reset signal generator and reset switch "L": reset For several hundreds msec. After the power supply rises, "L" is input, then it change to "H"
11	Xout	O	System clock output terminal (8MHz)
12	Vss	-	Ground terminal
13	Xin	I	System clock input terminal (5MHz)
14	Vcc1	-	Power supply terminal (+3.3V)
15	CEC_TX_RX	I/O	CEC serial data input/output with the HDMI connector
16	NO_USE	-	No used
17	KEY_INT	I	Key wake-up signal input terminal
18	AC_CUT	I	AC cut detection signal input terminal "L": AC cut
19	BD_IF_START	O	Ready signal output to the BD decoder "H": ready
20	NO_USE	-	No used
21	NO_USE	-	No used

Pin No.	Pin Name	I/O	Description
54	NO_USE	-	No used
55	NO_USE	-	No used
56	KARAOKE_MODE	I	Karaoke mode information input terminal
57	MIC_CLK	I/O	Clock signal output terminal
58	MIC_DATA	I/O	Serial data output terminal
59	MIC-DET_OUT	O	Microphone detection signal output terminal
60	Vcc	-	Power supply terminal (+3.3V)
61	NJU72341_POWER	O	Power control for MIC IC
62	Vss	-	Ground terminal
63	PLUG DET / PLUG1 DET / PLUG2 DET	I	Microphone A or B plug insert detection signal input terminal Detection by AD
64	NO_USE	-	No used
65	DRIVE_SD / POW- ER_DET	I	Shut down signal input from the power amplifier "L": shut down
66	NO_USE	-	No used
67	STA516_TH_WARN	I	Thermal warning for temperature of the IC STA516 more than 130 °C
68	FL_CS	O	Chip select signal output to the fluorescent indicator tube driver
69	ASEL0	O	Audio selection signal output terminal "L": external audio input, "H": FM
70	ASEL1	O	Audio selection signal output terminal
71	NO_USE	-	No used
72	NO_USE	-	No used
73	ST_RDS_INT	I	RDS interrupt signal input from the FM receiver
74	NO_USE	-	No used
75	BD_SDI (IF_SDO)	O	

6-2. FRONT PANEL SECTION



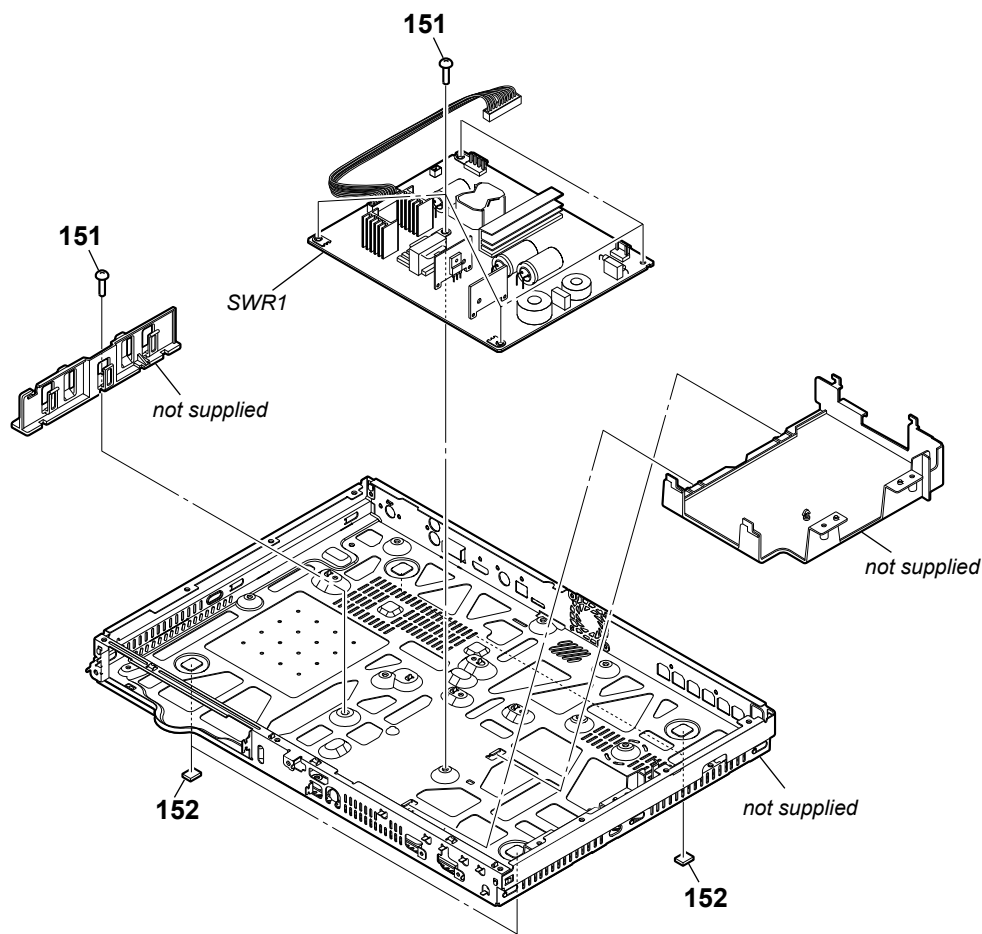
6-3. CHASSIS BOTTOM SECTION



Note: Wires (flat type) for service is all straight.
Please bend it referring go “HOW TO BEND FFC”
on page 13 when you install it in the unit.

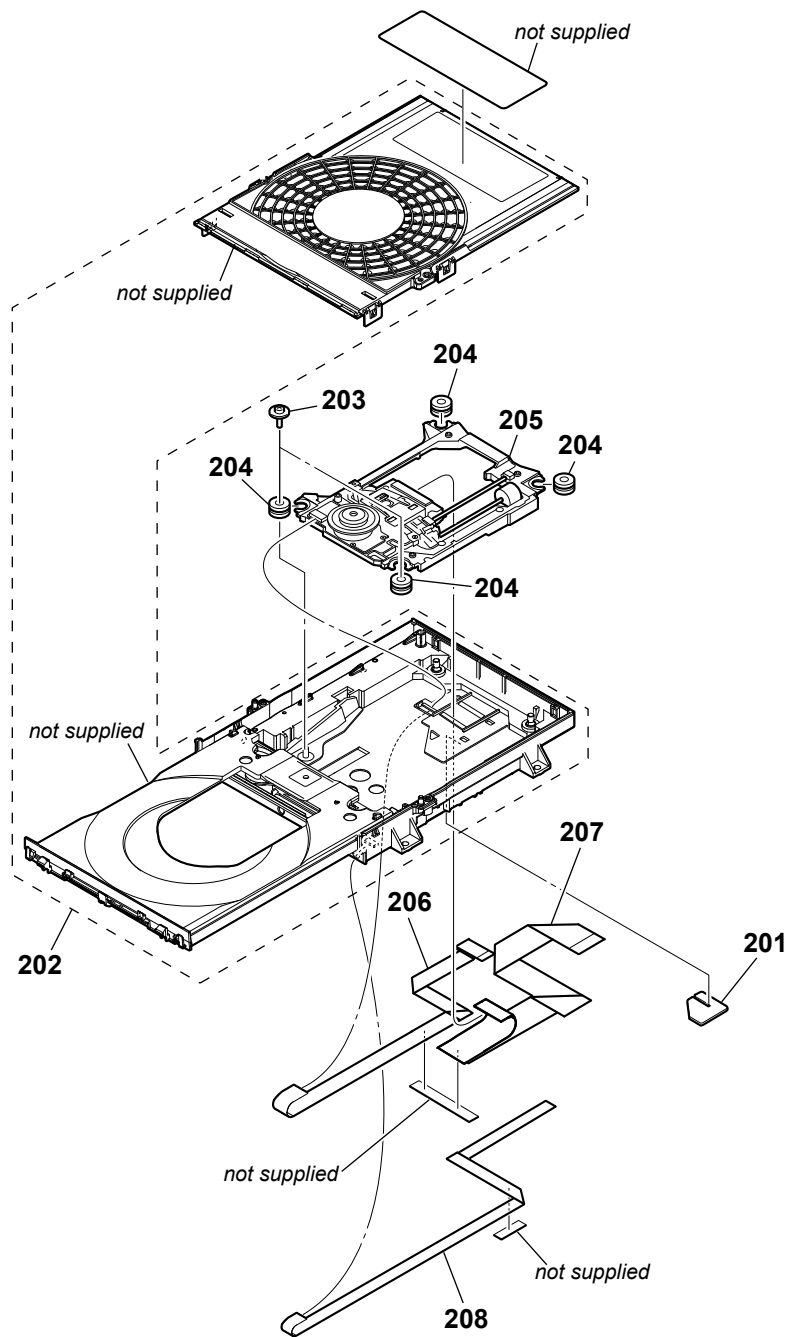
Ref. No.	Part No.	Description	Remark	Ref. No.	Part No.	Description	Remark
△ 101	4-966-267-12						

6-4. CHASSIS SECTION



Ref. No.	Part No.	Description	Remark
151	3-077-331-71	+BV3 (3-CR)	
152	4-295-590-01	FOOT	
△ SWR1			

6-5. BD DRIVE SECTION (BPX-7)



SECTION 7

ELECTRICAL PARTS LIST

AMP

Note:

- Due to standardization, replacements in the parts list may be different from the parts specified in the diagrams or the components used on the set.
- -XX and -X mean standardized parts, so they may have some difference from the original one.
- Items marked "*" are not stocked since they are seldom required for routine service. Some delay should be anticipated when ordering these items.
- RESISTORS
All resistors are in ohms.
METAL: Metal-film resistor.
METAL OXIDE: Metal oxide-film resistor.
F: nonflammable
- CAPACITORS
uF: μF
- COIL

Ref. No.	Part No.	Description	Remark	Ref. No.	Part No.	Description	Remark
C3339	1-114-868-11	CERAMIC CHIP 0.1uF 10%	50V	D3303	6-503-733-01	DI 1SMB51AT3G	
C3340	1-118-066-11	CERAMIC CHIP 0.1uF 10%	100V	D3304	6-503-733-01	DI 1SMB51AT3G	
C3341	1-118-066-11	CERAM					

HBD-E385/E390/T39

AMP MB148

Ref. No.	Part No.	Description	Remark
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