

SOJのRAS,CAS,OEは間違っているの
後で修正すること(回路図のみ)。

FOR ON BOARD 64M DRAM DETECT.
R100 PULL UP '64' LINE.HI->64DRAM LO->16M D-RAM
R101 JUMPER ENABLE CPU CONTROLLED 64 LINE.
R102 JUMPER FOR 16M CHIP.THIS CASE IS NOT USE CPU
CONTROLLED '64'LINE. SO USE THIS JUMPER, YOU MUST
REMOVE R101!!

CASE FOR 3.3V DRAM,PLACE
IC68, 61, 62, 63
CASE FOR 5V DRAM,PLACE
IB68, 61, 62, 63

まだ回路図のメモが残りません。

REVISION				1st EDITION				CIRCUIT NO. L6044801			
APPROVED	CHECKED	DESIGNED	DATE	APPROVED	CHECKED	DESIGNED	DATE	Design File	Design Name	Design Date	Design Rev
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